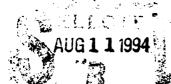
# NAVAL POSTGRADUATE SCHOOL Monterey, California

AD-A283 124



94-25251



## **THESIS**

Modeling and Simulation of a Fiber Distributed Data Interface Local Area Network (FDDI LAN) Using OPNET<sup>©</sup> for Interfacing Through the Common Data Link (CDL)

by

Ernest E. Nix, Jr.

June 1994

Thesis Advisor:

Shridhar Shukla

Approved for public release; distribution is unlimited.

94 8 10 0 14

REPORT D	Form Approved OMB No. 0704-0188								
Public reporting burden for this collection of sources, gathering and maintaining the data noo aspect of this collection of information, include Reports, 1215 Jefferson Davis Highway, Suite l Washington DC 20503.	ded, and completeding suggestion	ting and reviewing the collect as for reducing this burden, to	ction of information washington	mation. Send cor headquarters Se	nments regardir rvices, Director	ng this burden estimate or any other rate for Information Operations and			
AGENCY USE ONLY (Leave	TYPE AND DATES COVERED  Master's Thesis								
4. TITLE AND SUBTITLE MODELING AND SIMULATION C AREA NETWORK (FDDI LAN) US COMMON DATA LINK (CDL)	5. FUNDING NUMBERS								
6. AUTHOR(S) Ernest E. Nix, Jr.					I				
7. PERFORMING ORGANIZATION Naval Postgraduate School Monterey CA 93943-5000	8. PERFORMING ORGANIZATION REPORT NUMBER:								
9 SPONSORING/MONITORING A		10. SPONSORING / MONITORING AGENCY REPORT NUMBER:							
11 SUPPLEMENTARY NOTES The views expressed in this thesis are U.S. Government.	those of the	author and do not reflect	the official	policy or posi	tion of the D	epartment of Defense or the			
12a DISTRIBUTION/AVAILABILI Approved for public release: distr					12b. DISTRIBUTION CODE				
13. ABSTRACT (maximum 200 wor The Optimized Network Engineering involves the modification of OPNET as as an aid in the development of recomm (DSPO) Common Data Link (CDL) changes made to the original model to e model's performance and usefulness a	Tool (OPN) Fiber Distribut endations for t project. This enhance its pe	ted Data Interface Local / the characteristics and met work includes a step-by- riformance and data disp	Area Netwo nes to be evistep guide lav characte	ork (FDDI LA) ventually inclu for FDDI simi	N) model in or ided in the De ilation in OPI	order to enhance its usefulness efense Service Project Office's NET <sup>®</sup> , and a discussion of the			
14. SUBJECT TERMS FDDI, SYNCHRONOUS, LAN, S		15. NUMBER OF PAGES: 213							
				<del>_</del>		16. PRICE CODE			
17. SECURITY CLASSIFICA- TION OF REPORT Unclassified  18. SECURITY CLASSIFI- CATION OF THIS PAGE Unclassified  19. SECURITY CLASSIFICA- TION OF ABSTRACT Unclassified  UL  20. LIMITATION OF ABSTRACT UL  UL  UL									

NSN 7540-01-280-5500

Standard Form 298 (Rev. 2-89) Prescribed by ANSI Std. 239-18

#### Approved for public release; distribution is unlimited.

Modeling and Simulation of a Fiber Distributed Data Interface Local Area Network (FDDI LAN) Using OPNET<sup>®</sup> for Interfacing Through the Common Data Link (CDL)

by

Ernest E. Nix, Jr.
Lieutenant, United States Navy
B.S. Ed. University of South Carolina, 1985

Submitted in partial fulfillment of the requirements for the degree of

#### MASTER OF SCIENCE IN ELECTRICAL ENGINEERING

from the

NAVAL POSTGRADUATE SCHOOL Monterey, California June 1994

Author:	Eurest & My /
	Ernest E. Nix, Jr.
Approved by:	Shridhar Shukla, Thesis Advisor
	SMM 2/
	Gilbery Lundy, Second Reader
	Michael A. Morgan, Clarman Department of Electrical and Computer Engineering

## **ACKNOWLEDGEMENTS**

The author gratefully acknowledges the efforts of those whose assistance made this thesis achievable: Dr. Shridhar Shukla, who provided guidance, support and motivation; MIL 3, Inc. Technical Support Staff who were available days, nights, weekends and holidays, and treated every question seriously, regardless of content; Mr. James "The Wizard" Scott, thesis preparer extraordinaire, who performed miracles on request, and Mrs. Hernestina Nix, who never once complained.

OPNET<sup>®</sup> is a registered trademark of MIL 3 Inc.

BONeS<sup>®</sup> is a registered trademark of Comdisco Systems, Inc.

Access	ion For	
MTIS DTIC		
Unann	pecan	
Justin	fication	
By		
	ibution/	17
Avai	lability	ଡିବର୍ଣ୍ <b>ଟର</b>
Dist	Avall and Special	-

## **ABSTRACT**

The Optimized Network Engineering Tool (OPNET<sup>®</sup>) is a commercially available communications network simulation package. This thesis involves the modification of OPNET<sup>®</sup>s Fiber Distributed Data Interface Local Area Network (FDDI LAN) model in order to enhance its usefulness as an aid in the development of recommendations for the characteristics and metrics to be eventually included in the Defense Service Project Office's (DSPO) Common Data Link (CDL) project. This work includes a step-by-step guide for FDDI simulation in OPNET<sup>®</sup>, and a discussion of the changes made to the original model to enhance its performance and data display characteristics. Simple tests are provided to verify the completed model's performance and usefulness as a working tool for further development.

## TABLE OF CONTENTS

I.	INTRO	DUC	TION					 					1
	A.	PRC	BLE	M STATE	MENT.			 					1
	B.	SCC	PE.					 					2
	C.	BEN	<b>IEFIT</b>	<b>S</b>				 					2
	D.	ORC	3ANIZ	ZATION				 					3
			-										
П.	MODI	ELIN(	G AN	FDDI LA	N IN OI	PNET	•	 					4
_	A.	OVE	RVIE	<b>W</b>				 					4
	B.	PRE	LIMI	NARY TI									5
		1.	The	oretical vs	OPNET								
		2.		I LAN E									
			<b>a</b> .	"F Max	•								
			ъ. Ъ.	"D Max									
			C.	"SA,"									
			d.	"TTRT"						-	 -		. 11
	C.	MOI		TRUCTI									
	C.	1.		I LAN.									
		2.		I Station.									
		2. 3.		esses									
		<b>J</b> .	a.	Source I									
			a. b.	Sink No									
				Medium									
		4	C.	ets			•	•					
		4.		ICI For									
			<b>a</b> . L										
		•	b.	Packet F									. 33
		<b>5</b> .		ronment I									
			<b>a</b> .	"station	_								. 34
			b.	"ring_id									
			C.	"low des									35
			d.	"high de									
			e.	"arrival							-		
			f.	"mean_r	-								
			g.	"async_1									
			h.	"sync ba									
			i.	"T_Req"									
			j.	"spawn :	station".			 					. 38
			1-	Matation	latenani								20

			1.	"prop	_delay	<b>.</b>					 						38
			m.		eration												
			n.	"durat	ion" .	<del>-</del>					 						39
			0.		ose_sir												
			p.	"upd	int"						 						39
			q.		ie"												
			r.	_	ile"												
			<b>S</b> .	"seed'	•						 						40
			t.	"debu	g"						 						40
	D.	SIM	ULAT	NOI	_												
		1.	Buile	d the LA	4N						 						41
		2.	Соп	ect "OF	BUG	2070"			. ,		 						41
		<b>3</b> .	Impl	ement "	OPBU	JG 208	31" Pa	tch			 						41
		4.	Upd	ate Env	ironme	ent File	<b>.</b>				 						41
		<b>5</b> .	Gen	erate Pr	obe Fi	le					 						42
		<b>6</b> .	Sim	lation l	Editor.						 						42
		<b>7</b> .	Start	t the Sir	nulatio	n					 						45
		<b>8</b> .	Anal	ysis .							 						45
		<b>9</b> .	Deb	ug Tool	١						 						47
Ш.				ICATIO													
	A.			ZATIO													
	B.	_		vating F													
		1. 2.		vaung r nging th													
		2. 3.		inenez uRma m													
		<b>J</b> .	2.		TK"												
			a. b.		DATA												
		4.		ification													
		4.			n Mod												
			<b>a</b> . b.		onmen		_										
			U. C.		e Mod		_										61
			d.		Modif												
			u. e.		Node N												
			f.		t Form												
	C.	DEB		ANCE													
	C.	1.		rview.													
		2.		ables .													
		2. 3.		aules . Alization													-
		3. 4		SCARD													
		<b>→.</b> 5.		ATS" S													
	D.			LINK.													
	<b>.</b>	1		ion Mod						•	 	•	•	• •	٠	•	13 74

	<b>2</b> .	Process Model Modifications
E.	MUI	.TICAST
	1.	Overview
	2.	Environment File
	<b>3</b> .	Station Model
	4.	Source Process Model
		a. Variables
		b. Initialization State
		c. "ARRIVAL" State
	<b>5</b> .	MAC Process Model
		a. Variables
		b. Encapsulation State
		c. Frame Repeat State
	6.	Limitation
	•	
IV. MOD	EL TI	<b>ESTING</b>
A.		CHRONOUS THROUGHPUT
	1.	Overview
	2.	Setup
	3.	Results
В.		LIMINARY LINKING MODEL
•	1.	Overview
	2.	Setup
	3.	RESULTS
C.	• •	CHRONOUS TIMING
<b>.</b>	1.	Overview
	2.	Setup
	3.	Results
D.		NCHRONOUS EFFICIENCY
•	1.	Overview
	2.	Results
E.		TICASTING
<b>.</b>	1.	Overview
	2.	First Test
		a. Setup
		b. Results
	<b>3</b> .	Second test
	٠.	a. Setup
		b. Results
	4.	Third Test
	\lnot.	a. Setup
		b. Results
	5	Fourth Test

	<b>a</b> .	Setup					 . 115
	b.	Results					 . 115
v. conclus	IONS .	AND RECO	MMEND	ATIONS			 . 118
		SIONS					
		ENDATIO					
APPENDIX A.	FILE	RETRIEVA	AL VIA F	<b>TP</b>			 . 121
APPENDIX B.	PACI	KET AND I	CI FRAM	E STRUC	TURES.		 . 123
A. PAC	KET	<b>FORMATS</b>					 . 123
1.	"fddi	_llc_fr"					 . 123
<b>2</b> .	"fddi	_mac_fr" .					 . 123
3.		_mac_tk" .					
B. ICI FO		TS					
1.		_mac_ind".					
2.	"fddi	_mac_req".					 . 124
APPENDIX D.		U <b>G TOO</b> L I					
APPENDIX E.	MAC	C CODE	: "fddi_m	ac_mult.pr	.c"		 . 133
APPENDIX F.	SOUP	CE "C" CC	DDE: "fdd	i_gen_mul	t.pr.c		 . 175
APPENDIX G.	SINK	"C" CODE	: "fddi_si	nk_mult.pr	.c		 . 184
APPENDIX H CAPABLI		NVIRONMI I LAN					
APPENDIX I.	CON	ENTIONS				• • • •	 . 200
APPENDIX J.	GLOS	SARY				• • • •	 . 201
LIST OF REFE	RENC	ES					 . 202
INITIAL DIST	RIRIT	TION LIST					203

### I. INTRODUCTION

## A. PROBLEM STATEMENT

The simulation model described in this thesis was developed in support of the Defense Support Project Office's (DSPO) Common Data Link (CDL) project. The Common Data Link is a full duplex, jam resistant, point-to-point microwave communication system for use in imaging and signals intelligence collection systems (DSPO, 1993, p. 1). Essentially, CDL is to provide a protocol for communication between two or more Fiber Distributed Data Interface Local Area Networks (FDDI LAN). These include an airborne LAN providing sensor information with high data transfer rates, and a ground based LAN providing command and control information.

This work is concerned primarily with the modification and testing of a commercially available communications network simulation program, MIL 3, Inc.'s Optimized Network Engineering Tool (OPNET<sup>®</sup>). This thesis represents the first portion of three relatively independent research tasks being performed as MS theses to provide evaluations of several Network Interfaces (NI) to the CDL and a multilink point-to-point protocol, in support of the CDL project.

## **B. SCOPE**

The scope of this thesis includes the following:

- Introduce the CDL concept as the context in which the FDDI simulation model is to be modified and tested.
- Provide a tutorial style introduction to the OPNET® FDDI model, designed to expand upon the tutorial provided by the manufacturer. This is directed to those who will conduct further studies in the CDL project, and also to students whose class laboratory work will include simulations in OPNET®.
- Discuss in detail the modifications made to the given model. Provide analysis of the model's actual simulation performance as a validation of the model's usefulness to the CDL work at NPS through comparisons against trials published in the research literature using other simulation tools.

## C. BENEFITS

The primary contribution of this thesis is the development of a functioning simulation model that will support the features typically required in a CDL deployment scenario. Typical data communication requirements include the following:

- a wide range of data rates,
- a wide range of error rates and types of error correction required.
- real-time requirements such as user-specified delivery delays and its variation (jitter),

- connection requirements (whether connection-oriented or connectionless,
   multicasting, broadcasting, etc.),
- retransmission requirements,
- coupling and synchronization with other data sources, and
- adjustable prioritization relative to other sources.

The second benefit is to document in detail the MIL 3, Inc.'s FDDI LAN simulation model in its operation and in its modification. The third benefit lies in the use of the developed model as an instructional tool for classroom laboratory exercises supporting the study of FDDI LAN operation.

## D. ORGANIZATION

This thesis is organized as follows. Chapter II provides a tutorial on the use of the FDDI LAN model provided with OPNET. Chapter III addresses the details of the modifications made to the given model to simulate multicasting and priority-based traffic. Where applicable, clarifications regarding the OPNET manuals are highlighted. Chapter IV presents the results of simulation tests intended to verify the validity of the modified model. The thesis ends with conclusions and recommendations for future work in Chapter V.

## II. MODELING AN FDDI LAN IN OPNET®

## A. OVERVIEW

This chapter is intended to provide a tutorial on the use of OPNET<sup>®</sup> to model an FDDI LAN by providing a brief set of steps to build and execute a simulation. The current version as of this writing is Release 2.4.A, dated 02/27/93, which is the third revision. Release 2.4.A, Errata 1, dated 08/01/93 is a manual update. Some prerequisite knowledge is required of the user, including "C" programming language syntax, ability to use a UNIX workstation, and an understanding of the FDDI protocol. MIL 3, Inc. provider thorough documentation in the form of an eleven volume set of manuals, the first of which is Vol. 1.0, entitled: Tutorial Manual. It includes a general introduction to OPNET<sup>®</sup>, a trouble-shooting guide, and five chapters presenting different communications network models. While none of these discusses FDDI in particular, all are designed to familiarize the novice user with the mechanics of the user interface, and should be studied prior to working with OPNET. Volumes 4.0 and 4.1, the Tool Operations Manual, describes the editors of the user interface, and should likewise be studied. The chapter entitled "FDDI" in Vol. 8.1.0, Example Models Manual, Protocol Models, discusses the FDDI simulation in detail, and provides the essential information to build, develop and execute a simulation. Most of the information presented here is available in the manuals, but a number of idiosyncrasies exist which are not readily documented. These required trial and error experimentation to discover, and in many cases required explanation

from MIL 3, Inc.'s excellent technical support organization. The new user is advised to heed every sentence regarding mechanical details; much of the advice given is hard-earned.

This thesis will not present an explanation of the FDDI protocol in detail, except as necessary to emphasize or clarify the operation of the model. Many discussions exist in the research literature and textbooks, for example, Stallings, (1991, 1993). Those interested in the physical characteristics of optical fiber systems are referred to Powers, (1993). A useful introduction to modeling FDDI in OPNET<sup>®</sup> is *Modeling and Simulation of a Fiber Distributed Data Interface Local Area Network*, a Naval Postgraduate School MSEE thesis by Aldo Schenone, which summarizes OPNET<sup>®</sup>s FDDI model, includes a detailed description of the FDDI protocol, presents the results of several simulations, and ends with a challenge to other researchers to further develop the model. (Schenone, 1993)

The remainder of this chapter will briefly introduce the structure of OPNET S FDDI LAN model and its components, introduce some preliminary modifications, then lead the reader through a simple simulation.

## **B. PRELIMINARY THEORY**

#### 1. Theoretical vs OPNET® Model of FDDI

The setting of parameters in OPNET® simulations is based on the following equation and discussion, which is found in most literature treating FDDI LANs, including

Powers (1993, p. 340), Stallings (1993, p. 225), Tari, et al (1988, p.55), and Jain, (1991, p.20), to name a few:

$$D_{Max} + F_{Max} + TokenTime + \Sigma SA_{i} \leq TTRT$$
 (1).

where:

 $SA_i$  = synchronous allocation for station i,

 $D_{Max}$  = propagation time for one complete circuit of the ring,

 $F_{Max}$  = time required to transmit a maximum-length frame (4500 octets), and TokenTime = time required to transmit a token.

In an actual LAN, a station management protocol handles the assignments of SA, which may be changed in real time. OPNET® simulations represent steady-state performance, and do not contemplate changing network conditions.

All stations negotiate a common value of TTRT. Also, these timers and variables are maintained at each station

- Token Rotation Timer (TRT)
- Token Holding Timer (THT)
- Late counter (LC)

Each station is initialized to the same TRT, which is set to TTRT. Note that LC, TRT and THT are not global; each station maintains its own copies, which will differ from those of other stations. If a given station receives the token before its TRT has expired, then that TRT is reset to TTRT. On the other hand, should the token arrive after the expiration

of TRT, then its lateness is recorded by setting LC to "1" (at that station). Two consecutive late tokens will increment LC to "2", in which case the token is considered to be lost, and a re-initialization process will commence. OPNET<sup>®</sup> has no provision for re-initialization. On the other hand, as a computerized simulation, it never permits LC=2 to occur.

When the token arrives early, (before TRT expires), THT is set to the current value of TRT. The transmission rules are as follows:

- 1. A station may transmit synchronous traffic for a time SA, as specified for that station.
- THT is enabled after synchronous traffic is sent, or if there was no synchronous traffic to send. The station may transmit asynchronous traffic while THT > 0.

In the TX\_DATA state of the MAC process model, THT is incremented from zero to THT. This is an important point in regard to the prioritization scheme by which a value T\_Pri[i] is assigned to each priority setting, and the eligibility of a given priority to transmit depends on T\_Pri[i] in comparison to THT. That is, for THT decrementing, priority i traffic may be transmitted as long as T\_Pri[i] is less than THT. This implies lower T\_Pri[i] is assigned to higher priority. In OPNET\*, THT increments up from zero. Priority i traffic may be transmitted as long as T\_Pri[i] is greater than THT, implying that higher T\_Pri[i] is assigned to higher priority. This subtle point is important to know when setting values to T\_Pri[i] in the INIT state of the MAC process model.

There is an important distinction in the timing of transmission eligibility for synchronous and asynchronous traffic:

- The time spent sending synchronous traffic may not exceed SA, for station

  i. That is, the protocol will not allow a synchronous packet transmission to commence if it can not be completed without exceeding SA,. OPNET supports this criterion.
- Asynchronous transmissions may commence as long as THT has not expired. Any packet transmission in progress when THT expires is allowed to complete, but no more will commence.

The protocol allows the actual token rotation time to have a maximum val.

(2)TTRT, with an average value of TTRT over time.

#### 2. FDDI LAN Equation Parameters

Each of the terms in Equation 1 given above is addressed in this section, with reference to its representation in OPNET Environment file attributes.

#### a. "F\_Max"

The time required to transmit a maximum length packet is based on the assumption that any station is capable of transmitting at the rate of 100Mbps. Since the maximum packet length is 36,000 bits (4500 octets or bytes), the algebra yields 0.360 ms for F\_Max. Powers agrees (1993, p. 340), but Tari et. al. use 0.361 ms (1988, p. 55). In OPNET<sup>®</sup>, the fddi\_mac process model defines the transmission rate as 100 Mbps, in the Header Block. F\_Max is not directly assigned as an attribute, but simply exists as a physical characteristic which must be considered in determining TTRT and SA, assignments.

#### b. "D Max"

The Maximum Ring Latency is the time required for a frame to travel around the ring. The maximum value is often assumed in textbook discussions, but it should be calculated for individual cases. The total delay may be defined as follows:

Powers uses 1.73 ms, Tari et.al. uses 1.62 ms, and Dykeman and Bux use 1.62 ms. D\_Max includes the time required for a frame (which is basically a number of light flashes) to travel the length of the fiber on the ring, plus the time required to cross each station interface.  $5.085 \times 10^{-06}$  sec./km. is the value used in the literature for the delay rate of a signal in optical fiber. The reciprocal results in 1.9665e+08 m/s, which agrees with the generally accepted value of  $\frac{2}{3}$  c for the speed of light in glass. OPNET and Dykeman & Bux use a station delay of 60.0e-08 sec. Powers assumes 1  $\mu$ s as a representative value. Ultimately the value is a physical characteristic that could be measured on a real device, and may be declared in a computer simulation. The value 1.617 ms derives from using the maximum possible dimensions: 500 dual attachment stations or 1000 single attachment stations on a 200 km ring yield the following:

$$(1000 \cdot 60.0 \times 10^{-08}) + (200 \cdot 5.085 \times 10^{-06}) = 1.617 \text{ ms.}$$

The environment file attribute prop\_delay represents the actual time the packet is on the fiber between two stations, rather than the delay rate, and therefore defines the size of the ring. OPNET® has no safety feature to prevent the user from entering

attributes that would define a ring larger than 200 km. Note that the stations are assumed to be equally spaced. The user should realize that the value of 5.085e-06 given with the original example environment file implies a one kilometer length of fiber, rather than a delay rate. Powers (1993, p. 328) notes that the early proposal for FDDI limits fiber length between stations to 2 kilometers. In OPNET<sup>®</sup>, the attributes prop\_delay and station\_latency are used in the "C" code to postpone the reception of a packet until sufficient time has passed to allow for physical delays.

#### c. "SA;"

Synchronous allotment, or synchronous bandwidth, is the time a station is granted to transmit synchronous traffic, regardless of the lateness of the token. It is a form of prioritization, providing a means by which certain types of traffic are not delayed. For example, voice traffic would be synchronous. Textbook discussions represent SA, in units of time for each station.

Asynchronous traffic is transmitted whenever there is THT remaining after the transmission of all synchronous traffic. It is the responsibility of the Station Management Protocol (in OPNET®, the user) to ensure that the synchronous bandwidth is sufficient to handle the synchronous offered load. One mance involves the inviolate nature of SA, for each station. A given station's synchronous offered load may amount to relatively little in terms of bits per second, while the packet size is assigned a value too large to be transmitted in the time SA,. In this case, synchronous traffic would never be transmitted, and outbound packets would simply accumulate in the buffers of the MAC. The Environment file attribute

"sync bandwidth" corresponds to SA<sub>1</sub>, but is expressed as a unitless fraction of TTRT, rather than as a time.

#### d "TTRT"

Equation 1 suggests that physical requirements of the fiber and the stations are used to determine a workable TTRT value. The FDDI specification allows a range of settings from 4 ms to 165 ms (Powers, 1993, p. 339). Powers also notes that synchronous voice transmission requires  $\sum SA_1 = 10$  ms. In OPNET, the Environment file attribute T\_Req corresponds to TTRT.

## C. MODEL STRUCTURE

OPNET<sup>®</sup>'s FDDI LAN model structure is hierarchical. The LAN is a ring made of stations and the connections between them. Figure 1 shows a 50-station FDDI LAN as shown in the user interface window. Figure 2 is a ten station ring provided for greater clarity of detail. The stations are modeled as connected nodes, each of which is in turn defined by a process model. The processes are represented by state transition diagrams, which are the ultimate source of the "C" language code that describes the model's behavior. Figure 3 illustrates the FDDI station model. Figures 4-6 are the process models for the source, sink, and MAC processes, respectively. These correspond one-to-one to the nodes "11c\_s1nk," "11c\_s1nk," and "mac" shown in Figure 3. The packets of information that travel between stations on the ring, and between nodes within the station, are also modeled and may be



Figure 1. 50-Station FDDI LAN, "fddi\_net\_50"

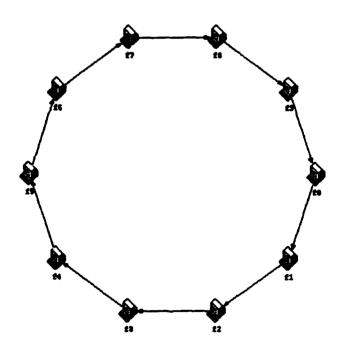


Figure 2. Ten-Station FDDI LAN, "fddi\_net\_10"

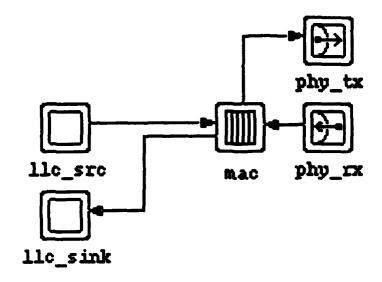


Figure 3. FDDI Station Model, "fddi\_station"

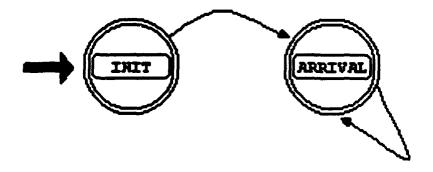


Figure 4. Source Process Model, "fddi\_gen"

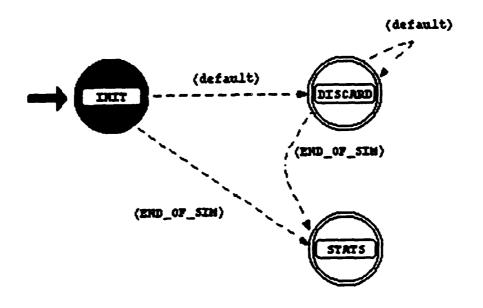


Figure 5. Sink Process Model, "fddi\_sink"

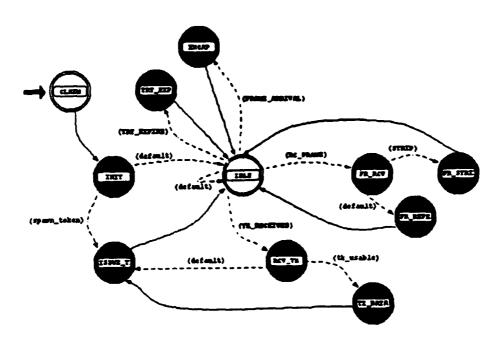


Figure 6. MAC Model, "fddi\_mac"

modified. Model parameters may be entered by several methods, with the Environment file being by far the most convenient.

#### 1. FDDI LAN

Figure 7 shows a 32-station FDDI ring in the user interface window as it would appear on a computer screen. This image is displays the Network Editor, whose icon appears toward the upper left corner of the figure. (A note on the mechanics of activating the various editors: as indicated in the tutorial manual, the center mouse button activates the desired editor. If instead either the left or right button is pressed, then the opposite button must also be pressed to "cancel" the first; only then will the center button work as expected). To the right of the ring are on-screen menus of attributes for one station (actually, three menus are shown to display simultaneously all the fields). This menu is invoked by placing the cursor over the desired station, then pressing the right mouse button. The FDDI protocol supports up to 500 dual-attachment stations on a ring, and OPNET® permits from two to 500 stations in a ring.

Actual generation of the ring is best done outside OPNET®, through a UNIX command window set to the "~\op\_models\fdd1" directory path. The command "fdd1\_bu1ld.em.x \number\_of\_stations>" will automatically generate an FDDI LAN with the number of stations specified. The user should verify that this function is present in the desired subdirectory. This operation is described in manual Vol. 8.1.0, "FDDI," and refers the user to Vol. 6.0, External Interfaces Manual, which has a more complete description of the ring building protocol. Were OPNET® active during this ring-building process, then the "Rehash" icon toward the lower right of the user interface window must be activated to update the

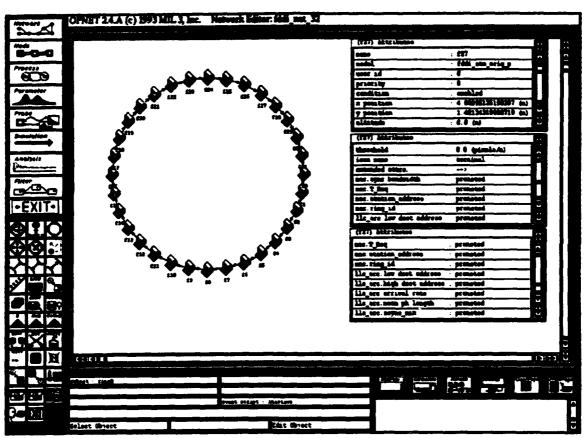


Figure 7. Network and Attributes Menus in User Interface Window

program's access to models in the subdirectory. In general, the "Rehash" command should be used frequently, particularly when new files are generated through simulation runs or through model editing.

The LAN as shown is not actually a true ring architecture, as no dedicated physical layer object exists in OPNET<sup>®</sup> for modeling ring architectures. The model is in fact a circle of point-to-point links; the ring is an abstraction whose characteristics and behavior are represented in the "C" programs that comprise the process models. (OPNET<sup>®</sup>, Vol. 8.1.0, "FDDI," p.23)

#### 2. FDDI Station

Figure 8 illustrates the FDDI station in the user interface window, summoned and printed from the Node Editor. Also shown are the menus listing the attributes associated with each part of the station model. Message traffic in the form of packets is generated at the source, <code>llc\_src</code>, at a rate specified by the user. The source model does not function as a true Logical Link Control (LLC) beyond correctly interfacing with the Medium Access Control (MAC) model. (OPNET<sup>®</sup>, Vol. 1.0, "FDDI," p. 21) The MAC entity is represented by mac in the model, and is responsible for encapsulating packets generated by the source, holding these packets until they can be transmitted, receiving packets from other stations, destroying packets as needed, and maintaining the locally held Token Holding Timer (THT) and Token Rotation Timer (TRT). Packets are counted and statistics gathered at <code>llc\_sink</code>. These three nodes are modeled in detail by respective process models, which may be assigned with the attributes menus shown in Figure 8. The field "process model" may be changed

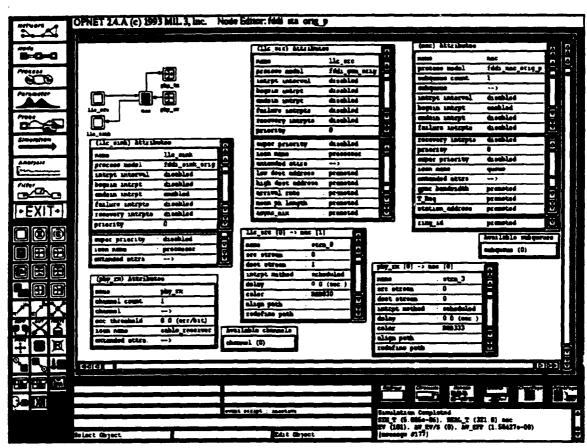


Figure 8. FDDI Station Model with On-Screen Attributes Menus

by cursor action, with selection made from the resulting submenu presenting a list of available process models. The nodes "phy\_rx" and "phy\_tx" represent the receiver and transmitter interfaces to the ring, and are not further defined by process models.

The user may modify the station model within the Node Editor by setting the attributes fields as desired, then saving the model by activating the "write node model" icon toward the lower left corner of the user interface window. By then exiting the Node Editor, entering the Network Editor, and calling the desired network model (e.g., "fdd1\_net\_32"), each station acquires the new setting when the network model is archived and bound ("A+B" icon). The same modification may be effected from within the Network Editor by calling the attributes menus for each station and setting them individually. This method would be preferred only if the user desires to set differing attributes in various stations. Note that setting the attribute fields is not the same as modifying the process model itself, which is accomplished with changes to the "C" programming code accessed through the Process Editor.

#### 3. Processes

Process models are specified by State Transition Diagrams (STD) representing the actions of the nodes within the station model. Figure 9 illustrates the process model fddi\_mac as it appears in the Process Editor in the user interface window. Figure 10 shows the on-screen menu that appears when the cursor is placed over one of the states (ENCAP in this case), and the right mouse button pressed. Invoking the "enter execs" attribute calls the text editor shown in Figure 11. Here the user may inspect the programming code behind

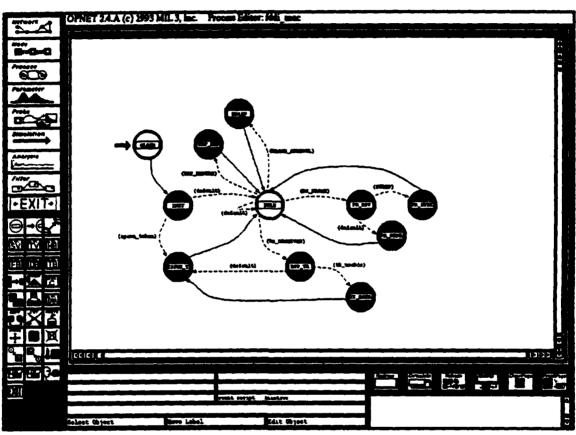


Figure 9. Process Model "fiddi\_mac" in User Interface Window

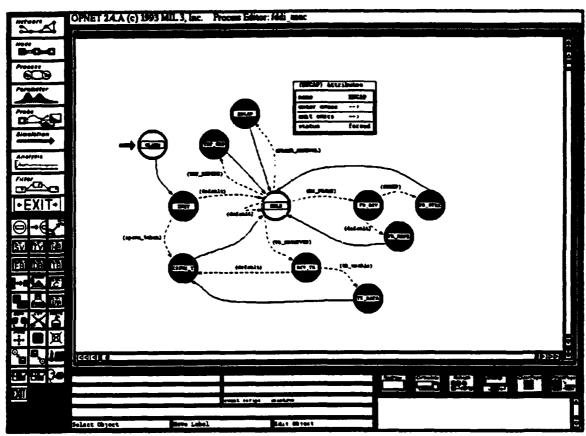


Figure 10. Process Model "fiddi\_mac" with On-Screen Attributes Menu

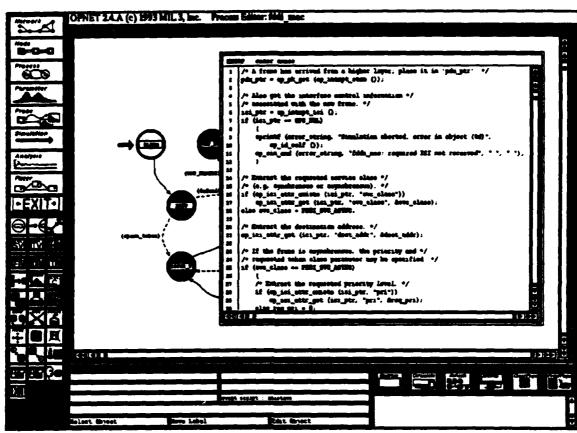


Figure 11. Process Model "fiddi\_mac" with Text Editor

the model's behavior, and modify it if desired. Each state has its own section of code, and the icons to the left of the window include additional editors, all described in Vol. 4.0, Tool Operations Manual. These are primarily variable and function declaration sections. The entire code for the process may be called with the icon ".C," but this editor is for viewing only. Any changes made in the ".C" editor will remain when the editor is dismissed, but will disappear if the process is compiled. If calling the ".C" editor returns a ".C file unavailable" message, then recompiling will generate the file again (sixth icon above the lower "EXIT" icon in the Process Editor). Many UNIX stations include a cleanup command that deletes certain temporary files upon logging out of the system, and the "C" language codes ending in ".c" are not necessary once simulations are generated. They may always be recovered by recompiling the models. (In the file directory containing OPNET<sup>®</sup>, the model source codes have the suffix ".pr.m".) If changes are made (in a proper editor), the model must be recompiled. If several changes are being made within different sections, each may be saved with the keystrokes (CTRL+5), deferring compiling to the end. (The set of manuals includes a summary page of OPNET stext editor keyboard commands.) If desired, the model may be saved without compiling by using the "Write Process Model" icon. When a process is changed, the station model in the Node Editor must also be called and written afresh. Then the corresponding network model must be called into the Network Editor, and be archived and bound again. If the modified process was not compiled earlier, then the "C+A+B" icon will compile all the process models in addition to archiving and binding them.

#### a. Source Node

The source node of the FDDI station model generates packets at a rate and size specified by the user. It also determines the destination address for each packet, the priority if applicable, and records the packet's creation time so that delay statistics can later be gathered. These data are passed to the MAC for encapsulation. In the Node Editor, the source is labelled "11c\_src." and the process model is "fdd1\_gen."

In the source process' original form, as released by MIL 3, Inc. in version 2.4.A, the packet arrival (generation) rate is stochastically assigned on an exponential distribution approximating that specified by the user. If a precise, invariant arrival rate is desired, it may be assigned with the following change to the INIT state in the Process Editor, where "constant" is substituted for "exponential" in the line:

```
inter_dist_ptr = op_dist_load ("exponential", 1.0 /
    arrival_rate, 0.0);.
```

A voice traffic transmitter station, for example, would require a constant packet arrival rate from the source. Similarly, packet length is originally assigned a constant value in the given model, but may be set to a stochastic approximation of the requested value by replacing < "constant"> with < "exponential"> in the line:

If all stations on the ring are to have the same assigned attributes and the same source code (i.e., all < "constant"> or all < "exponential">), then the remaining steps

are to save and compile the process in the Process Editor, then call and save the station model in the Node Editor, and finally archive and bind the relevant LAN model in the Network Editor. If the stations on the LAN do not all have identical source code (i.e., some "constant" and others "exponential"), then the modified process models and their corresponding node models must be renamed. The following steps illustrate the creation of a station modified to allow a ring to simulate a number of voice stations amid other transmitters:

- 1. In the Process Editor, substitute "constant" for "exponential" in the "fdd1\_gen" model's INIT state editor. Save the change by keying <CTRL+S> while the cursor is inside the INIT state's editor.
- 2. Use the "Write Process" icon to save the modified process under a new name, for example fddi\_gen\_const.
- 3. Compile the new process model, then exit the Process Editor.
- 4. The Node Editor is used to create and save a new station model, by calling the original model and changing the "process model" field in the on-screen menu for the relevant node (in this example the "fdd1\_gen" process in the llc\_src node is changed to "fdd1\_gen\_const").
- 5. The new model is saved by invoking the "Write Node Model" icon. Exit the Node Editor.
- 6. In the Network Editor, the desired stations on the relevant LAN are reassigned using the on-screen menus: when the "model" attribute field is invoked at a

particular station, a list of available station models appears, and the desired one is chosen. If the expected model does not appear in the list, activate the User Interface Window "Rehash" icon to refresh OPNET access to recently created files.

- 7. Desired stations are reassigned as required, and the LAN is saved, then rearchived and bound.
- 8. Differently named models using the same functions may cause naming conflicts at simulation run time. Should this occur, then the word "static" must be inserted in the Function Blocks ("FB" icon) of both the original and the new source model processes in the Process Editor, just prior to fddi\_gen\_schedule().

The above steps illustrate a change made to the source code, and do not represent the same situation in which identical stations are assigned different values in the given on-screen attributes menus.

#### b. Sink Node

The 11c\_sink node of the station model is the final destination of all message traffic. The INIT state establishes counters to hold statistical information regarding network performance (throughput and delay). The STATS state updates these counters as packets are received. The DISCARD state reports the statistical information at specified intervals, and finally destroys the packet. Because new packets are created for each transmission, they must eventually be destroyed when received, or the host computer conducting the simulation will soon fill its memory.

The "fdd1\_s1nk" model in the current version of OPNET® (Release 2.4.A, dated 02/27/93) is defective. It will cause the simulation to abort upon completion, with the error message "Program Abort: packet pointer is NIL," in the event any station did not receive traffic. Figure 12 illustrates the State Transmission Diagram as originally given, and the corrected version is shown in Figure 13. The user should correct the defective version, referring to Vol. 1.0, *Tutorial Manual*, "Bpt," pp.6-10. Saving this modification requires the same steps described for the source model, with the exception that no text has been changed. This defect and its correction are documented by Mil 3, Inc. as OPBUG 2070.

MIL 3, Inc. maintains an electronic bulletin board containing information on model corrections and upgrades between OPNET<sup>®</sup> revisions. Users may acquire these upgrades using file transfer protocol (ftp) procedures to download desired files. Appendix A includes a sample of dialogue used to acquire an upgraded file from MIL 3, Inc.

#### c. Medium Access Control (MAC)

The MAC process model encapsulates frames received from the source node for transmission to other stations, maintains token holding and token rotation timers, inspects received packets, decapsulates received frames, and determines token usability. Vol. 8.1.0, Example Models Manual, "FDDI," provides a detailed description of the MAC process and of the functions of its component states.

The MAC model provided with OPNET Version 2.4.A, (filename: "fdd1\_mac.pr.m") has been upgraded by MIL 3, Inc. and the newer model and

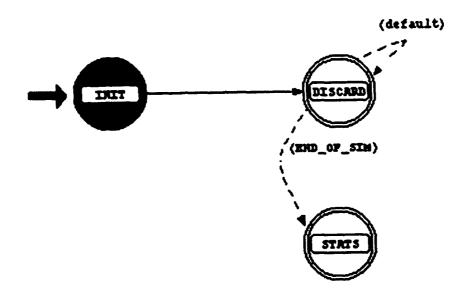


Figure 12. Original (Defective) Sink Process Model

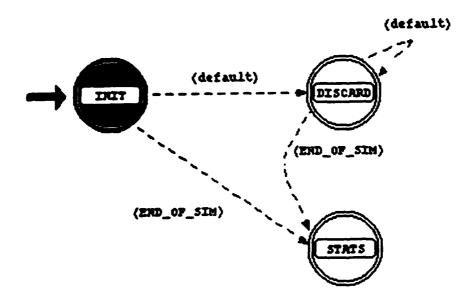


Figure 13. Corrected Sink Process Model

documentation are available via ftp on Internet, under the subdirectory "~/patches/2.4.8/opbug\_2081". When retrieving a file via ftp, the user should verify the entire file is received by checking the file size listed on the bulletin board against the size of the file received. Entering < "type image"> at the ftp prompt should ensure a full and intact file retrieval. The original "fdd1\_mac.pr.m" file is then removed from the user's directory (~/op\_models/fddi), stored in a safe place, and replaced with the newer version. The new file must then be compiled from the command window with the command, < "op\_mkpro -m fddi\_mac">, the procedure for which is described in Vol. 6.0, External Interfaces Manual, "Env". It has been observed that the "drag and drop" method of transferring files in using the File Manager in the SunOS Windows environment sometimes causes the subject file to gain or lose a byte or two, leading to "b1tsum error" messages when said file is compiled in OPNET®. Standard UNIX commands are the most reliable method for moving OPNET® source files.

The patch is not necessary to operate the simulation; it is a refinement of the model, and will be included in the next revision of OPNET. The patch OPBUG 2081 actually includes three repairs, documented as OPBUGs 2081, 2095, and 2097. OPBUG 2081 corrects existing timing and efficiency inaccuracies connected with the token acceleration feature, by which the token is destroyed and the simulation enters a "fast forward" mode in order to reduce the number of events while no station has a need to transmit. In a real FDDI LAN, TRT is reset each time the token passes, whether or not the token is used. The model in its original form allows the TRT timer to continue running when

the simulation enters "token acceleration," resulting in unexpected Late\_Count occurrences. OPBUG 2095 is also related to the token acceleration feature, correcting the existing incorrect initialization of several variables when the simulation enters token acceleration. In particular, the variable Fdd1\_Num\_Stations, the number of stations on the ring, is always reset to one, upsetting calculations predicting the proper location of the token at the end of an idle period. Finally, OPBUG 2097 addresses the fact that the original model neglects to properly account for the transmission delay associated with the token itself.

#### 4. Packets

All communications between stations in a LAN and between the internal nodes of a station are conducted using data framed into packets. The Parameter Editor, described in Vol. 4.0, *Tool Operations Manual*, "Pm," is illustrated in Figure 14, which shows the packet structure fddi\_mac\_fr. which is used to encapsulate the frames generated in the Source node and sent to the MAC. Appendix B lists the five packet structures used in the FDDI LAN simulation, giving their fields and assignments. OPNET® simulation does not enforce limits on packet size required by the standard IEEE 802.5.

#### a. ICI Formats

Interface control information packets (ICI) are used for internal communication within a station, reporting for example service options, error conditions, and packet arrivals. Figure 15 shows the ICI Editor within the Parameter Editor, with the ICI fddi\_mac\_req. This ICI specifies the control information passed from the source to the MAC when transmission requests are generated. The ICI fddi\_mac\_ind specifies control information passed from the MAC to the LLC when a packet has been received by a station.

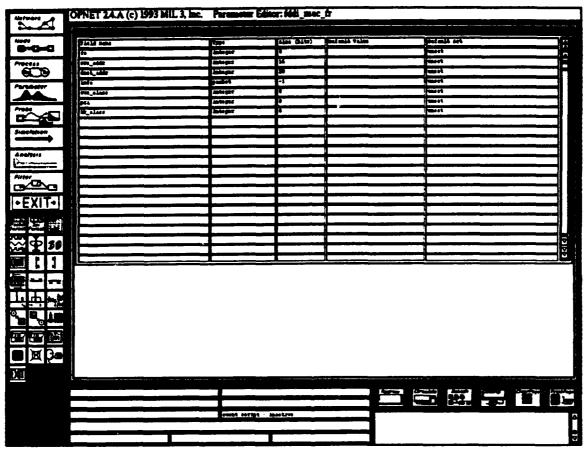


Figure 14. Packet Format "fddi\_mac\_fr" in the Parameter Editor

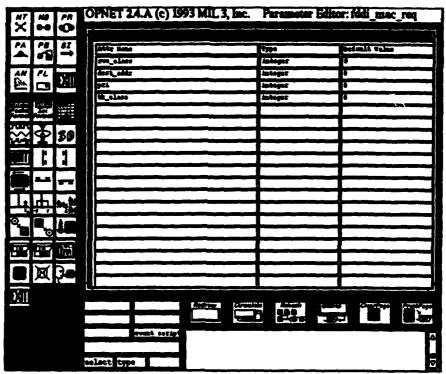


Figure 15. ICI Format "fddi\_mac\_rec" in Parameter Editor

For OPNET<sup>®</sup> simulation purposes, both structures are created once per station in the simulation, and reused as needed.

## b. Packet Formats

Three types of packet frame formats exist in OPNET® to simulate communications between the stations. For the simulation these are created as needed and destroyed when no longer needed. Packets of format "fdd1\_11c\_fr" are created in the LLC source as arrivals are generated. The format has only one field, containing the creation time, which is used to generate delay and throughput statistics when the packet is finally received at its destination address. The packet format "fdd1\_mac\_fr" is used in the MAC state ENCAP to encapsulate the generated packets for transmission on the LAN. The "info" field contains the "fdd1\_11c\_fr" structure providing the data of interest. Because OPNET® simulates only the characteristics of transmission and not the actions of stations in response to information received, the packets used are not precise replicas of real FDDI frames. The token is represented by the frame format "fdd1\_mac\_tk". The field "fc" is inspected by each MAC process receiving a packet to determine whether it is a token or a message packet.

#### 5. Environment File

Appendix C is an example of an environment (or configuration) file used to assign station attributes to a 32-station FDDI LAN. Inspection shows that the fields specified in the file correspond to the "promoted" fields in the on-screen attributes menus that appear in the Network and Node Editors, and to the fields in the Simulation Editor. Promoted attributes may be assigned directly within these editors, a tedious and error-prone process at best. If

a simulation is begun with none of these attributes specified, OPNET<sup>e</sup> will prompt the user for inputs at the command screen, another error-prone and tedious process. The environment file is the most efficient way to assign parameters of interest, and may be quickly modified between simulations, using the UNIX text editor. Vol. 6.0, External Interfaces Manual, "Env." discusses the environment file, and points out that attributes assigned in the environment file supersede those assigned in the Node and Network Editors. The file is recognized to OPNET® by its ".ef" suffix. The assigned attributes are described in chapter "FDDI" of manual Vol 8.1.0, Example Models Manual, and are presented here in their order of appearance for summary and in some cases for required elaboration. The attributes are not declared as variables in the "C" programs, but rather are generated by adding them to the onscreen menus in the Node and Network Editors. The user can create new attributes by adding them to the "extended attributes" field in the Node Editor, then including them in the environment file. This procedure is discussed in Chapter III. "Env," pp.33-34, in the External Interfaces Manual discusses the use of name wildcards in the attributes given below. Sequence of entries is not significant in the environment file.

## a. "station address"

This attribute is required for station identification; numbering of stations is from zero to N-1, where N is the number of stations on the ring. The INIT state in the MAC process calls this variable. Note that for quick changes to the file, lines may be commented with the pound key <#>.

## b. "ring\_id"

This attribute identifies the ring, in the event more than one may be modeled simultaneously. It is set to zero if there is only one ring.

#### c. "low dest address"

This attribute assigns the lowest identification address that may receive traffic from this station. The use of the wild-card asterisk, shown in Appendix C, assigns the same value to all stations. Quotation marks are used here because the attribute assigned has spaces vice underscore marks between words.

## d. "high dest address"

This attribute assigns the high end of the range of addresses to which a station may send traffic. It is used in conjunction with the previous attribute by the "11c\_src" process in the INIT state. It is permissible to limit the range of target addresses, down to one, but all target addresses must lie in a contiguous sector. For example, the code as given has no provision for allowing a particular station to send packets to two different stations without also possibly sending to the stations between them.

#### e. "arrival rate"

This attribute assigns the rate at which the source process will generate packets, and it is called by the INIT state. It may be set to zero for any station intended to be idle. As originally used in the process code, arrival rate is a stochastic approximation exponentially distributed about the assigned value. To make this a precise unchanging value, as in the case of a synchronous voice transmitter, the code would have to be modified as described in the previous discussion of the source process.

## f. "mean pk len"

Mean packet length is expressed in bits. Despite the name, this value is actually held constant by the INIT state of the source process model. The user may modify the code using the procedure described earlier to substitute < "exponential"> for < "constant"> in the line:

which appears toward the bottom of the INIT state enter executives in the Process Editor.

Then the station will generate non-identical packets, which may be more realistic behavior.

OPNET<sup>®</sup> will permit any number of bits for the packet length; the user should know that

FDDI packets have a maximum size of 36,000 bits.

## g. "async\_mix"

FDDI stations may generate synchronous and asynchronous traffic. This attribute sets the proportion, with 1.0 indicating all asynchronous traffic generated by the given station, 0.5 indicating half synchronous and half asynchronous. Any value between zero and one inclusive may be chosen. The INIT state in the 11c\_src model calls this attribute with the statement:

## h. "sync bandwidth"

This attribute is used in the MAC process, INIT state, where it is expressed as a percentage of TTRT. It is analogous to  $SA_i$  in Equation 1, but is numerically a fraction of  $T_Req$  (TTRT), while  $SA_i$  is an amount of time. Synchronous bandwidth should not be confused with synchronous offered load. Bandwidth is expressed in time, while synchronous offered load is a bit transmission rate. Therefore, synchronous bandwidth is the time allotted for the transmission of synchronous offered load. It is entirely possible to set parameters so that these two attributes do not match well.

In describing "sync bandwidth," Vol. 8.1.0, Example Models Manual, "Fddi", warns the user not to allow the sum of all assigned attributes "sync bandwidth" to exceed one, since OPNET® does not enforce FDDI protocol standards. However, this warning neglects to consider the physical delay parameters in Equation 1, which indicate that total synchronous bandwidth must be somewhat less than TTRT. The correct assignment of "sync bandwidth" involves some algebra, and is perhaps best explained with an example.

Given:

D Max:

1.617 ms.

F\_Max:

0.360 ms.

Token\_Time:

0.00088 ms.

TTRT:

8.0 ms.

Using Equation 1 yields:

8.0 ms. - (1.617 ms. + 0.360 ms. + 0.00088 ms.) = 6.02112 ms. =  $\Sigma SA_i$ 

This is 6.02112 ms. of total bandwidth to be divided among as many stations assigned. Assume there are five such stations:

$$6.02112 \text{ ms.} + 5 \text{ stations} = 1.204224 \text{ ms} = SA_i$$

SA, is converted to "sync bandwidth" with a division by TTRT:

$$1.204224 \text{ ms.} \div 8.0 \text{ ms.} = 0.150528$$

This is the value entered into the Environment file.

## i. "T Req"

This attribute is called in the MAC process, INIT state, and represents the specified station's requested value of TTRT. A real FDDI LAN has a TTRT negotiation phase; OPNET® simply chooses the smallest T\_Req value available. The user may, but need not set different values to each. This value is in units of seconds, which is not apparent from the manuals nor from the default value that appears at the command prompt if no value is assigned.

# j. "spawn station"

The spawn station is simply the starting point for the token, and may be assigned to any station on the LAN.

# k. "station\_latency"

This is the delay incurred by packets as they pass a station's ring interface. Powers gives 1  $\mu$ sec. (1993, p.336); 60.0e-08 sec. agrees with Dykeman and Bux (1988). Station latency is a component of D\_Max in Equation 1.

# l. "prop delay"

Propagation delay is the time separating stations on a ring, based on the amount of fiber between them. It is given here in seconds, and may be used to define the ring size. The INIT state of process fdd1\_mac calls this value, which is used as one of the delay

parameters applied to transmission commands. FDDI standards limit the ring size to a maximum of 200 miles of fiber (Dykeman, 1988, p. 997), and OPNET<sup>®</sup> assumes that the fiber length is divided evenly among the stations. That is, all stations are evenly spaced on the LAN in OPNET<sup>®</sup> simulations, whatever the number of stations and length of fiber. Dykeman and Bux (1988, p. 1000) define propagation delay in units of time per distance, and give a value of 5.085  $\mu$ s/km. The value given in the original example environment file, 5.085 × 10<sup>6</sup> seconds, corresponds to one kilometer of fiber between stations.

## m. "acceleration\_token"

This attribute speeds the culation by removing the token during idle periods when no station has packets to transmit, significantly reducing the number of events.

#### n. "duration"

This is the simulated run time in seconds. Most systems should reach steady state in less than a second.

#### o. "verbose sim"

This feature enables on-screen reports regarding event numbers, time remaining until completion, etc.

### p. "upd int"

This specifies in seconds the intervals at which to make on-screen simulation status updates. It must be less than duration to be useful.

## q. "os\_file"

The output scalar file receives scalar data accumulated over several simulations. It is useful in observing the effect of varying one or more attributes, for example TTRT, over a series of experiments.

## r. "ov file"

The output vector file receives throughput and delay information relevant to one simulation run. Output vector files can be quite large, on the order of several megabits, and for this reason are often automatically deleted by a < "cleanup"> command included in a UNIX station's logoff sequence. The user should alter the filename or save desired plots as ".ac" files using the Analysis Tool, rather than log off planning to study the vector data at some future time.

### s. "seed"

This is a constant used by the simulation's random number generator. It may be any positive integer, but should be left constant once chosen.

### t. "debug"

This enables the Debug Tool, allowing the user to step through a simulation one event at a time. Once enabled, the command < "help"> provides a listing of the debugger's features.

# D. SIMULATION

This section presents the steps involved in running a simulation and observing the resulting output data. The user must keep in mind that OPNET<sup>e</sup> is unaware of IEEE 802.5. That is, it is the user's responsibility to ensure reasonable input parameters are assigned in keeping with the established standards. The steps given will use the original model provided.

### 1. Build the LAN

If a 32-station LAN is not already available in the Network Editor, then one should be created using the command < "fddi\_build.em.x 32">, as described earlier.

### 2. Correct "OPBUG 2070"

The simulation will abort if the original process model fddi\_sink is used for the SINK node llc\_sink, and some station happens to have not received any packets. The correction described earlier should be applied, and the model recompiled and saved.

## 3. Implement "OPBUG 2081" Patch

As described earlier, this repair corrects minor timing inaccuracies in the model, related to the token and to the token acceleration feature. The simulation will work without aborting if the patch is not applied, but the user planning on implementing code changes within the model over the long term should patch the model before doing so.

# 4. Update Environment File

Refer to the configuration file in Appendix C for input parameters. As mentioned before, use of this file will save the user the effort involved in setting parameters by hand through the Node, Network, and Simulation Editors. Note that attributes assigned in the

Environment file will supersede any that are assigned through these editors. The file should be given some distinguishing name, for example "fdd132.ef".

### 5. Generate Probe File

Use of a Probe file is optional. The process code as written will generate vector file outputs only for overall throughput, delay, and mean delay. Additional outputs may be monitored through the use of a Probe file, illustrated in Figure 16. Vol. 4.1, *Tool Operations Manual*, "Pb," describes the Probe Editor. With it the user may monitor, for example, each station's arrivals (packet generation) and throughput at any physical interface point on the LAN, measured in packets and/or in bits per second. For simplicity, only packet arrivals at station f11 will be assigned a probe in this simulation.

#### 6. Simulation Editor

Figure 17 illustrates the Simulation Tool in the user interface window, with the settings necessary to run this simulation. Use of this tool is discussed in Vol. 4.1, Tool Operations Manual, "Sm". Fields are assigned by use of the cursor, and are filled by choosing from on-screen menus or keyboard entry. The "Simulation" field should be assigned the LAN filename, < fddi\_32\_net> (note that filename suffixes are not visible to the user in the various Editors). The fields "Probe File", "Vector File", "Scalar File", "Seed", "Duration", and "Upd Intvl" may all be left blank if they are assigned in the configuration file; "Probe File" is optional in any case. The "Arg Name" field should be assigned <environment file>, and the "Arg Value" field should be assigned the

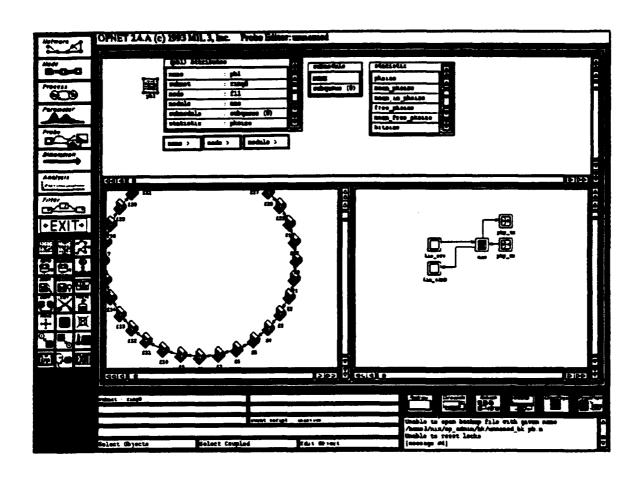


Figure 16. Probe Assignment

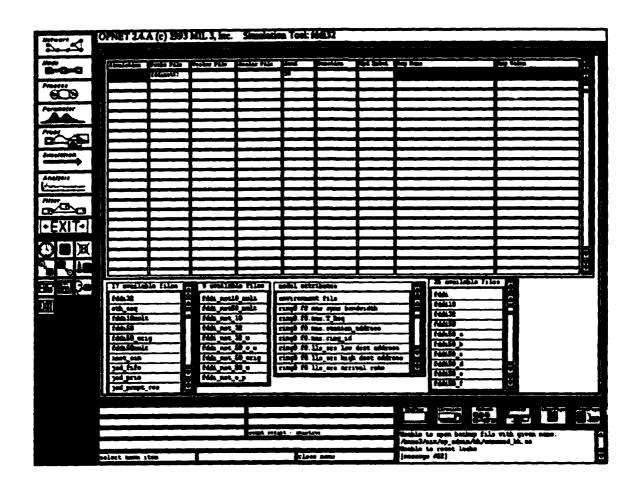


Figure 17. Simulation Tool

"Write Simulation" icon, to spare the effort of filling in these fields again on future simulations.

#### 7. Start the Simulation

Once the fields are set, the simulation is started with the "Execute Simulation Sequence" icon. Had the user neglected to assign some parameter, the simulation will wait until the command line prompt has been answered; the user should keep the command screen in view. Upon completion of the simulation, a vector file (suffix ".ov") will be generated, along with a scalar file (suffix ".os"). The "Rehash" icon must be invoked to refresh OPNET<sup>®</sup>'s access to the files. Then the user may exit the Simulation Tool and enter the Analysis Tool.

### 8. Analysis

Figure 18 shows the Analysis tool in the user interface window, whose operation is explained in Vol. 4.1, *Tool Operations Manual*. The first action upon entering this tool is to call the available vector outputs, using the "open Output Vector File" icon, then selecting from the choices presented. If more than one are present, choose the one that was assigned in the environment file. The on-screen menu will then disappear, leaving the user to select the "Create Single Vector Panel" icon, which presents the on-screen menu shown in Figure 18. The entries "end-to-end delay (sec.)", "throughput (bits/sec)", and "mean delay (sec.)" are generated directly from the SINK process model. The remaining field, "ring0.fll.mac[0].pks1ze," comes from the Probe Editor. Each may be plotted by selection with the cursor, then dragging the box corners to the desired

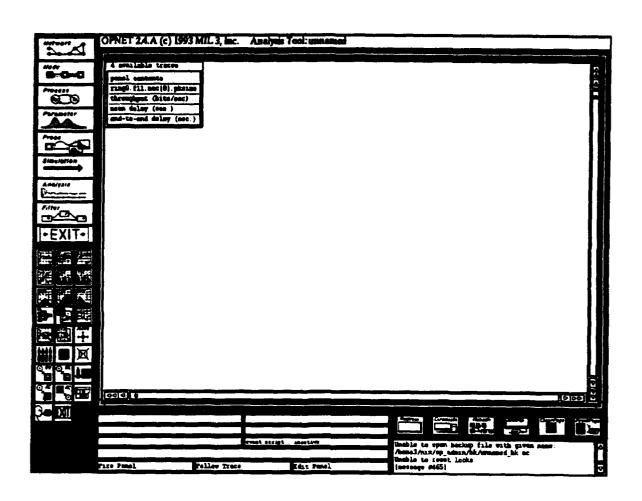


Figure 18. Vector Trace Selection in the Analysis Tool

size. Having placed the panel, the plotted points are fired by clicking the left mouse button, or by placing the cursor over the "Fire All Panels" icon and clicking the same button. Figure 19 shows all four plots generated, placed together on the screen. The "Create Multi-Vector Panel" icon is used to place several plots in the same panel, an operation that is meaningful when the Probe Editor is used to generate comparable outputs. Once the desired plots are on the screen, they may be saved with the "Write Analysis Configuration" icon, which will store the plots in a ".ac" file for later recovery. This is important because the vector file will be written over the next time a simulation is run using the same output vector filename. In addition, the UNIX station's logout sequence may include a <"remove \*.ov"> command to prevent the accumulation of large vector files in memory.

The output scalar file, on the other hand, accumulates steady-state data over several simulations, allowing the generation of plots showing, for example, the effect of various TTRT values on total throughput. The user wishing to create sugar plot should ensure the file is empty of previous data before commencing a series of simulations.

#### 9. Debug Tool

The debug tool may be activated from the environment file. The command  $\langle "help" \rangle$  will list the available commands. The user may step through a simulation one event at a time, or specify stopping points. The  $\langle "fulltrace" \rangle$  command causes every variable to be reported at each event, allowing the user to follow the sequence of events in a

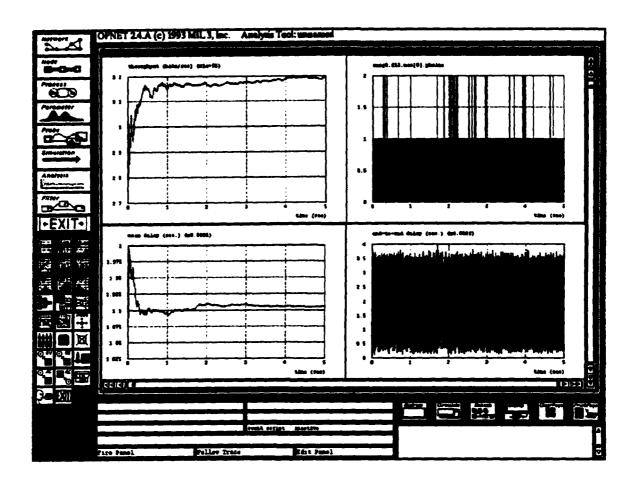


Figure 19. Analysis Tool Display, Four Panels

simulation, and to search for logic errors should failures occur. Appendix D is a short section of the debugger's output when the < "fulltrace"> command is active.

# III. MODEL MODIFICATIONS

# A. OVERVIEW

The FDDI station model provided with OPNET is shown by Schenone (1993) to perform as expected by performance equations provided by research literature, for example. Dykeman and Bux (1988). However, the model as given lacks the flexibility to adequately demonstrate the characteristics and metrics required to formulate recommendations for the development of a CDL network interface. To begin, no way exists in the original model to monitor the throughput and delay statistics of synchronous traffic separately from asynchronous traffic. In addition, the code must be modified to allow the implementation of different asynchronous priority levels, and further altered to allow the generation of statistics of these subcategories. Also desirable is a method to hold traffic in the sink process of one station on the LAN, rather than destroying all packets, so that a bridging protocol may eventually be developed for communication between LANs, which is the ultimate goal of the Common Data Link Project. Finally, a multicast/broadcast facility, by which a packet may be addressed to multiple stations, is needed. Modifications implementing these features were generated for this thesis, and are described in this chapter. Appendices E, F, and G contain the final form of the "C" programs representing the process models "fddi\_mac\_mult", "fddi\_gen\_mult", and "fddi\_sink\_mult", respectively. All contain inserted comments to indicate where changes have been made. In some cases, modifications are extensive

enough that the original structure is not apparent. For these cases the reader who is interested in comparisons is referred to the original code available in the Process Editor.

The modifications described here were suggested in large part from readings in the research literature. In particular, Tari, Schaffer, Poon and Mick (1991) published results generated from another commercially produced network simulation tool, the Block Oriented Network Simulator (BONeS®), demonstrating that increased asynchronous offered traffic load has minimal effect on the throughput of synchronous voice data traffic. At the same time, the throughput of the various asynchronous levels was shown to degrade in order of priority with increasing asynchronous offered load. These findings emphasize the fact that OPNET® has no facility in place providing for the monitoring and plotting of throughput or delay data in respect to class or priority levels.

Closely interrelated are the setting of asynchronous priority levels, a system of subqueues to segregate traffic by priority, and the gathering and display of performance statistics according to priority. In the following discussion, reference is sometimes made from one to the others before all are complete.

# **B. PRIORITIZATION**

# 1. Activating Prioritization in OPNET®'s FDDI Model

As given in the original released model, code exists to support a prioritization scheme, but it is not implemented. The station model includes a priority field that may be set in the Node Editor, but no setting will take effect until the INIT state in the MAC process is

modified. As given, T\_Pr1[1] is simply assigned the value < Fdd1\_T\_Opr> (which is the negotiated value of TTRT, with the negotiation consisting of selection of the lowest requested T\_Req from all stations) for all priority settings 1, resulting in no distinction made between priorities. The state TX\_DATA in the MAC process model contains the code that determines transmission eligibility, then transmits packets if timing conditions are satisfied. The user should notice here that unlike the real FDDI protocol, the Token Holding Timer is incremented from zero to tht\_value (THT), not decremented from THT to zero. This results in a reorientation of T\_Pr1[1] settings, wherein higher settings allow a larger transmission window, and therefore, higher priority. To re-emphasize: in the OPNET® model, T\_Pr1[1] is larger for higher priority stations. Actual settings of T\_Pr1[1] are a matter of user's choice and real-world physical characteristics. One quick approach is to alter the code in INIT from the original:

```
for (i = 0; i < 8; i++)
{
     T_Pri [i] = Fddi_T_Opr;
}</pre>
```

by substituting the text:

```
T_Pri [i] = (double)Fddi_T_Opr/(8-i);
```

to impart some weight to the priority settings (Appendix E, line 250). Note that priority settings in OPNET® are counted from zero to seven, in keeping with the "C" programming

language convention of numbering elements of an N-element array from zero to N-1. As mentioned earlier, actual FDDI convention numbers the priority settings from one to eight.

## 2. Changing the Scheme and the Code

For the purposes of CDL, an ability in a station to generate traffic of differing priorities is a desirable characteristic. The modifications discussed here allow this behavior, though with a certain amount of abstraction included. Essentially, each packet generated in the source process is assigned a priority setting, in a manner that is functionally identical to the determination of the destination address. However, the priority of one packet has no influence on the priority of the next one generated at the same station. Of course in real-world transmissions, packets are grouped into messages, meaning that thousands of consecutive packet arrivals should have the same priority settings to reflect real behavior. In fact, the subqueue structure imparted to the MAC causes outgoing packets to be sent in decreasing order of priority, thereby modeling expected behavior to some small degree. More significantly, the user should keep in mind that the model's purpose is to model a LAN's handling of the traffic it does receive. The fact that packets are transmitted with random priorities in a scattershot fashion is not significant to the LAN's overall performance.

### 3. Subqueues

Because subqueues are essential to the development of the prioritization scheme, their construction is addressed first. As is seen in the FDDI station model in the Node Editor, the MAC node is represented as a queue. Therefore, only a change to "subqueue count" field in the attributes menu is necessary to change the MAC's structure into a bank of subqueues. Code is already in place that treats the MAC as a set of subqueues, although by

default, only one is available at first (this is labelled (subqueue (0)), as is seen when the "subqueues" field is selected from the on-screen attributes menu for the MAC node. Nine subqueues are desired here: one for each asynchronou: priority setting, plus another to handle synchronous traffic. Subqueue indexing corresponds to priority settings, so that subqueue (0) receives and releases the lowest priority asynchronous traffic, while subqueue (7) is assigned the highest priority traffic. Subqueue (8) is designated for synchronous traffic. This segregation of traffic into subqueues is necessary to support the recording of performance statistics and plots of traffic generation through the use of the Probe Editor. Also, while the Kernel Procedures (KP) available to the user include one that allows packets to be removed from the transmission queue in order of priority, rather than in the usual first-in-first-out (FIFO) order, subqueues allow simpler logic (Vols. 5.0 and 5.1 are directories of the commands and functions used by OPNET<sup>®</sup>). Vol. 2.0, Modeling Manual, "Nddef," pp. 27-29, describes the procedure to adjust code so that references to queues may be replaced with references to subqueues, particularly in relation to prioritization schemes. In summary, the subqueues represent a way point for packets. They are created and assigned a priority setting in the source, encapsulated for transmission in the MAC (ENCAP state), and placed in the appropriate subqueue while the station awaits the next token arrival to transmit them.

# a. "RCV\_TK"

In the RCV\_TK (receive token) state, the first test of token usability is a determination of the presence of outbound traffic. The statement:

calls for an inspection of the queue. This statement is replaced with a loop structure that searches all subqueues:

Of course the above requires a declaration of the variable NUM\_PRIOS and the loop counter 1.

## b. "TX DATA"

The TX\_DATA (transmit data) state contains the code that transmits packets while the token remains available, and monitors THT. The THT (tht\_value) is checked inside a loop whose condition is, "while packets remain in the queue, transmit." This loop contains most of the code in TX\_DATA. This condition must be set inside another loop which counts through each desired subqueue, and which must include an additional number of "break loop" points. Accordingly, the code:

is rendered into the following:

```
for (i = NUM_PRIOS - 1; i > -1; i--)
{
    while (op_subq_stat (i, OPC_QSTAT_PKSIZE) > 0.0)
    {
        /* Remove the next frame for transmission.*/
        pkptr = op_subq_pk_remove (0,OPC_QPOS_HEAD);
        ... etc ...
    }
}

(Appendix E, line 920)
```

The transmission loop is broken when any of the following occur:

- No more packets are enqueued.
- In the case of synchronous transmission, insufficient bandwidth remains to complete a transmission. (Note that synchronous traffic is allocated by the user an inviolate amount of time in which to transmit, regardless of the lateness of the token. However, the model checks the bandwidth remaining to ensure a transmission can be completed within the allotted time, and will not commence a transmission that would delay the token. This is in agreement with the actual protocol, and in contrast to the asynchronous case, in which packet transmission may commence while the THT is active, even if the transmission will keep the token past THT expiration.)
- The remaining packets are of too low a priority to be transmitted in the time remaining to THT (T\_Pri[i] < THT).

After closure of the outer loop, the station deregisters its interest in the token by indicating it has no more traffic to send. This information is used by the token

acceleration mechanism, which will destroy the token for the time period no station has traffic to transmit, then recreate it when needed, thereby significantly reducing the number of ever.ts and the amount of time spent in a simulation. The original code appears at the bottom of the TX\_DATA state:

```
if (tk_ .gistered && op_q_stat (OPC_QSTAT_PKSIZE) == 0.0)
{
    tk_ :gistered = 0:
    fddi_tk_deregister ();
}
```

As before, this must be altered to search through a set of subqueues first before deciding no traffic remains to be sent:

```
q_check = 1;
for (i = NUM_PRIOS - 1; i < -1; i--)
{
    if (op_subq_stat (i, OPC_QSTAT_PKSIZE) == 0.0)
    {
        q_check = 0;
    }
    else
    {
        q_check = 1;
        break;
    }
}
if (tk_registered && q_check == 0)
{
    tk_registered = 0;
    fddi_tk_deregister ();
}

(Appendix E, line 1084)</pre>
```

#### 4. Modifications to Prioritization

In order to enact the priority scheme described above, changes are needed in the station model (Node Editor), all three process models (Process Editor), packet format (Parameter Editor), and to the Environment file (UNIX text editor).

## a. Station Model Changes

For the modified priority scheme, new attributes are needed in the on-screen menus that appear for the mac node of the fddi\_station model in the Node Editor. The original field "priority" is left in place, but not used. Note that the "super priority" field (described in Vol. 6.0, External Interfaces Manual, "Rel," p. 15) is not related to the FDDI protocol, but is a tool for scheduling of events in the simulation; it is not used, and should be left disabled.

In order to support the priority setting protocol that occurs in the source model (described later), two new attributes are created: "high pkt priority" and "low pkt priority." The new fields are created as listed in the following steps:

- 1. Call the on-screen attributes menu for the "mac" node.
- 2. Place the cursor over the "extended attrs" field, and press the left mouse button to call the submenu (the right mouse button dismisses the attributes menu; try again).
- 3. Assign the fields as shown in Figure 20: names will be "low pkt priority" and "high pkt priority", units are <none>, type is <integer> (selected from another on-screen menu that will appear,

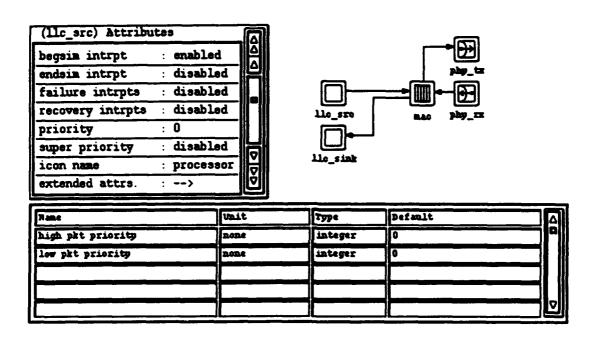


Figure 20. Adding Extended Attributes to the Station Model

rather than typed in), and defaults are at the user's discretion. Zero for both are reasonable. These assignments are preserved with the keyboard combination <CTRL+5>. Further, the model must be saved using the "Write Node Model" icon, as described in Vol. 4.0, Tool Operations Manual, "Nd," pp.13-15.

4. Once the model is saved, then called again to the Node Editor, unexpected values will probably appear in the fields corresponding to the new attributes (which will now appear in the primary on-screen menu as well as in the "extended attrs" submenu). These values should be set to promoted> so that they may be assigned in the Environment file. To set the newly created field to promoted>,
place the cursor over the field, then type CNTL+0> at the keyboard, invoking the literal "promoted."

This last item is not described in the manuals; it was obtained from MIL 3, Inc.'s technical support via electronic mail.

## b. Environment File Changes

Corresponding to the attributes created and then assigned promoted>
fields above, the following code is added to the Environment file:

```
"*.*.llc_src.high pkt priority" : 7
"*.*.llc_src.low pkt priority" : 0
```

The quotation marks are required here because of the spaces in the attribute names. Had low\_pkt\_priority been used instead, then the quotation marks would have been omitted. Examples of both styles appear in Appendix H, an example Environment file that includes all

attributes added to the model (some of which are still to be described). The Environment file may be modified in the UNIX text editor. Pound signs (#) indicate comments. Order of attributes is not significant, and any attributes not used by a model are simply ignored.

### c. Source Modifications

The approach to assigning a random priority to each packet as its arrival is generated is functionally identical to the procedure by which the destination address is generated. The OPNET kernel function op\_ima\_obj\_attr\_get() is used to call attributes from the node model or from the Environment file. The KP op\_dist\_load() is used to load a distribution to be used in generating a stream of stochastic values. These are used in the source process model "fddi\_gen" INIT state in the following manner:

(Appendix F, line 108)

In the preceding, the first two lines call the desired attributes from the Environment file to the calling station ("my\_1d"). The second field in the procedure call is taken verbatim from the Environment file, while the third field is the address of the attribute. The address may have any name;  $\langle \&high\_pkt\_priority \rangle$  is purely a memory aid for the user, and is not required by syntax to resemble the field name to which it is assigned. The value returned by op\_dist\_load() is used in the ARRIVAL state to finally generate the priority setting with the statement:

which finally returns an integer between the values set in the Environment file. This integer is assigned to the packet with the commands:

```
op_pk_nfd_set (pkptr, "pri", pkt_prio);
op_ici_attr_set (mac_iciptr, "pri", pkt_prio);

(Appendix F, line 214)
```

Where only one priority setting is desired for a particular station, the attributes need only be both set to that desired value, with the station specified in the Environment file. A limitation here is similar to that of addressing: any section of values from zero to seven may be chosen, but it is not possible, for example, to assign a station the asynchronous priority settings two, four, and six.

In the SV (State Variable) edit window, high\_pkt\_priority and low\_pkt\_priority are declared as integers, and pkt\_priority\_ptr is declared as a pointer of type "Distribution." Because OPNET uses a form of proto-C, the declarations made in the editor actually have the following form:

```
Distribution* \pkt_priority_ptr:
int \high_pkt_priority:
int \low_pkt_priority:
```

(Appendix F, line 9)

When the code is compiled and the ".C" icon is invoked to present the entire process model code, the above will have the following appearance:

```
Distribution* sv_pkt_priority_ptr;
int sv_high_pkt_priority;
int sv_low_pkt_priority;
```

OPNET<sup>®</sup> will also produce the following in the ".C" code:

(Appendix F, line 44)

The above commands have the effect of choosing, on a uniform distribution, a priority setting from a given range whose endpoints are retrieved from the Environment file

#### d. MAC Modifications

Because a priority scheme is already supported in the original model, little change is required in the mac node once a priority value is assigned in the source node. The user should note that communication between nodes is conducted with locally held variables; globals are avoided. This may result in different declared variable names for the same data.

which is acceptable. Thus, the pri of the source becomes the req\_pri of the mac node. In the MAC node, priority settings are used as the indexes for the subqueues. NUM\_PRIOS is declared for use as a loop counter.

# e. Sink Node Modifications

The changes to the "fdd1\_sink" process are nearly all related to the generation of performance parameters, which are discussed in the next section. In the original model, the received packet's priority setting is not even relayed to the sink, since the only information necessary to the calculation of overall throughput and delay statistics are the packet's creation time and its time of receipt. A fundamental addition to the code in the DISCARD state is the line:

(Appendix F, line 78)

which recovers the priority from the field "pri" in the frame structure fddi\_llc\_fr. With this information, additional modifications will bring about the ability to create throughput and delay information for each asynchronous priority setting, and also to separate synchronous traffic statistics from asynchronous. But before any of this will work, the fddi\_llc\_fr packet structure must be modified.

#### f. Packet Format

The frame that is created in the source and passed to the MAC, then encapsulated into a more extensive frame, then ultimately passed from the destination station's MAC to the sink process for accounting and final destruction, is of the format fddi\_llc\_fr.

To support the prioritization scheme, the format needs to include more information than only the frame's creation time. To enhance its characteristics, the Parameter Editor's "Packet Format" icon is invoked, and the format fddi\_llc\_fr is called. Another line is added, as shown in Figure 21, making "priority" an attribute of the packet. Type is set to "integer," size can be "0," default value is "0," and default set is "unset." The changes are saved with the "Write Model" icon.

# C. PERFORMANCE MEASURES

#### 1. Overview

OPNET soriginal FDDI LAN model provides no ready way to monitor synchronous traffic separately from asynchronous, and no way to monitor the throughput and delay statistics for individual asynchronous levels. The inability of OPNET soriginal FDDI model to provide anything besides overall performance is a serious limitation to its usefulness in the CDL project. A major goal of this work is to augment the code in the sink process so that additional output vectors are generated, allowing the effects on individual class and priority levels to be seen. For example, in the original model, the user may assign any desired proportion of the generated traffic to be synchronous, but the original model has no facility to measure the synchronous traffic alone. The following paragraphs describe the modifications made to allow the display of statistics segregated by class and priority.

As an incidental note, the user should realize there is no particular significance to the sequence in which the states appear in the ".C" file. That is, the order DISCARD.

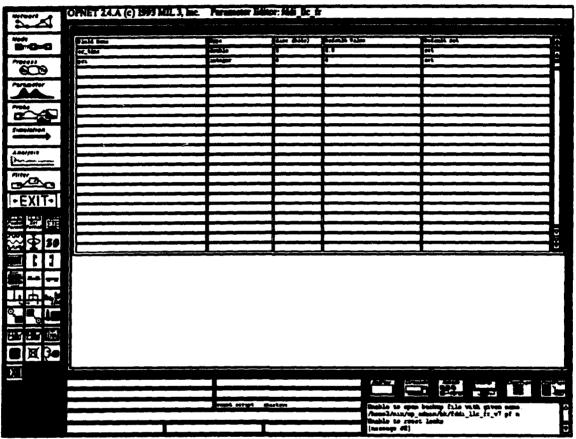


Figure 21. Adding a New Field to the "fddi\_llc\_fr" Frame Format

STATS, then INIT that appears in the "fdd1\_s1nk" process code is no indication of the sequence in which the simulation "visits" these states for each station. In fact, the more logical order, INIT, DISCARD, then STATS will be followed in this discussion, though preceded by a discussion of the variables needed. Appendix G is the file "fdd1\_s1nk\_mult.pr.c", containing the modifications described here.

## 2. Variables

There are essentially four primary variables of interest in the sink process model: fddi\_sink\_accum\_delay, fddi\_sink\_total\_pkts, fddi\_sink\_total\_bits, and fddi\_sink\_peak\_delay. These exist as single integers or as floating point numbers, and are incremented or recomputed as packets are received by the station. The overall idea is to expand these into vector arrays, in which each element represents a running total for one priority setting, with the last element representing synchronous traffic totals. As mentioned earlier, this approach requires the "fdd1\_mac\_fr" format in the Parameter Editor to be modified to include the priority as a field, since the original model needs only the packets' creation time and time of arrival in order to compute the overall throughput, mean delay, and end-to-end delay. However, as was discovered through trial and error, while the given variables can be changed to vector arrays, and the model can be modified to accommodate the new structure, and the code will compile (if the syntax is correct), any attempted simulation will abort with a segmentation violation error. This is because the "C" programs, which may be modified by the user, must interface with OPNET<sup>®</sup>'s kernel procedures, which are beyond the user's access. Ultimately, the given variables must be kept and new ones created. Since the overall performance remains a useful statistic, the variables mentioned are left in place, while new ones are declared with the desired vector structure. These are fddi\_sink\_accum\_delay\_a, fddi\_sink\_total\_pkts\_a, fddi\_sink\_total\_bits\_a, and fddi\_sink\_peak\_delay\_a, which are declared and initialized in the Header Block.

The following declarations are added to the State Variables section:

```
Gshandle \thru_gshandle_a[10];
Gshandle \m_delay_gshandle_a[10];
Gshandle \ete_delay_gshandle_a[9];
```

Once compiled, the following appear in the ".C" file:

```
Gshandle sv_thru_gshandle_a[10];
Gshandle sv_m_delay_gshandle_a[10];
Gshandle sv_ete_delay_gshandle_a[9];
#define thru_gshandle_a pr_state_ptr->sv_thru_gshandle_a
#define m_delay_gshandle_a
pr_state_ptr->sv_m_delay_gshandle_a
#define ete_delay_gshandle_a pr_state_ptr-
>sv_ete_delay_gshandle_a
```

(Appendix G, line 43)

With these declarations in place, the modifications to the rest of the code are straightforward, and generally follow the examples set by the original code. In addition, the variables Offered\_Load and Asynch\_Offered\_Load are declared for use in generating scalar plots over a series of simulations. These are assigned values called from the Environment file by the state STATS.

### 3. Initialization State

The primary purpose of the Initialization State is to assign handles to the global statistics that are generated at the end of the simulation. The "KP" statement op\_stat\_global\_reg (<gstat\_name>) returns a handle used to reference a globally accessible statistic. This handle is needed to furnish new values (as they arrive with new packets) for the "KP" op\_stat\_global\_write(), which appears in the DISCARD state. The field entry <gstat\_name> is the text in the on-screen menu that appears in the Analysis Tool when the Create Single Vector Panel or "Create Multi Vector Panel" icons are invoked. The code added is very similar to what is already in place. For example:

(Appendix G, line 325)

creates a handle for the collection of data for priority 1 level throughput, and creates a field which will appear in the on-screen menu in the Analysis Tool. In another example:

(Appendix G, line 364)

creates a handle for total asynchronous mean delay, and generates another field which will appear in the on-screen menu in the Analysis Tool. Each priority level has its corresponding handle assignment line. The actual statistics to accompany these handles are generated in the DISCARD state. As seen in the code itself (Appendix G), each element has a handle assignment line, for throughput, mean delay, and end-to-end delay. Figure 22 shows off the

resulting on-screen menu from the "Create Single Vector Panel" icon, reflecting the new data that may be plotted. Note that the code here reconciles "C" language vector numbering conventions with real-world priority level settings.

## 4. "DISCARD" State

The DISCARD state is where the received packet is "opened" and statistics generated from the packet contents. As mentioned before, "KP" op\_stat\_global\_write (<gstat\_handle>, <value>) is the statement that accumulates data. The <value> field may be a previously computed figure, or may be calculated within the "KP". The <gstat\_handle> field is the same used in the INIT state. DISCARD uses the priority value found with the arriving packet as the index for the vector structure. For example:

```
op_stat_global_write (thru_gshandle_a[5],
fddi_sink_total_bits_a[5] / op_sim_time());
```

(Appendix G, line 128)

generates a current throughput figure for asynchronous priority level six (recall the necessary offset for vector element numbering convention) Also necessary is the recording of delay values for each priority, which is done with the following:

(Appendix G, line 182)

This state also destroys the packet once its contents are recorded. This is necessary to prevent the simulation from filling the host computer's memory with dead packets.

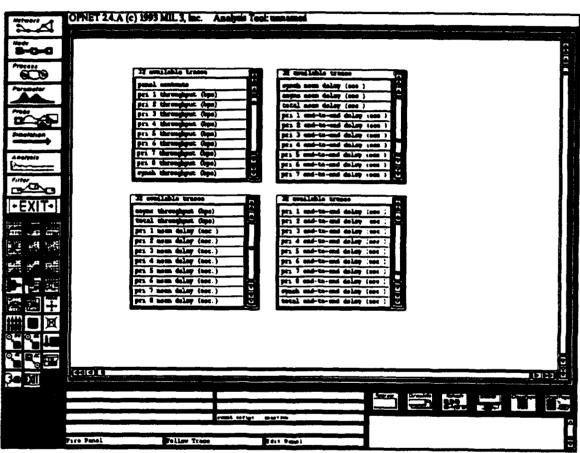


Figure 22. Newly Created Vector Traces Available

## 5. "STATS" State

The STATS state produces the steady-state scalar data that may plotted using the "Create Scalar Panel" icon. These are saved, rather than written over, so that the user may observe changes to output as the input is varied. For example, the throughput of synchronous traffic over several simulations as different TTRT values are used. From these, a plot of throughput vs. TTRT may be generated. The statement:

is similar to the "write" command described before, and writes a scalar steady-state statistic in this case. The field "scstat" appears in the on-screen menu called with the "Create Scalar Panel" icon. Examples of the use of this statement appear in Appendix G, lines 210-226.

Another on-screen menu line item is drawn from the Environment file. The values <total\_offered\_load> and <asynch\_offered\_load> are placed and assigned in the Environment file (see Appendix H), as described earlier. These correspond to the

variables Asynch\_Offered\_Load and Offered\_Load declared in the header block. These are joined by the commands:

```
op_ima_sim_attr_get (OPC_IMA_DOUBLE,
    "total_offered_load", &Offered_Load);
op_ima_sim_attr_get (OPC_IMA_DOUBLE,
    "asynch_offered_load", &Asynch_Offered_Load);

(Appendix G, line 299)
```

and added to the on screen menu with the commands:

This code in Appendix G contains a warning to the user that the offered load settings are not automatically updated in any way. If the user desires to plot throughout or delay as a function of offered load over a series of simulations, then the user must remember to keep the offered load assignments current in the Environment file for each simulation.

# D. BRIDGE LINK

The alteration described here represents a simple first step toward a network interface. Instead of destroying frames after they are received, one station on the LAN is modified to hold its packets in subqueues. Further development will bring about a protocol for removing these buffered packets from the original LAN and transferring them to another.

## 1. Station Model Modifications

The received frames are stored in subqueues according to their priority in a manner analogous to that already described for the mac node. This requires that the sink node, llc\_sink, be changed from its original processor form into a queue node. To affect this modification to the station model, the following steps are followed:

- The sink node is selected by placing the cursor over it and clicking the left mouse button.
- 2. The node is removed by invoking the "Cut" icon. The "Packet Stream" between the sink and the mac also disappears.
- 3. The "Create Queue" icon is selected, and the resulting box is dragged to the location just vacated. Clicking the left mouse button places the new node. The station now has a queue node rather than a processor node.
- 4. The on-screen attributes menus is called with the right mouse button, and the fields are all set to the same values that were in effect before, including the node name.
- 5. The "process model" field will be set to the newly modified sink process model. If the process has not yet been modified, then the original assignment may be used, then changed when the process has been modified and saved under a new filename.
- 6. The "subqueues" field is set to <9>, accommodating eight levels of asynchronous traffic and also synchronous traffic.

- 7. The packet stream line between the sink and the mac nodes must be replaced.
- 8. The new station model is saved under a new filename, for example, < "fddi\_sink\_link">.

Figure 23 shows the resulting station model, with the appropriate on-screen attributes menu.

## 2. Process Model Modifications

The changes to the process model code are few, and are included in Appendix G as inactive code ("commented out"). The code that destroys received packets:

(Appendix G, line 98)

is replaced by code that enqueues the packets according to their priority settings:

The user should realize that for the moment, no more code exists for the disposition of these enqueued packets; a long simulation simply accumulates packets and fills computer memory. The subqueues are infinite by default, but may be limited (using another on-screen menu) to demonstrate overflows. In that case, there is no code for the disposition of packets that are lost through buffer overflow, and these will simply accumulate in the host computer's memory as well. In sum, the user must be aware of the memory demands of OPNET® simulations.

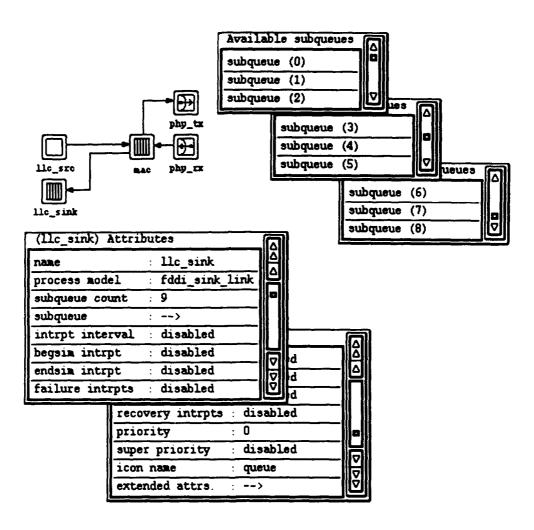


Figure 23. Modified Station Model, "fddi\_sink\_link"

# E. MULTICAST

#### Overview

Multicast is the addressing of a packet to more than one station. Broadcast is a special case with the transmission of the packet to all stations. In the original model, a station desiring to send the same message to all stations would repeat the packet transmission for each destination station. Actually, this last is an abstraction; OPNET<sup>®</sup> simply generates packets from each station addressed to randomly generated destinations, with no indication that any particular transmission represents a copy of any previous transmission. However, the fact remains that each packet is addressed to only one station. In the actual FDDI protocol, each packet is passed from station to station until it reaches its destination, but then continues past its destination until it is finally removed from the ring by its originating station. The OPNET® simulation economizes on the number of simulation events (and therefore the simulation time) by having the destination stations remove the packets they receive. This occurs in the mac state FR\_REPEAT, which also contains comments suggesting that the user may wish to overrule this economizing feature in the event that group addressing is desired. Figure 24 is the State Transition Diagram for the mac process, repeated from Figure 6 for the reader's reference. The state FR\_STRIP includes the code by which an originating station removes packets that have completed a circuit of the LAN. It is not used in the original code, nor will it be used in the modifications described here.

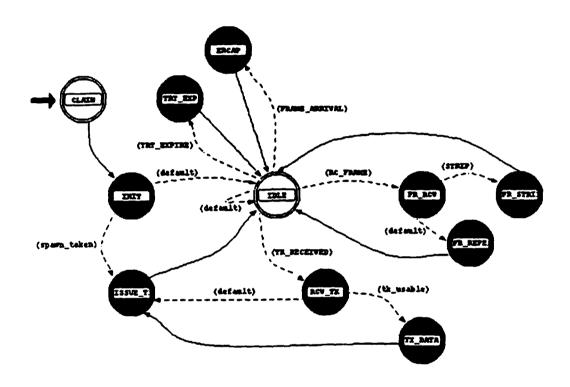


Figure 24. MAC Process Model State Transition Diagram

The basic idea is as follows: rather than carry a destination address, each packet carries an array with a number of elements equal to the number of stations on the ring. These elements are simply ones and zeros, with a one indicating by its location that the packet is addressed to a station corresponding to that location. For example, in a five station LAN, the address field [0 1 0 1 1] would indicate the packet is addressed to stations one, three, and four (as in the case of indexing vector array elements, the stations on an N-station LAN in OPNET<sup>®</sup> are numbered from zero to N-1). As the packet is passed around the LAN, each station inspects this array, passing the packet on if the station is not designated a destination address. Destination addresses keep a copy of the packet's information, set their place in the address field to zero, then pass the packet on to the next station. The last destination address will destroy the packet after verification that only zeros remain in the destination address array, thereby preserving some of the economy gained in minimizing the number of events in the simulation. While the destination address array would need to be transported with each packet, OPNET<sup>©</sup>'s Kernel Procedures can only accommodate a pointer to the array. The following sections describe the changes necessary to effect multiple addressing. The reader is again referred to Appendices E, F and G, containing the ".C" files for the MAC, source, and sink process models, respectively. However, the implementation of multicasting involves no changes to the sink process model.

#### 2. Environment File

Each station is assigned a maximum and a minimum number of possible destination addresses to use in addressing each packet. The following are added to the Environment file:

These will be called by the source process model's INIT state. Appendix H contains an example Environment file including these new attributes. The minimum number must be at least one, and the maximum should be no greater than N-1, where N is the number of stations (the logic written in the source model's code does not allow stations to address packets to themselves). If it is desired that some station generate no traffic, then the "arrival rate" field should be set to zero. Setting "min num addees" and "max num addees" to zero will only result in packets transmitted with no destination addresses assigned. Setting "max num addees" to a number greater than N-1 will cause an endless loop in the code that generates destination address assignments in the source process (the logic in the loop is, "assign x different destinations, but do not repeat any.").

## 3. Station Model

The additions to the environment file must be added to the station model in the Node Editor, in a manner analogous to the method described in the discussion of prioritization (III.B.4.a. Station Model Changes). The steps required to add another attribute to the on-screen menu are not recounted here, but the desired final result is shown in

Figure 25. It is a good practice to save the changed station model under a new name, for example < "fddi\_sta\_mult">, until the user is certain that the modifications do more good than harm to the original model.

## 4. Source Process Model

A real packet on a real LAN would necessarily be self- contained, carrying with it all its destination addresses. However, the functions used to assign the packet address field in OPNET<sup>®</sup> will not support a vector structure, and so pointers to memory locations must be used, with these memory locations containing the destination addresses. The modifications to the source process to effect multiple addressing are summarized in the following sections. As is the case with the station node model, each of the process models should be saved under new names, for example fddi\_gen\_mult, as a matter of practice.

#### a. Variables

To begin, a global variable, NUM\_STATIONS, is defined in the Header Block (it is also defined in the mac process model), to be used as a loop counter. This variable must be kept updated to accurately reflect the correct number of stations on the FDDI LAN. This is easily forgotten when different LANs are created, using the same station model with different numbers of stations.

As was mentioned, the destination address field, originally a single integer value, must now be made a pointer to an array of integers. Although "C" programming language treats the name of a vector array as the array's pointer, the established Kernel Procedures do not support the simple change of syntax. In short, a new variable, \*da\_ptr (destination address pointer), is declared as an integer pointer in the Temporary Variables

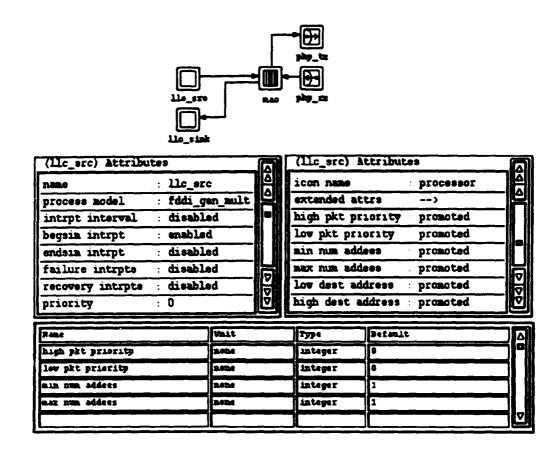


Figure 25. Multicast-Capable FDDI Station with Attributes Menus

(TV) editor. The original variable, dest\_addr, is removed from the TV section, and declared with the State Variables (SV) as an array of integers, with dimensions [NUM\_STATIONS + 1]. In addition, variables to accompany the Environment file attributes are declared with the State Variables: min\_num\_addees and max\_num\_addees. The resemblance between these variables and the attributes they accompany is meaningless in respect to "C" language syntax, but is of course a useful memory aid to the user. The pointer num\_addees\_dist\_ptr represents the value used in determining stochastic values. Also, the integer num\_addees is declared in the State Variables, to represent the number of stations to which a given packet will be assigned. It is used as a loop counter, and will be different for each packet.

#### b. Initialization State

In the source model's Initialization state, the range bounding the number of destination addresses is determined with a call to the Environment file:

(Appendix F, line 86)

These result in the assignment of the values from the Environment file to the addresses of the corresponding variables. A distribution is established with the following:

(Appendix F, line 114)

This value is used to generate streams of stochastic values, and is used in the ARRIVAL state.

#### c. "ARRIVAL" State

In the ARRIVAL state, the actual number of stations to receive the new packet is determined, and then a loop is used to choose these stations one at a time. Each loop iteration is very similar to the original procedure that was in place when only one station was assigned to each packet. The loop contains a provision to prevent the repeated assignment of the same station, and also to prevent the assignment of the originating station as a destination address. The following statement determines the number of destination addresses for a given packet

The following loop is used to find and assign the chosen stations:

```
for (i = num_addees; i > 0; i--)
{
    gen_packet:
    nix = op_dist_outcome (dest_dist_ptr);
    if (dest_addr[nix] == 1 || nix == station_addr)
    {
        goto gen_packet;
    }
    dest_addr[nix] = 1;
}

(Appendix F, line 174)
```

This loop continues to iterate until the specified number of stations, without repetition, is assigned.

Recall that the destination address array is declared with one element more than the number of stations in the LAN (dest\_addr [NUM\_STATIONS + 1]). Here, all the elements in the destination array must be shifted one space to the right, and a simple loop is used to set dest\_addr[i+1] equal to dest\_addr[i], for i iterations. This step is necessary because the first array element will be overwritten with the array's memory address in the course of the packet's travels, as it is transmitted from one station, received by the next, and its destination address field is opened, inspected, then closed by each station in turn. This behavior is verified by use of the debug tool set to "fulltrace." accompanied by strategically placed printf statements. The author does not pretend to know why this happens. The array element shift is a deft enough way to sidestep the problem. However, all references in other states to the destination address array must be offset to accommodate this shift.

#### 5. MAC Process Model

The MAC process model receives each packet, inspects it, the decides whether or not the packet is addressed to the station. In the original model, the packet is removed from the ring by the destination station, and relayed by other stations. With multicasting of packets, a third case arises, in which a station receives a packet addressed to it, but must also pass the packet on to other destination stations. A number of print commands are placed in the code, but left inactive. They are of much use in the verification of the model's operation.

#### a. Variables

The same variable NUM\_STATIONS used in the source model is also defined in the Header Block of the MAC. The user must remember to keep this value updated in both places when the same station model is used in a different size LAN. The destination address is changed from an integer into an open-ended array of integers, dest\_addr[], in the Temporary Variables editor. An integer pointer, \*da\_ptr, is declared as well.

# b. Encapsulation State

The Encapsulation state receives frames from the source process, and place them inside the format fddi\_mac\_fr for transmission on the LAN. An intermediate step is to inspect the frame received from the source for its destination address, which must be written into the encapsulating frame's destination address field as well. The original statement:

op\_ici\_attr\_get (ici\_ptr, "dest\_addr", &dest\_addr);

is unworkable with dest\_addr in array form, which is why the pointer \*da\_ptr is declared.

Instead, the following is used:

followed by a loop assigning each element in the array a value from the corresponding element in the array found at address da\_ptr. Vol. 5.0, Simulation Kernel Manual, discusses this command statement. This loop uses as a counter the value NUM\_STATIONS+1, for the reason mentioned earlier: use of a printf statement would reveal that the first element in the array has been written over and replaced with a number representing the memory address of the array. Fortunately, the entire original array of zeros and ones has been shifted, so the first element is intact. Correspondingly, all references to the array from within the MAC process must be made with respect to this shift.

# c. Frame Repeat State

The Frame Repeat state inspects each received packet and acts on one of three cases: the packet is addressed only to this station, or the packet is not addressed to this station at all, or the packet is addressed to this station and to other stations as well. The first two cases are already present in the original model, and require some simple modifications. The third case represents a significant change, requiring the addition of an entire block of code to the state, in which the packet information is copied first, then passed on to the next station.

The first statement in the FR\_REPEAT state opens the arriving packet's address field for inspection:

(Appendix E, line 604)

Here, &da\_ptr has been substituted for the original &dest\_addr. As is the case in the ENCAP state, the arriving pointer is used to initialize the locally held destination address array:

(Appendix E, line 610)

This destination address array is then passed through a loop to determine if it has more than one destination address, and to see if the element corresponding to this station is set to one, indicating the packet is addressed to this station. If the packet proves to be addressed to this station only, then the actions of the original code are carried out: relevant fields are copied to an ICI packet format for transmission to the sink process, then the packet is destroyed. If inspection of the destination address array shows the packet is not addressed to this station at all, the original code is again sufficient to place the packet back on the ring.

The third case represents a new situation. When the packet is addressed to this station and to others as well, the information must be saved here, and be transmitted onto the ring again. These actions are carried out with commands borrowed from the first two cases, and include some new considerations as well. The function "op\_pk\_nfd\_get()"

is used to retrieve data from specified fields in the packet. That is, it is a decapsulation function. When the data happens to be in a structure format, the function has the effect of destroying the information. This is an important point because the information must be preserved for retransmission. Therefore the function:

must be followed at some point with the statement:

in which info\_ptr has been declared in the Temporary Variables to have the same type as the structure in the "info" field. When this information is summoned for re-encapsulation with the function:

(Appendix E, line 747)

"pri", do not require this procedure, since they are not lost with decapsulation. Two packets result: one is an ICI frame carrying the received information to the sink process, and the other is a re-encapsulated packet sent to the LAN. The station's last action before

re-encapsulating the destination address array is to zero its corresponding address element.

The last station to receive the packet will destroy it, upon determining that only zeros remain in the destination address array (Appendix E, lines 644 and 692).

## 6. Limitation

The primary limitation of the model with multicasting active is in the generation of throughput and delay statistics. As the code currently stands, each packet is counted by every station that receives it, leading to multiplication of throughput data. On the other hand, some use may possibly be made of this characteristic by comparing the tallied throughput against the actual offered load, as a measure of the effectiveness of the multicasting scheme. For example, a measured throughput of 100 Mbps versus a known offered load of 50 Mbps could indicate that multicasting effectively generates 50 Mbps without physically taxing the bandwidth capability of the FDDI LAN, since no additional packets are generated.

# IV. MODEL TESTING

This chapter provides several test results intended to verify the enhanced capabilities added to the basic FDDI station model in OPNET. In addition to improving the performance and display features, the modifications must preserve the basic behavior to be useful. The tests presented here include an illustration of the model's treatment of synchronous and priority-based asynchronous traffic, a simple demonstration that the modified sink process does indeed store the traffic it receives, a check against theoretical performance equations, and a demonstration on the monitoring of multicast.

Not all of the modifications described in this thesis are incorporated simultaneously in all station models. The State Transition Diagram correction to the original fdd' sink process model is of course installed in all models that use the llc\_sink node. Likewise, the patch for OPBUG is installed for all MAC model versions. The generation of randomly differing priority assignments for all packets is closely interrelated to the generation of output statistics segregated by class and priority; both involve extensive modifications to the original process models for the source, sink and MAC, which are stored as fddi\_gen, fddi\_mac, and fddi\_sink. (The actual file names found in the UNIX subdirectory have the suffix ".pr.m." and when these are compiled, corresponding files with the suffix ".pr.c" are created.) The original models of those names are retired under the ending "\_orig.pr.m." Likewise, the original node-level station is stored under the name fddi\_sta\_orig, while fddi\_station is the upgraded

version. The multicasting capability is stored in a separate station model, fddf\_sta\_mult, which includes the processes fddf\_mac\_mult, fddf\_gen\_mult, and fddf\_sink\_mult, and contains all of the other modifications as well. The multicast capability was the last feature installed for this thesis, and has not been completely developed.

# A. SYNCHRONOUS THROUGHPUT

#### 1. Overview

This test was motivated by an earlier study, in which a software simulation tool was used to demonstrate the effect of increasing offered load on the asynchronous and synchronous throughput of an FDDI LAN. Using the Block Oriented Network Simulator (BONeS<sup>®</sup>), Tari et. al. were able to show that synchronous throughput does not degrade appreciably, even when the offered load is well in excess of 100 Mbps. They were able further to show the decay in throughput suffered in the asynchronous priority levels as offered load is increased. Figure 26 is taken from this study. In addition to providing an idea of expected performance, this figure also highlights the fact that in its original form, OPNET<sup>®</sup>s FDA model provides no method to display any throughput data other than the overall performance parameters, total throughput, total mean delay, and total end-to-end delay.

## 2. Setup

The set up was intended to imitate the older experiment as closely as possible.

Ten simulations were conducted using the same 50-station LAN. Ten stations (f0, f5, f10, f15, f20, f25, f30, f35, f40, f45) were designated "constant" generators.

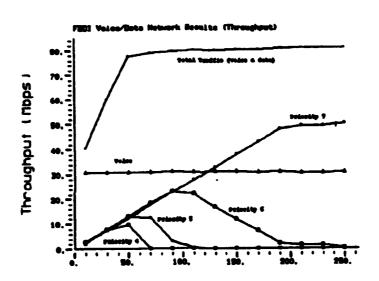


Figure 26. Throughput Measurements Using BONeS® (Tari, et. al., 1988, p. 58)

That is, they were modified inside the source process "C" code to generate packets at a constant rate, rather than at an exponentially distributed approximation of the rate specified in the Environment file. These ten stations transmitted synchronous traffic only, while the other 40 stations were allocated no synchronous bandwidth, sending asynchronous traffic only. The synchronous stations generated 512-bit packets at a constant arrival rate of 6000 packets per second. The asynchronous stations transmitted 1000-bit packets, at a rate stepped so as to increase the total offered load by 10 Mbps in each simulation. Therefore, total offered load ranged from 40.72 Mbps to 140.72 Mbps over ten tests, with steady state data recorded in a data file of scalar data. TTRT was set to 10.7 ms, in order to support the delay of 50 stations on a 50 km ring while maintaining ∑SA<sub>i</sub> ≥ 10 ms. This differed from the older study, for which a TTRT value of 8.0 ms was used, despite the authors' stated intention to model voice network traffic. As noted by Powers (1993, p.340), synchronous voice transmission requires that the token visit each voice transmitter every 20 milliseconds. resulting in  $\sum SA_i = 10$  ms. This in turn requires TTRT to exceed 10 ms by an amount sufficient to account for physical delays inherent to the ring fiber and the stations. Asynchronous priority threshold levels. T\_Pri[i], in the INIT state of the MAC process code were set in increments of 0.125 TTRT, so that Priority 1 traffic transmission was cut off when THT incremented to 1.3375 ms, and Priority 8 traffic could be sent for the entire THT period. Recall that this incrementing THT is a function of OPNET, reversed from the decrementing timer described in most literature.

### 3. Results

Figure 27 shows a plot of total throughput over a series of ten simulations in OPNET, demonstrating a roughly linear rise until the offered load begins to exceed 90 Mbps, which is in qualitative agreement with the older experiment, and agrees with results published by Dykeman and Bux (1988, pp. 1003-1007). This plot also serves to show off one of the model's improvements in data display, allowing "Offered Load" to be an abscissa for scalar plots.

Figure 28 illustrates the fact that synchronous bandwidth allocation is not affected by the asynchronous offered load; therefore, synchronous throughput remains nearly constant as the offered load is increased.

Figures 29-31 illustrate the effect of increasing offered load on the throughputs of asynchronous traffic at priority levels two, three, and four, respectively. No priority settings above four suffered any degradation within the range of offered load observed in this test. The decay of Priority 3 traffic at approximately 100 Mbps of offered load suggests that (3/6)TTRT may be a guiding point for setting priority thresholds that will take effect before the LAN's capacity is reached.

# **B. PRELIMINARY LINKING MODEL**

#### 1. Overview

The modifications made to the sink process as a step toward a bridging node are the simplest of all described in this thesis, and the testing of the finished model is also simple.

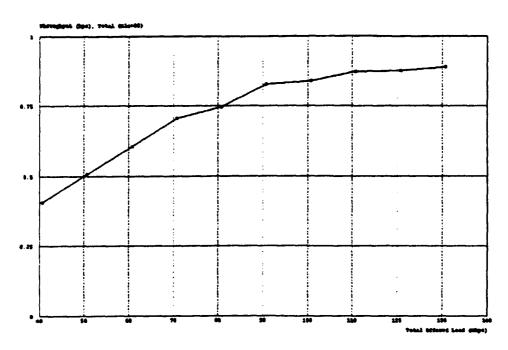


Figure 27. Total Throughput vs. Total Offered Load

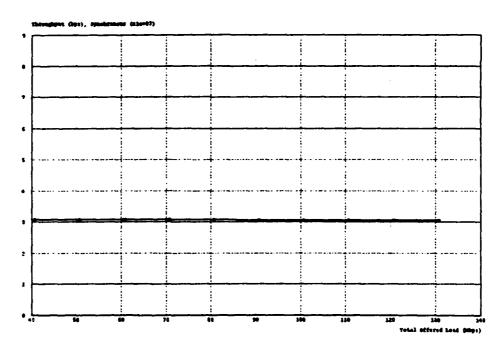


Figure 28. Synchronous Throughput vs. Total Offered Load

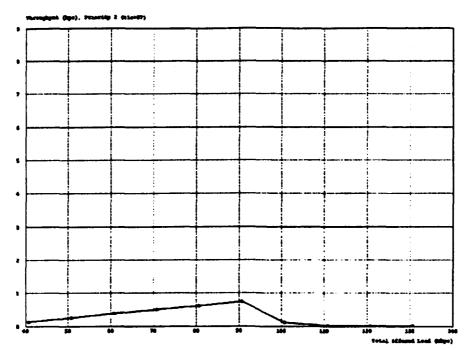


Figure 29. Priority Two Throughput vs. Total Offered Load

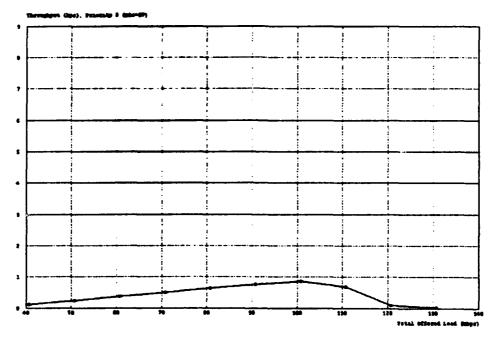


Figure 30. Priority Three Throughput vs. Total Offered Load

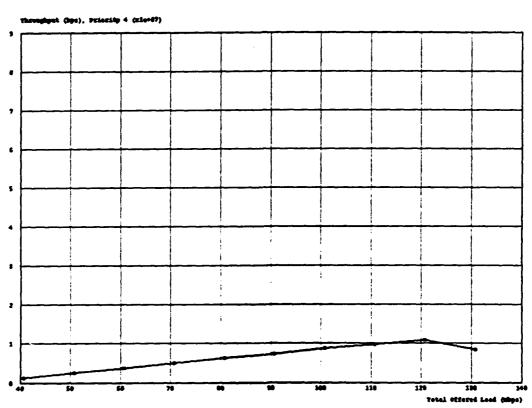


Figure 31. Priority Four Throughput vs. Total Offered Load

It is necessary only to show that all the traffic transmitted to the station is received and held. To demonstrate this action, a ten-station LAN was created, with station f9 designated the link node. The Environment file assignments were made so that all stations directed all traffic to station f9, which itself was not transmitting. A modest offered load was used to prevent an overload of packets in the simulation host computer's memory. All transmitting stations were assigned the same full range of asynchronous priority settings, and were assigned equal portions of synchronous bandwidth. Each of the buffers at station f9 should have then been seen to receive packets pre-sorted for eventual transmission. The Probe Editor was used to monitor "pks1ze," the number of packets in each subqueue of the modified sink process model.

# 2. Setup

The following calculations and Environment file settings in Table 1 were chosen as reasonable:

Table 1. ENVIRONMENTAL FILE SETTINGS.

packet size: 2000 bits

arrival rate: 10 pkt./sec. offered load: 9 • 10 • 2000 = 180,000 bps.

TTRT (T Req): 0.004 sec.

async mix: 0.9

prop delay:  $5.085 \times 10^{-06} \text{ sec./km.} \times 1 \text{ km.}) = 5.085 \times ^{-06} \text{ sec.}$ 

station latency:  $60.0 \times 10^{-08}$  sec.

D\_Max:  $(5.085 \,\mu\text{sec.} \cdot 10) + (60.0 \times 10^{-08} \cdot 10) = 0.05685 \,\text{ms.}$ 

F\_Max: 0.360 ms.
Token Time: 0.00088 ms.

synchronous BW: 4-(0.05685 + 0.360 + 0.00088) = 4 - 0.41773 = 3.58227 ms.

Dividing bandwidth evenly among 9 stations gives the following:

$$\frac{3.58227 \text{ ms.}}{9 \text{ stations}} = 0.39803 \text{ ms./station.}$$
 (2).

This result is compared with TTRT to determine the "sync bandwidth" attribute:

$$\frac{0.39803 \ ms.}{4.0 \ ms.} = 0.0995075 \ ms. \tag{3}$$

which is a unitless fraction of TTRT.

#### 3. RESULTS

The given parameters were applied, and the receiving buffers were inspected at the end of one second of simulation time. Figure 32 is the plot obtained illustrating the accumulation of packets in all nine subqueues. (Although all the plots may be placed in one panel, the data are divided into two panels for readability of the hardcopy). The plots show the following after one second:

Table 2. SUBQUEUE ACCUMULATION

```
subqueue(0): 13 packets
subqueue(1): 11 packets
subqueue(2): 10 packets
subqueue(3): 7 packets
subqueue(4): 7 packets
subqueue(5): 12 packets
subqueue(6): 11 packets
subqueue(7): 10 packets
subqueue(8): 9 packets
total: 90 packets
```

This agrees with the given packet generation rate of 90 packets/sec., and verifies that the preliminary linking model holds all received packets on station.

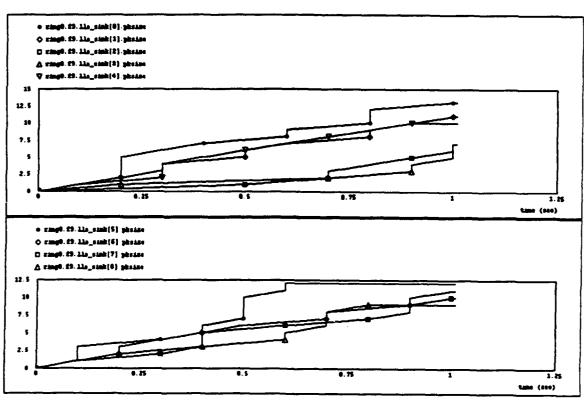


Figure 32. Packet Accumulation at Linking Model "fddi\_sink\_link"

# C. SYNCHRONOUS TIMING

#### 1. Overview

In order to preserve the real-time nature of synchronous traffic, the bandwidth allocated to a given station may not be exceeded. Although transmission time (bandwidth) may remain to a station, the protocol determines a priori whether the next packet transmission would cause the allotted bandwidth to be exceeded. If the bandwidth limit would be exceeded, then the station will not transmit. The code in the TX\_DATA state of the MAC process ensures OPNET s FDDI LAN models adhere to this behavior, as shown in the following simple experiment.

### 2. Setup

A new LAN was created, using 13 stations on 91 km. of fiber, resulting in seven kilometers of fiber between stations. The odd figures were chosen in order to avoid symmetries in the arithmetic involved, thereby enhancing the instructional value of the test. All 13 stations were assigned an equal portion of the total available bandwidth for synchronous traffic, and all stations transmitted only synchronous traffic. Physical attributes were identical for each station. The following calculations apply:

Table 3. 13-STATION LAN ENVIRONMENT SETTINGS.

prop\_delay:  $7 \text{ km.} \cdot 5.085 \times 10^{-6} \text{ sec./km.} = 0.0355950 \text{ ms.}$ 

station latency:  $60.0 \times 10^{-8}$  sec./station

F\_Max: 0.360 ms.

D\_Max:  $(13 \text{ stations} \cdot 60.0 \times 10^{-4} \text{ sec./station}) + (13 \text{ links} \cdot$ 

0.035595 ms./link) = 0.4627428 ms.

Token\_Time:  $0.88 \mu sec.$ 

**D** Max + F Max + Token Time = 0.8236228 ms.

TTRT: 4.0 ms.

 $TTRT \ge \sum SA_i + 0.8236228 - \sum SA_i \le 3.1763772 \text{ ms.}$ 

Divide the synchronous allotment evenly among 13 stations:

3.1763772 ms./13 stations = 0.2443367 ms./station.

Given the standard transmission rate of 100 Mbps, each station may transmit 24,433 bits with its synchronous allotment. This bandwidth is converted to a fraction for the Environment file attribute "sync bandwidth":

$$\frac{0.2443367 \ ms.}{4.0 \ ms} = 0.06108418 \tag{4}$$

For this test, all stations were constant transmitters. That is, the code for the source process was adjusted so that packet transmission rates and packet lengths were assigned invariant values, rather than stochastic approximations of the attributes assigned. A TTRT of 4 ms indicates 250 token passes per second, which was chosen as the packet arrival rate for all stations. Packet size was 24,000 bits, resulting in a total offered load of 78.0 Mbps. A larger packet size, for example 25,000 bits, should be too large to transmit, resulting in no throughput at all.

#### 3. Results

Figure 33 shows the resulting total throughput derived from the given setup.

Invoking the "convert to text" attribute of the on-screen menu indicates a value of 77.7

Mbps after one second, with a slow rise still in progress. This is in close agreement with the offered load.

Figure 34 is an empty panel, accompanied by a text screen indicating that no throughput results when the packet size exceeds that allowed by the synchronous bandwidth assignment. In this case, packet size was increased to 25,000 bits. Arrival was also reduced to 100 packets/sec., resulting in a total offered load of 32.5 Mbps. Figure 35 illustrates the accompanying accumulation of packets in station f3's subqueue(8), which is reserved for synchronous traffic. The perfectly linear shape is a result of the constant arrival rate assignment.

# D. ASYNCHRONOUS EFFICIENCY

#### 1. Overview

A network's throughput efficiency is calculated from the following equations, which assume that only asynchronous traffic is being transmitted:

$$E = \frac{N(T-D)}{(TN+D)} \tag{5}.$$

where:

N = number of stations,

T = TTRT and,

D = ring latency (total time required for a token to circulate the ring in the absence of data traffic).

Ring latency D is in turn defined as

$$D = L(\frac{n}{c}) + N \cdot T_{e} \tag{6}$$

where:

L =length of ring in kilometers

 $n_c = 5.085 \times 10^{-06} \text{ sec./m}.$ 

 $T_x$  = token processing time (0.88  $\mu$ s.)

(Powers, 1993, pp. 336-337; uses  $T_a = 1.0 \mu s$ . as a typical value)

The same 13 station, 91 km LAN described previously was used here, with the Environment file adjusted so that no synchronous bandwidth was assigned, and only asynchronous traffic was generated. Offered load was 78.0 Mbps. The given equations indicate the expected efficiency is 87.44%.

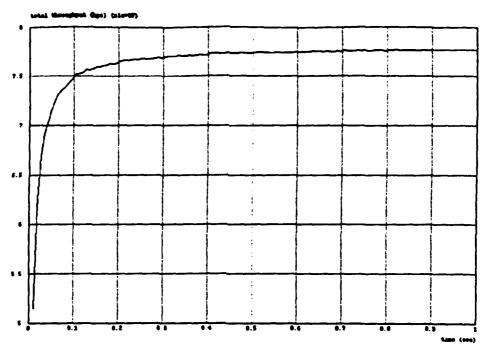


Figure 33. Synchronous Throughput: BW Exceeds Packet Transmission Time

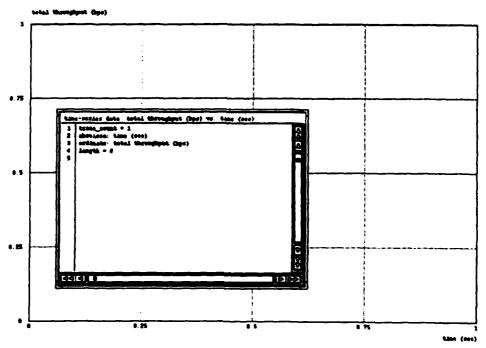


Figure 34. Synchronous Throughput: Packet Transmission Time Exceeds BW

#### 2. Results

Figure 36 illustrates the resulting throughput, 68.48 Mbps, which is 87.79% of the offered load. This agrees quite well with the predicted throughput.

# E. MULTICASTING

#### Overview

The multicasting function is designed to assign to each packet a randomly chosen number of destination addresses, then to use this selected number as an index for a loop in which a different destination address is assigned on each iteration. Upon completion of the loop, a vector array of ones and zeros has been created, in which a one in position *i* indicates the packet is addressed to station f<sub>i</sub>. Of course, a real transmitter would send many consecutive packets to the same set of addresses, in streams that comprise messages. However, this modification is in keeping with the original model's actions, which randomly assigned destinations on a packet-by-packet basis.

Preliminary tests indicated the model is not fully developed. Because no change was made to the statistics generation mechanism in the sink process model, the new model as written should have resulted in packets being counted toward total throughput each time they were received at a destination, giving an inflated throughput computation. On the other hand, this inflated figure could possibly be of value as a measure of "virtual throughput." In any event, tests indicate that throughput statistics are not being counted correctly. However, a study of the model's behavior using the debug tool in conjunction with "printf" statements

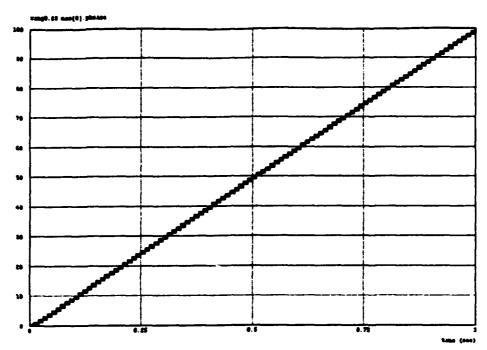


Figure 35. Accumulating Synchronous Packets in MAC

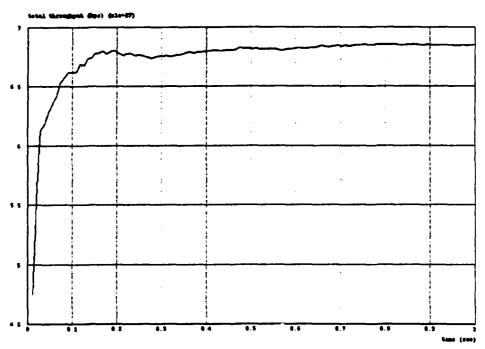


Figure 36. Asynchronous Throughput

indicated that no packets were being lost, and all stations seemed to carry out correctly the operation of receiving a multicast packet, saving the information, and passing the packet on to the next station.

Three tests are described here. The first is an attempt to reproduce the results generated for one of the simulations used in Section A, then uses the same input parameters to see if any differences exist in the handling of synchronous-only and asynchronous-only traffic. The second test compares all synchronous throughput with all asynchronous throughput. The third test is a verification that no packets are actually being lost. The fourth test is an attempt to generate a plot of expected throughput when one station broadcasts all its traffic.

The tests generally indicate that the multicast capable model, when limited to single addressing, does not behave in the same manner as the single-address-only model. In particular, throughput is lower than expected. Further development will be necessary to make the multicasting model a reliable tool.

#### 2. First Test

#### a. Setup

Figure 37 displays the throughput plots resulting from the third run in the set of simulations used to construct Figure 27. This test is based on the idea that if the multicast-capable model is limited to single addressing, and given the same input parameters provided in the earlier test, then the resulting throughput ought to be identical for total asynchronous and synchronous traffic. Therefore, the following inputs were used for a 50 station, 50 km. LAN using the multicasting station model.

Ten stations (f0, f5, f10, f15, f20, f25, f30, f35, f40, f45) transmit only synchronous traffic in 512-bit packets with a constant arrival rate of 6000 packets/sec, resulting in a synchronous offered load of 30.72 Mbps. The remaining 40 stations transmit asynchronous traffic only, in 1000-bit packets at an arrival rate of 750 packets/sec, for an asynchronous offered load of 30 Mbps. TTRT is set to 10.7 ms. For all stations, the Environment file attributes "m1n num addees" and "max num addees" are both set to 1, enforcing the limit of one destination address per packet. Appendix H ws the Environment file used here.

#### b. Results

Figure 38 illustrates the resulting throughput. Table 1 summarizes the throughput results for the two simulations. The unpredictable throughput of the multicast-capable model's synchronous and asynchronous modes would suggest some logic error in coding. The reduction in overall throughput suggests perhaps some difficulty with the simulation's timing mechanism.

Table 4. THROUGHPUT COMPARISON, FIRST TEST

	Offered Load	Single Address Capable	Multicast Capable
Total	60.72 Mbps	60.60 Mbps	55.00 Mbps
Synchronous	30.72 Mbps	30.80 Mbps	43.00 Mbps
Asynchronous	30.00 Mbps	30.00 Mbps	12.00 Mbps

A real FDDI station would not generate destination addresses in the manner coded into the simulation model; the time required would be unreasonable. On the

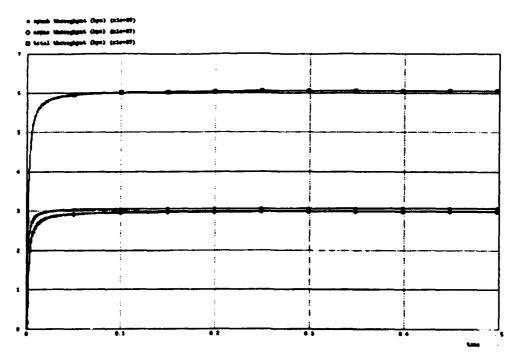


Figure 37. Throughputs: Single Addressing Only Stations

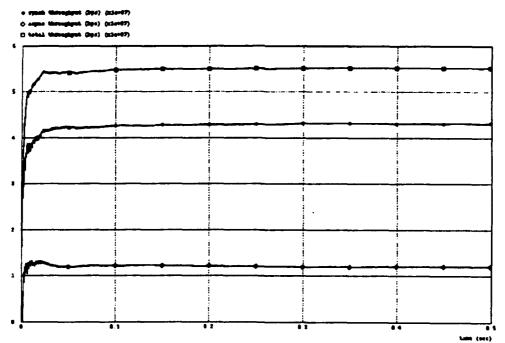


Figure 38. Throughput: Multicast Capable Stations, Single Addressing Mode

other hand, although the simulation's execution is noticeably slowed by the extra events generated in the destination address assignment loop, the throughput rate should not be affected because the simulated passage of time is controlled by the Kernel Procedures. That is, one microsecond does not pass until the simulation has completed one microsecond's worth of events, at all points on the LAN. This is how simultaneous events around the LAN are conducted, one at a time. For example, a new packet arrival at station f7, and a packet destruction at station f23, and a token release at station f0 may all occur simultaneously. A study of the sequence of events, revealed through use of the debug facility, shows that the simulation's clock is incremented after these events are all completed, thereby modeling simultaneous events. The eight percent reduction in total throughput may indicate the simulation timer is proceeding without waiting for the completion of the loop.

#### 3. Second test

#### a. Setup

This test was intended to compare the throughput resulting from two nearly identical simulations, in which the first involved only synchronous traffic, and the second involved only asynchronous traffic. A ten-station LAN was created, with all stations having the same packet arrival rate (750 packets/sec) and packet size (1000 bits). As in the previous test, destination addresses were limited to one station per packet. TTRT was set to 4.0 ms.

#### b. Results

Figure 39 and Figure 40 illustrate the throughputs resulting from 7.5 Mbps offered load of all-synchronous and all-asynchronous traffic, respectively. Interestingly, both plots are identical, suggesting perhaps that the disparate results of the previous test indicate

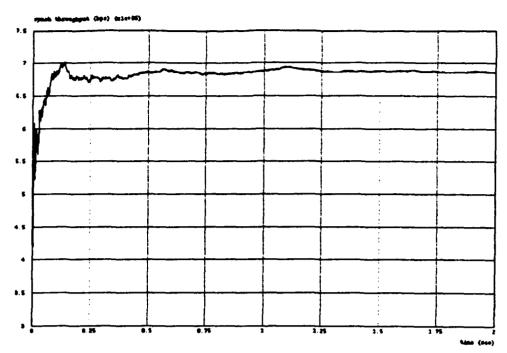


Figure 39. Synchronous Throughput

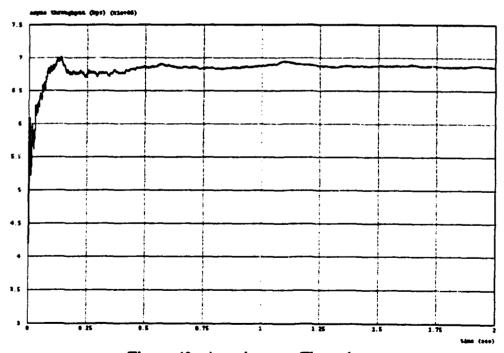


Figure 40. Asynchronous Throughput

a coding error whenever mixed loads of synchronous and asynchronous traffic must be tracked simultaneously. Also noteworthy is the fact that the resulting throughput in both cases, 6.86 Mbps, was 91.5% of the offered load. This is comparable with the previous test: the 55 Mbps throughput in Figure 38 represents 91.7% of the 60.72 Mbps offered load. Again, this suggests a coordination problem between the addressing loop and the simulation's timekeeping function.

#### 4. Third Test

#### a. Setud

This test was intended to compare the throughput from two different perspectives available. One result was generated in the familiar accounting procedure conducted in the sink process model, and the other result used the Probe Editor to place a monitoring probe on one of the station transmitter nodes, phy\_tx. If the transmissions are arranged so that all traffic passes this node, then the result should be two identical throughputs.

The same LAN of ten multicast-capable stations was used, with only station f9 transmitting, and with all of its traffic directed to station f8. In between, a probe was placed on the transmitter node of station f7. The packet arrival rate was 7500 packets/sec., and the packet length was 1000 bits, giving an offered load of 7.5 Mbps, all of which was asynchronous, with the full range of prioritization available. TTRT was 4.0 ms.

#### h. Results

Figure 41 shows a roughly constant difference of approximately 0.15 Mbps between the throughput monitored from the physical transmitter and that calculated in the

receiving station's sink process. The irregular plots are unusual, since the simulations normally show a smooth steady state after less than half a second. Significantly, both curves remain close to the offered load, though they are jagged.

The 40 bits per packet overhead (created in the Parameter Editor, where the fields "fc", "src\_addr." and "dest\_addr" are assigned sizes of 8, 16 and 16 bits, respectively) is a possible source of disparity, although a difference of 0.3 Mbps would be expected in that case (7500 x 40). A study of the sink process model shows that overhead is not included in throughput calculations, while the probes do count the bits in the encapsulating packet structure. As before, the problem requires more study.

#### 5. Fourth Test

#### a. Setup

A final test of the expected throughput of the multicast-capable model actually observed the multicasting facility, or more exactly, the broadcasting facility of the model. Again, a ten station LAN was used, with only one station transmitting, with TTRT set to 4.0. This transmitter, station f7, generated 7500 packets at 1000 bits per packet, for a total offered load of 7.5 Mbps, all asynchronous. In addition, each packet was addressed to all nine of the other stations, which would be expected to yield a throughput of 67.5 Mbps. (The throughput statistics are gathered by comparing timestamps at the receipt of a packet with the packet's creation time, which is carried as a field in the fddi\_llc\_fr packet format).

#### b. Results

Figure 42 illustrates the actual result. The probe at station f7's "phy\_tx" node reflects the offered load of 7.5 Mbps with reasonable accuracy. However, the expected throughput of 67.5 Mbps was not nearly realized. The throughput plotted, 48.9 Mbps, represents 72.44% of the predicted amount. Again, this indicates a need to further develop the model, and to better define the meanings of throughput and offered load when packets are addressed to more than one station at a time.

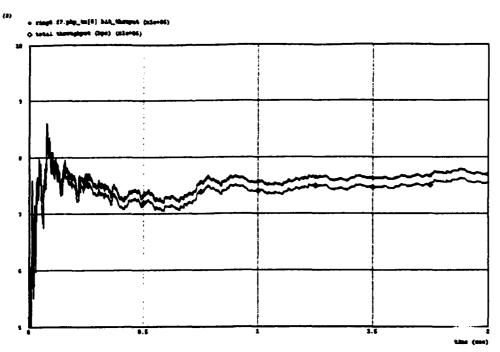


Figure 41. Throughput from Two Vantage Points

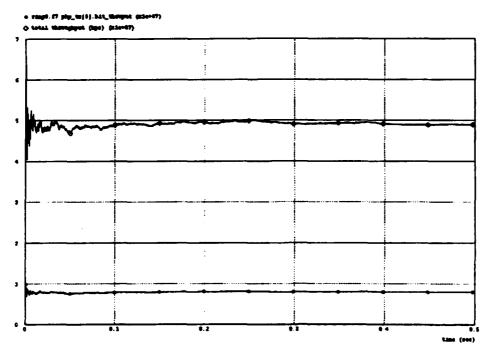


Figure 42. Broadcast from One Station

# V. CONCLUSIONS AND RECOMMENDATIONS

## A. CONCLUSIONS

This thesis has been directed to two main purposes: to explain the use and operation of OPNET s FDDI LAN simulation, and to describe the changes that were installed in the model in order to make it a more useful, accurate and versatile tool for the Common Data Link project. Both goals share another common objective, which is to develop and document the "corporate knowledge" of the CDL working group which will continue to work with the model studied here.

One immediate conclusion is the observation that OPNET is a powerful and flexible tool, but it requires much time and study to be used effectively. Where desired model attributes are lacking, the patient user may code his or her own. The following are the accomplishments documented with this thesis:

- 1. Throughput, mean delay, and end-to-end delay data are recorded separately and may be displayed separately for synchronous packet traffic and each priority level of asynchronous packet traffic. In addition, two new scalar plot axes, "Asynchronous Offered Load" and "Total Offered Load" are available for the display of LAN performance data in relation to usage.
- 2. The FDDI station model is capable of randomly choosing different priority threshold settings to assign to generated asynchronous packets, where before the

model was bound to one setting per simulation. This modification allows an additional measure of flexibility in assigning the transmission characteristics of an FDDI LAN.

- 3. A rudimentary linking node has been created, which accumulates received traffic in buffers for eventual transmission to another LAN.
- 4. A multicasting capability has been added to the FDDI station model, enabling packets to be addressed to more than one destination station. Preliminary tests and studies indicate the model correctly generates, transmits, receives, and disposes of the multicast packets, although unexpected throughput data suggest the model possibly has coding inaccuracies or improper interfacing of simulation timing and destination address generation.
- A modest series of preliminary tests verifies the continuing valid operation of the modified models, for the most part.
- 6. A significant number of unexpected features of OPNET's FDDI LAN model are documented, for the benefit of those researchers continuing to work on the CDL proje:

# **B. RECOMMENDATIONS**

Because the eventual goal of the work begun in this thesis is the development of a large scale model simulation for a Network Interface, the development of the FDDI LAN model may be expected to continue. The following are some possible areas for further development:

- As mentioned, the multicasting feature yields unexpectedly low throughput. The
  complex nature of the changes made to the code to enable multicasting suggests
  the likelihood of some logic error or timing interface discrepancy. This ought to
  be found and corrected.
- The testing presented in this thesis is only preliminary, and could well be expanded upon. Because of time constraints, delay characteristics were not addressed at all.
- 3. The buffered subqueues of the preliminary bridging model are infinite by default, but may be assigned limits through the use of on-screen attributes. The code currently has no provision for the proper disposal of packets lost to buffer overflows; lost frames will fill the host terminal's memory until none remains. A related but separate issue is the fact that there is no retransmission protocol in effect for the current model.

## APPENDIX A. FILE RETRIEVAL VIA FTP

The following is an excerpt from a screen dialogue demonstrating access to MIL 3

Inc.'s bulletin board, movement among subdirectories, and retrieval of files.

```
sun24:/home3/nix
 % ftp
 ftp> open
 (to) mil3.com
 Connected to mil3.com.
 220 rmaxwell FTP server (SunOS 4.1) ready.
 Name (mil3.com:nix): anonymous
 331 Guest login ok, send ident as password.
 Password: nix@ece.nps.navy.mil
 230 Guest login ok, access restrictions apply.
 ftp> dir
 200 PORT command successful.
150 ASCII data connection for /bin/ls (131.120.20.124,2926) (0 bytes).
total 18
 -rw-r--r-- 1 0
                            100
                                    2110 Jul 21 00:32 FTP.instructions
-rw-r--r-- 1 0 100 2110 Jul 21 00:32 FTP.instructure  
-r--r--r-- 1 0 1 5135 Jan 21 1992 README.OPSI  
dr-xr-xr-x 2 0 1 512 Jan 14 1992 dev  
dr-xr-xr-x 2 0 1 512 Jan 14 1992 etc  
drwxr-xr-x 5 0 1 512 Oct 27 1992 examp  
drwxr-xr-x 2 0 1 512 Dec 20 16:40 incoming  
dr-xr-xr-x 10 0 1 512 Dec 23 17:34 model_depot  
drwxr-xr-x 10 0 1 512 Dec 15 10:09 patches  
drwxrwxrwx 3 0 1 512 Dec 29 03:05 tmp
                                    5135 Jan 21 1992 README.OPSIG
dr-xr-xr-x 3 0
                           1
                                    512 Jan 14 1992 usr
226 ASCII Transfer complete.
700 bytes received in 3.1 seconds (0.22 Kbytes/s)
ftp> get README.OPSIG
200 PORT command successful.
150 ASCII data connection for README.OPSIG (131.120.20.124,2927) (5135
    bytes).
226 ASCII Transfer complete.
local: README.OPSIG remote: README.OPSIG
5257 bytes received in 6.8 seconds (0.75 Kbytes/s)
ftp> cd patches
250 CWD command successful.
ftp> dir
200 PORT command successful.
150 ASCII data connection for /bin/ls (131.120.20.124,2928) (0 bytes).
total 8
```

```
512 Mar 10 1993 2.3.L
drwxr-xr-x 6 101
drwxr-xr-x 4 101
                     100
                                 512 Apr 2 1993 2.3.Lhp
                                 512 Sep 24 22:42 2.3.M
drwxr-xr-x 6 101
                     1
                                 512 Dec 6 18:37 2.4.A
drwxr-xr-x 4 101
                     1
                                 512 Dec 17 22:32 2.4.B
drwxr-xr-x 5 101
                     1
drwxr-xr-x 4 101
                     1
                                 512 Jan 29 1993 2.4.beta2
                                 512 Apr 26 1993 2.4.beta3
drwxr-xr-x 13 101
                     1
drwxr-xr-x 5 101
                                 512 Jun 10 1993 2.4.beta4
                     1
226 ASCII Transfer complete.
511 bytes received in 0.63 seconds (0.8 Kbytes/s)
250 CWD command successful.
ftp> cd 2.4.B
250 CWD command successful.
ftp> dir
200 PORT command successful.
150 ASCII data connection for /bin/ls (131.120.20.124.2934) (0 bytes).
total 3
                                 512 Dec 15 10:11 opbug_2081
drwxr-xr-x 2 101 100
drwxr-xr-x 2 0
                       1
                                    512 Dec 17 22:33 tsup_3139
                                    512 Dec 17 20:15 tsup_3182
                       1
drwxr-xr-x 2 0
226 ASCII Transfer complete.
205 bytes received in 0.28 seconds (0.7 Kbytes/s)
ftp> cd opbug_2081
250 CWD command successful.
ftp> dir
200 PORT command successful.
150 ASCII data connection for /bin/ls (131.120.20.124.2935) (0 bytes).
total 57
-rw-r--r-- 1 101
                                 3988 Dec 15 07:12 README
                     100
                                 53379 Dec 15 07:12 fddi_mac.pr.m
-rw-r--r-- 1 101
                     100
226 ASCII Transfer complete.
141 bytes received in 0.22 seconds (0.62 Kbytes/s)
ftp> get README
200 PORT command successful.
150 ASCII data connection for README (131.120.20.124.2936) (3988
   bytes).
226 ASCII Transfer complete.
local: README remote: README
4092 bytes received in 5 seconds (0.81 Kbytes/s)
ftp> bin
200 Type set to I.
ftp> get fddi_mac.pr.m
200 PORT command successful.
150 ASCII data connection for fddi_mac.pr.m (131.120.20.124,2937)
   `(53379 bytes).
226 ASCII Transfer complete.
local: fddi_mac.pr.m remote: fddi_mac.pr.m
53388 bytes received in 64 seconds (0.81 Kbytes/s)
ftp> quit
```

# APPENDIX B. PACKET AND ICI FRAME

# **STRUCTURES**

The following packet structures are used in the FDDI LAN model.

# A. PACKET FORMATS

## 1. "fddi\_llc\_fr"

Field Name	Type	Size (bits)	Default Value	Default Set
			• • • • • • • • • • • • • • • • • • • •	• • • • • • • • • • •
cr_time	double	0	0.0	set
pri <sup>Note</sup>	integer	0	0	unset

Note: Added to allow generation of statistics related to prioritized traffic

## 2. "fddi\_mac\_fr"

Field Name	Туре	Size (bits)	Default Value	Default Set
fc	integer	8	•	unset
src_addr	integer	16	•	unset
dest_addr	integer	16	•	unset
info	packet	-1	•	unset
svc_class	integer	0	•	unset
pri	integer	0	•	unset
tk class	integer	0	•	unset

## 3. "fddi\_mac\_tk"

Field Name	Type	Size (bits)	Default Value	Default Set
	• • • •	• • • • • • • • • • •		•••••
fc	integer	8	•	unset
class	integer	0	•	unset
res_station	integer	0	•	unset

# **B. ICI FORMATS**

# 1. "fddi\_mac\_ind"

Attribute Name	Type	Default
		• • • • • •
src_addr	integer	0
dest addr	integer	0

# 2. "fddi\_mac\_req"

Attribute Name	Type	Default
svc_class	integer	0
dest_addr	integer	0
pri	integer	0
tk_class	integer	0

# APPENDIX C. EXAMPLE ENVIRONMENT FILE

## **FOR 32-STATION FDDI LAN**

```
# fddi32.ef
# sample simulation configuration file for fddi example model
# 32 station network
#*** Attributes related to loading used by "fddi_gen" ***
# station addresses
*.*.f0.mac.station_address: 0
*.*.fl.mac.station_address: l
*.*.f2.mac.station_address: 2
*.*.f3.mac.station_address: 3
*.*.f4.mac.station_address: 4
*.*.f5.mac.station_address: 5
*.*.f6.mac.station_address: 6
*.*.f7.mac.station_address: 7
*.*.f8.mac.station_address: 8
*.*.f9.mac.station_address: 9
*.*.fl0.mac.station_address: 10
*.*.fll.mac.station_address: ll
*.*.fl2.mac.station_address: 12
*.*.fl3.mac.station_address: 13
*.*.fl4.mac.station_address: 14
*.*.fl5.mac.station_address: 15
*.*.fl6.mac.station_address: 16
*.*.fl7.mac.station_address: 17
*.*.fl8.mac.station_address: 18
*.*.f19.mac.station_address: 19
*.*.f20.mac.station_address: 20
*.*.f21.mac.station_address: 21
*.*.f22.mac.station_address: 22
*.*.f23.mac.station_address: 23
*.*.f24.mac.station_address: 24
*.*.f25.mac.station_address: 25
*.*.f26.mac.station_address: 26
*.*.f27.mac.station_address: 27
*.*.f28.mac.station_address: 28
*.*.f29.mac.station_address: 29
*.*.f30.mac.station_address: 30
*.*.f31.mac.station_address: 31
```

```
*.*.*.mac.ring_id :0
# destination addresses for random message generation
"*.*.llc_src.low dest address" :
                                    31
"*.*.11c_src.high dest address" :
#"top.ring0.f0.llc_src.low dest address"
#"top.ring0.f0.llc_src.high dest address" :
# arrival rate(frames/sec), and message size (bits) for random message
# generation at each station on the ring.
"*.*.*.arrival rate":
"*.*.*.mean pk length" :
                              500
# set the proportion of asynchronous traffic
# a value of 1.0 indicates all asynchronous traffic
"*.*.*.async_mix" :
                           1.0
#*** Ring configuration attributes used by "fddi_mac" ***
# allocate percentage of synchronous bandwidth to each station
# this value should not exceed 1 for all stations combined: OPNET does
not
# enforce this: 01FEB94: this must be less than 1: see equation below
"*.*.mac.sync bandwidth" :
# "*.f0.mac.sync bandwidth" : .0935487
"*.*.mac.T_Req" : .010
# Index of the station which initially launches the token
"spawn station": 0
# Delay incurred by packets as they traverse a station's ring interface
# see Powers, p. 351 for a discussion of this (Powers gives lusec,
# but 60.0e-08 agrees with Dykeman & Bux)
station_latency: 60.0e-08
# Propagation Delay separating stations on the ring.
prop_delay:
                  5.085e-06
#*** Simulation related attributes
# Token Acceleration Mechanism enabling flag.
# It is reccomended that this mechanism be enabled for most situations
accelerate_token: 1
seed:
                     10
# Run control attributes
duration:
```

TRUE verbose\_sim: upd\_int: .1

fddi32mod os\_file:

fddi32mod

ov\_file: fddi32mod
# Opnet Debugger (odb) enabling attribute
# debug: TRUE

# APPENDIX D. DEBUG TOOL EXCERPT

The following is an excerpt from the debugger in fulltrace, showing the arrival and reception of a message traffic packet at its destination address. In this case, station fll has sent a packet to station f31. The packet has been passed "hand to hand" through each station in between, with the simulator enacting every step along the way. Note that when reception is complete, the simulation returns to station f11, which still has the token.

\_\_\_(ODB 2.4.A: Event)\_\_\_\_\_ \* Time : 0.995744186808 sec, [00d 00h 00m 00s . 995ms 744us 186ns 808ps] \* Event : execution ID (461704), schedule ID (#502933), type (self intrpt) \* Source: execution ID (461703), top.ring0.f30.phy\_tx (pt-pt transmitter) \* Data : code (0) > Module : top.ring0.f30.phy\_tx (pt-pt transmitter) odb> next \* Kernel Action: Pt-Pt Transmitter object Completing transmission of packet(s) channel (0) packet ID (4459)\_\_\_ (ODB 2.4.A: Event) \_\_\_\_\_

odb> next

<sup>\*</sup> Time : 0.995744186808 sec. [00d 00h 00m 00s . 995ms 744us 186ns 808ps]

<sup>\*</sup> Event : execution ID (461705), schedule ID (#502934), type (remote intrpt)

<sup>\*</sup> Source : execution ID (461703), top.ring0.f30.phy\_tx (pt-pt transmitter)

<sup>\*</sup> Data : code (0)

<sup>&</sup>gt; Module : top.ring0.f3l.phy\_rx (pt-pt receiver)

<sup>\*</sup> Kernel Action: Pt-Pt Receiver object Beginning reception of packet channel (0)

```
____ (ODB 2.4.A: Event) _____
   * Time : 0.995744186808 sec. [00d 00h 00m 00s . 995ms 744us 186ns
   808ps]
   * Event : execution ID (461706), schedule ID (#502935), type
   (remote intrpt)
   * Source : execution ID (461703), top.ring0.f30.phy_tx (pt-pt
   transmitter)
   * Data : code (0)
   > Module : top.ring0.f31.phy_rx (pt-pt receiver)
odb> next
        * Kernel Action: Pt-Pt Receiver object
           Completing reception of packet
           channel
                     (0)
           packet ID
                     (4459)
        * Kernel Action: Pt-Pt pipeline
           Calling (error) pipeline stage
           packet ID (4459)
        * op_td_get_int (pkptr, tda_index)
           packet ID (4459)
           TDA attribute (OPC_TDA_PT_LINK_OBJID)
           TDA value (82)
        * op_ima_obj_attr_get (objid, attr_name, value_ptr)
           objid
                 (82)
           attr_name (ber)
           value (0.0)
        * op_pk_total_size_get (pkptr)
           packet ID (4459)
           total size (25040)
        * op_td_set_int (pkptr, tda_index, value)
           packet ID (4459)
           TDA attribute
                          (OPC_TDA_PT_NUM_ERRORS)
           TDA value (0 bit errs)
        * Kernel Action: Pt-Pt pipeline
           Calling (ecc) pipeline stage
           packet ID (4459)
        * op_td_is_set (pkptr, tda_index)
           packet ID (4459)
           TDA attribute (OPC_TDA_PT_ND_FAIL)
```

```
* op_td_get_int (pkptr, tda_index)
            packet ID
                      (4459)
           TDA attribute
                           (OPC_TDA_PT_RX_OBJID)
           TDA value (734)
         * op_ima_obj_attr_get (objid, attr_name, value_ptr)
           objid
                    (734)
            attr_name (ecc threshold)
            value (0.0)
        * op_pk_total_size_get (pkptr)
           packet ID (4459)
           total size (25040)
        * op_td_get_int (pkptr, tda_index)
           packet ID
                      (4459)
                            (OPC_TDA_PT_NUM_ERRORS)
           TDA attribute
           TDA value (0 bit errs)
        * op_td_set_int (pkptr, tda_index, value)
           packet ID
                      (4459)
                            (OPC_TDA_PT_PK_ACCEPT)
           TDA attribute
           TDA value (I)
        * Kernel Action: Pt-Pt Receiver object
            Packet successfully received
                       (0)
           channel
                       (4459)
           packet ID
         ___ (ODB 2.4.A: Event) ___
   * Time : 0.995744186808 sec, [OOd OOh OOm OOs . 995ms 744us 186ns
  808ps]
   * Event : execution ID (461707), schedule ID (#502936), type
   (stream intrpt)
   * Source: execution ID (461706), top.ring0.f31.phy_rx (pt-pt
   receiver)
   * Data : instrm (0), packet ID (4459)
   > Module : top.ring0.f31.mac (queue)
odb> next
        * invoking process ("fddi_mac")
                 state (IDLE): exit executives____
        * op_intrpt_type ()
            intrpt type (stream intrpt)
```

tda is set (false)

```
* op_intrpt_strm ()
  active strm (0)
* op_pk_get (instrm_index)
  strm. index (0)
  packet ID (4459)
* op_pk_nfd_get (pkptr, fd_name, value_ptr)
  packet ID (4459)
  field name (fc)
  value (0)
* op_intrpt_type ()
  intrpt type (stream intrpt)
* op_intrpt_type ()
  intrpt type (stream intrpt)
* op_intrpt_strm ()
  active strm (0)
   ____state (FR_RCV): enter executives___
* op_pk_nfd_get (pkptr, fd_name, value_ptr)
  packet ID
             (4459)
  field name (src_addr)
  value
          (11)
 ____state (FR_RCV): exit executives_____
____state (FR_REPEAT): enter executives_____
* op_pk_nfd_get (pkptr, fd_name, value_ptr)
  packet ID (4459)
  field name (dest_addr)
  value (31)
* op_pk_total_size_get (pkptr)
  packet ID (4459)
  total size (25040)
* op_pk_nfd_get (pkptr, fd_name, value_ptr)
  packet ID (4459)
  field name (info)
  value
           (pk 1d (4458))
* op_ici_attr_set (iciptr, attr_name, attr_value)
  ICI id
              (81)
  attr name (src_addr)
  value (11)
```

```
* op_ici_attr_set (iciptr, attr_name, attr_value)
         ICI 1d
                   (81)
         attr name (dest_addr)
         value (31)
      * op_ici_install (iciptr)
         ICI ID
                   (81)
      * op_pk_send_delayed (pkptr. outstrm_index. delay)
        packet ID (4458)
         stream index (1)
         delay (0.0002504)
      * op_pk_destroy (pkptr)
         packet ID (4459)
      * Kernel Action: Destroying Packet
        packet ID (4459)
      ____state (FR_REPEAT): exit executives_____
       ____state (IDLE): enter executives____
     * returning from process ("fddi_mac")
     ____ (ODB 2.4.A: Event) _____
* Time : 0.995886571808 sec, [00d 00h 00m 00s . 995ms 886us 571ns
808ps]
* Event : execution ID (461708), schedule ID (#502837), type
(stream intrpt)
* Source : execution ID (461607), top.ring0.fll.mac (queue) * Data : instrm (0), packet ID (4457)
> Module : top.ringO.fll.phy_tx (pt-pt transmitter)
```

odb> next

# APPENDIX E. MAC "C" CODE:

# "fddi\_mac\_mult.pr.c"

The line numbering in this appendix is used for reference within this thesis only, and does not correspond with that seen in OPNET stext editors.

```
/* Process model C form file: fddi_mac_mult.pr.c */
1
2 /* Portions of this file Copyright (C) MIL 3, Inc. 1992 */
   /* OPNET system definitions */
   #include <opnet.h>
   #include "fddi_mac_mult.pr.h"
    FSM_EXT_DECS
7
   /* Header block */
    /* Define a timer structure used to implement */
   /* the TRT and THT timers. The primitives defined to */
   /* operate on these timers can be found in the */
10
   /* function block of this process model. */
11
12
    typedef struct
13
14
       int
                      enabled;
15
       double
                        start_time:
16
       double
                         accum:
17
       double
                         target_accum;
       } FddiT_Timer;
    /* O8FEB94: define the number of stations here. -Nix */
19
    #define NUM_STATIONS 50
21
   /* Declare certain primitives dealing with timer.s */
22
    double
                   fddi_timer_remaining ():
23
    FddiT_Timer*
                   fddi_timer_create ();
   double
                   fddi_timer_value ();
25
   /* Scratch strings for trace statements */
26
                str0 [512], str1 [512];
27
   /* define constants particular to this implementation */
    #define FDDI_MAX_STATIONS
28
                                     512
```

```
/* define possible values for the frame control field */
29
    #define FDDI_FC_FRAME
                                      0
                                      1
31
    #define FDDI_FC_TOKEN
    /* define possible service classes for frames */
32
33
    #define FDDI_SVC_ASYNC
34
    #define FDDI_SVC_SYNC
35
    /* define input stream indices */
    #define FDDI_LLC_STRM_IN
    #define FDDI_PHY_STRM_IN
37
    /* define output stream indices */
38
    #define FDDI_LLC_STRM_OUT
39
                                      0
40
    #define FDDI_PHY_STRM_OUT
41
    /* define token classes */
    #define FDDI TK_NONRESTRICTED
                                      0
43
    #define FDDI_TK_RESTRICTED
                                      1
44
    /* Ring Constants */
    #define FDDI_TX_RATE
                                   1.0e+08
45
46
    #define FDDI_SA_SCAN_TIME
                                      28.0e-08
    /* Token transmission time: based on 6 symbols plus 16 symbols of
47
48
    preamble */
    #define FDDIC_TOKEN_TX_TIME
                                         88.0e-08
49
50
    /* Codes used to differentiate remote interrupts */
    #define FDDIC_TRT_EXPIRE
51
    #define FDDIC_TK_INJECT
                                      1
52
    /* Define symbolic expressions used on transition */
53
54
    /* conditions and in executive statements. */
55
    #define TRT_EXPIRE (op_intrpt_type () == OPC_INTRPT_REMOTE &&
56
       op_intrpt_code () == FDDIC_TRT_EXPIRE)
    #define TK_RECEIVED phy_arrival && frame_control == FDDI_FC_TOKEN
57
58
    #define RC_FRAME phy_arrival && frame_control == FDDI_FC_FRAME
59
    #define FRAME_ARRIVAL
                                                         ١
       op_intrpt_type () == OPC_INTRPT_STRM &&
                                                               ١
60
61
       op_intrpt_strm () == FDDI_LLC_STRM_IN
62
    #define STRIP my_address == src_addr
63
    /* Define the maximum value for ring_id. This is the */
    /* maximum number of FDDI rings that can exist in a */
64
65
    /* simulation. Note that if this number is changed, */
```

```
/* the initialization for fddi_claim_start below must */
66
     /* also be modified accordingly.
67
68
     #define FDDI_MAX_RING_ID
     /* Declare the operative TTRT value 'T_Opr' which is the final */
69
     /* negotiated value of TTRT. This value is shared by all stations */
 70
     /* on a ring so that all agree on its value.
 71
                 fddi_t_opr [FDDI_MAX_RING_ID];
 72
     double
                 Fddi_T_Opr (fddi_t_opr [ring_id])
73
     #define
     /* This flag indicates that the negotiation for the final TTRT */
74
     /* has not yet begun. It is statically initialized here, and */
75
     /* is reset by the first station which modifies T_Opr. */
76
     /* Initialize to 1 for all rings.
77
                 fddi_claim_start [FDDI_MAX_RING_ID] = {1,1,1,1,1,1,1,1,1};
78
     #define Fddi_Claim_Start (fddi_claim_start [ring_id])
79
     /* Declare station latency parameters.
80
     /* These are true globals. so they do not need to be arrays. */
81
                 Fddi_St_Latency:
82
     double
83
     double
                 Fddi_Prop_Delay:
     /* Declare globals for Token Acceleration Mechanism. */
84
     /* Hop delay and token acceleration are true globals. */
85
                 Fddi_Tk_Hop_Delay;
86
     double
                 Fddi_Tk_Accelerate = 1;
87
     int
     /* These are actually values shared by all nodes on a ring, */
88
89
     /* so they must be defined as arrays.
                 fddi_tk_block_base_time [FDDI_MAX_RING_ID];
90
     double
91
     #define Fddi_Tk_Block_Base_Time (fddi_tk_block_base_time [ring_id])
92
                   di_tk_block_base_station [FDDI_MAX_RING_ID];
     int
93
     #define Fddi_Tk_Block_Base_Station (fddi_tk_block_base_station
94
        [ring_id])
     int
95
                 fddi_tk_blocked [FDDI_MAX_RING_ID];
     #define Fddi_Tk_Blocked (fddi_tk_blocked [ring_id])
96
                 fddi_num_stations [FDDI_MAX_RING_ID];
97
     int
     #define Fddi_Num_Stations (fddi_num_stations [ring_id])
98
99
                 fddi_num_registered [FDDI_MAX_RING_ID];
100
     #define Fddi_Num_Registered (fddi_num_registered [ring_id])
              fddi_address_table [FDDI_MAX_RING_ID][FDDI_MAX_STATIONS];
101
     Obiid
     #define Fddi_Address_Table (fddi_address_table [ring_id])
102
103
     /* Below is part of the OPBUG 2081 patch; FB ended here, before. -Nix */
```

```
104
      /* Event handles for the TRT are maintained at a global level to */
      /* allow token acceleration mechanism to adjust these as necessary */
105
106
      /* when blocking and reinjecting the token. TRT_handle simply */
107
      /* represents the TRT for the local MAC
108
      Evhandle fddi_trt_handle [FDDI_MAX_RING_ID][FDDI_MAX_STATIONS]:
109
      #define Fddi_Trt_Handle (fddi_trt_handle [ring_id])
110
      #define TRT_handle
                             Fddi_Trt_Handle [my_address]
      /* Similarly, the TRT data structure is maintained on a global level. */
111
                    fdd1_trt [FDDI_MAX_RING_ID] [FDDI_MAX_STATIONS];
112
      FddiT Timer*
113
     #define Fddi_Trt (fddi_trt [ring_id])
114
     #define TRT
                           Fddi_Trt [my_address]
115
      /* Registers to record the expiration time of each TRT when token is
116
         blocked. */
117
     double
                  fddi_trt_exp_time [FDDI_MAX_RING_ID] [FDDI_MAX_STATIONS]:
118
     #define Fddi_Trt_Exp_Time (fddi_trt_exp_time [ring_id])
     /* the 'Late_Ct' flag is declared on a global level so that it can be
119
120
121
     /* set at the tim ewhere the token is injected back into the ring.
122
                  fddi_late_ct [FDDI_MAX_RING_ID] [FDDI_MAX_STATIONS];
     int
123
     #define Fddi_Late_Ct (fddi_late_ct [ring_id])
124
     #define Late_Ct
                              Fddi_Late_Ct [my_address]
125
     /* Convenient macro for setting TRT for a given station and absolute
126
127
     #define TRT_SET(station_id.abs_time) fddi_timer_set (Fddi_Trt
128
         [station_id], abs_time - op_sim_time()); Fddi_Trt_Handle [station_id]
129
        = op_intrpt_schedule_remote (abs_time, FDDIC_TRT_EXPIRE,
130
        Fddi_Address_Table [station_id]);
131
     /* State variable definitions */
132
     typedef struct
133
134
        FSM_SYS_STATE
135
                              sv_ring_id:
136
        FddiT_Timer*
                           sv_THT;
137
        double
                              sv_T_Req;
138
        double
                              sv_T_Pri [8];
139
        Objid
                           sv_my_objid:
140
        int
                              sv_spawn_token;
141
        int
                              sv_my_address:
142
        Packet*
                           sv_tk_pkptr:
143
        double
                              sv_sync_bandwidth;
144
        double
                              sv_sync_pc:
145
        int
                              sv_restricted;
146
        int
                              sv_res_peer;
147
        int
                              sv_tk_registered:
148
        Ici*
                           sv_to_llc_ici_ptr;
```

```
149
                               sv_tk_trace_on:
        int
150
         } fddi_mac_mult_state;
     #define pr_state_ptr ((fddi_mac_mult_state*) SimI_Mod_State_Ptr)
151
152
     #define ring_id
                                  pr_state_ptr->sv_ring_id
                                  pr_state_ptr->sv_THT
153
     #define THT
154
     #define T_Req
                                  pr_state_ptr->sv_T_Req
     #define T_Pri
                                  pr_state_ptr->sv_T_Pri
155
                              pr_state_ptr->sv_my_objid
156
     #define my_objid
     #define spawn_token
                              pr_state_ptr->sv_spawn_token
157
                                  pr_state_ptr->sv_my_address
     #define my_address
158
                              pr_state_ptr->sv_tk_pkptr
159
     #define tk_pkptr
     #define sync_bandwidth pr_state_ptr->sv_sync_bandwidth
160
161
                                  pr_state_ptr->sv_sync_pc
     #define sync_pc
                                  pr_state_ptr->sv_restricted
162
     #define restricted
                              pr_state_ptr->sv_res_peer
163
     #define res_peer
                              pr_state_ptr->sv_tk_registered
164
     #define tk_registered
     #define to_llc_ici_ptr
                              pr_state_ptr->sv_to_llc_ici_ptr
165
                              pr_state_ptr->sv_tk_trace_on
166
     #define tk_trace_on
     /* Process model interrupt handling procedure */
167
168
     void
     fddi_mac_mult ()
169
170
        /* Packets and ICI's */
171
172
        Packet*
                        mac_frame_ptr;
173
        Packet*
                        pdu_ptr:
174
        Packet*
                        pkptr:
175
        Packet*
                        data_pkptr;
        Ici*
176
                        ici_ptr;
177
        /* Packet Fields and Attributes */
178
                           req_pri, svc_class, req_tk_class;
        int
                           frame_control, src_addr;
179
        int
180
        int
                           pk_len, pri_level;
181
        static
182
                           *da_ptr, dest_addr[];
        int
        /* Token - Related */
183
184
        int
                           tk_usable, res_station, tk_class;
185
        int
                           current_tk_class:
186
        double
                           accum_sync;
187
        /* Timer - Related */
        double
                           tx_time, timer_remaining, accum_bandwidth;
188
189
        double
                           tht_value;
190
        /* Miscellaneous */
191
        int
                        i;
```

```
192
        int
                        spawn_station, phy_arrival;
193
        char
                     error_string [512]:
                        num_frames_sent, num_bits_sent;
194
        int
     /* 26DEC93: loop management variables, used in RCV_TK */
195
196
        /* and ENCAP states. -Nix
                        NUM_PRIOS;
197
        int
198
        int
                        punt;
199
        int
                        q_check;
     /* O8FEB94: case management variables. used in FR_REPEAT. -Nix */
200
201
                        for_me;
        int
202
                        count_addees;
        int
     /* O8MAR94: "field holding" variables, used in FR_REPEAT. -Nix */
203
204
        Packet*
                    info_ptr;
205
        FSM_ENTER (fddi_mac_mult)
206
        FSM_BLOCK_SWITCH
207
208
            /** state (INIT) enter executives **/
209
210
            FSM_STATE_ENTER_FORCED (0, stateO_enter_exec, "INIT")
211
     /* Obtain the station's address . This is an attribute */
212
213
     /* of this process. Addressing is simplified by */
     /* simply using integers, and only one mode. */
214
     /* This mode is 16 bit addressing unless the */
215
216
     /* packet format 'fddi_mac_fr' is modified. */
              my_objid = op_id_self(); /* 29DEC93 */
217
218
              op_ima_obj_attr_get (my_objid. "station_address", &my_address);
219
     /* Register the station's object id in a global table. */
220
     /* This table is used by the mechanism which improves */
221
     /* simulation efficiency by 'jumping over' idle periods */
222
     /* rather than circulating an unusable token. */
223
              fddi_station_register (my_address, my_objid);
     /* Obtain the station latency for tokens and frames. */
224
225
     /* Default value is set at 100 nanoseconds. */
226
              Fddi_St_Latency = 100.0e-09;
              op_ima_sim_attr_get (OPC_IMA_DOUBLE, "station_latency",
227
228
                 &Fddi_St_Latency);
229
     /* Obtain the propagation delay separating stations. */
230
     /* This value is given in seconds with default value 3.3 microseconds.
231
232
              Fddi_Prop_Delay = 3.3e-06;
```

```
op_ima_sim_attr_get (OPC_IMA_DOUBLE. "prop_delay",
233
234
                 &Fddi_Prop_Delay):
     /* Derive the Delay for a 'hop' of a freely circulating packet. */
235
              Fddi_Tk_Hop_Delay = Fddi_Prop_Delay + Fddi_St_Latency;
236
     /* The T Pri [] state variable array supports priority */
237
     /* assignments on a station by station basis by */
238
     /* establishing a correspondence between integer priority */
239
240
     /* levels assigned to frames and the maximum values of the*/
     /* Token holding timer (THT) which would allow packets to be*/
241
     /* sent. Eight levels are supported here, but this can easily */
242
     /* be changed by redimensioning the priority array. */
243
     /* By default all levels are identical here, allowing */
244
245
     /* any frame to make use of the token, so that in fact */
246
     /* priority levels are not used in the default case. */
     /* OlJAN94: (8-i) is a quick attempt to impart different weighting */
247
     /* scales on each priority level, and is not necessarily realistic.-Nix
248
249
250
              for (i = 0; i < 8; i++)
251
                 T_Pri[i] = (double) Fddi_T_Opr/(8.0 - i); /* OlJAN94 */
252
     /* printf("INIT: T_Pri[%d] = %d; Fddi_T_Opr = %d\n",
253
                                        */
254
     /* i, T_Pri[i], Fddi_T_Opr);
255
                 }
     /*Create the token holding timer (THT) used to restrict the */
256
     /* asynchronous bandwidth consumption of the station */
257
258
              THT = fddi_timer_create ();
259
     /* Create the token rotation timer (TRT) used to measure the */
     /* rotations of the token, detect late tokens and initialize */
260
     /* the THT timer before asynchronous tranmsmissions. */
261
262
              TRT = fddi_timer_create ();
     /* Set the TRT timer to expire in one TTRT */
263
264
              TRT_SET (my_address, op_sim_time () + Fddi_T_Opr);
265
     /* Initialize the Late_Ct variable which keeps track. */
     /* of the number of TRT expirations. */
266
267
              Late_Ct = 0;
268
     /* initially the ring operates in nonrestricted mode */
269
              restricted = 0;
     /* Create an Interface Control Information structure */
270
271
     /* to use when delivering received frames to the LLC. */
```

```
to_llc_ici_ptr = op_ici_create ("fddi_mac_ind");
272
     /* The 'tk_registered' variable indicates if the station */
273
     /* has registered its intent to use the token. */
274
275
              tk_registered = 0:
276
     /* Determine if the model is to make use of the token */
     /* 'acceleration' mechanism. If not, every passing of the */
277
     /* token will be explicitlyly modeled, leading to large */
278
     /* number of events being scheduled when the ring is idle */
279
     /* (i.e. no stations have data to send). */
280
              op_ima_sim_attr_get (OPC_IMA_INTEGER, "accelerate_token",
281
282
                 &Fddi_Tk_Accelerate);
     /* Obtain the synchronous bandwidth assigned */
283
284
     /* to this station. It is expressed as a */
     /* percentage of TTRT, and then converted to seconds */
285
              op_ima_obj_attr_get (my_objid, "sync bandwidth", &sync_pc):
286
287
              sync_bandwidth = sync_pc * Fddi_T_Opr;
     /* Only one station in the ring is selected to */
288
     /* introduce the first token. Test if this station is it. */
289
290
     /* If so, set the 'spawn_token' flag. */
              op_ima_sim_attr_get (OPC_IMA_INTEGER, "spawn station",
291
292
                 &spawn_station):
293
              spawn_token = (spawn_station == my_address);
294
     /* If the station is to spawn the token, create */
295
     /* the packet which represents the token. */
296
              if (spawn_token)
297
298
                 tk_pkptr = op_pk_create_fmt ("fddi_mac_tk");
     /* assign its frame control field */
299
300
                 op_pk_nfd_set (tk_pkptr, "fc", FDDI_FC_TOKEN);
301
     /* the first token issued is non-restricted */
302
                 op_pk_nfd_set (tk_pkptr, "class", FDDI_TK_NONRESTRICTED);
303
     /* The transition will be made into the ISSU_TK */
304
     /* state where the tk_usable variable is used. */
305
     /* In case any data has been generated, prset */
306
     /* this variable to one. */
307
                 tk\_usable = 1;
308
     /* When sending packets the variable accum_bandwidth is */
309
     /* used as a scheduling base. Init this value to zero. */
```

```
/* This statement is required in case this is the spawning */
310
     /* station, and the next state entered is ISSUE_TK */
311
              accum_bandwidth = 0.0;
312
313
     /** state (INIT) exit executives **/
314
           FSM STATE_EXIT_FORCED (0, state0_exit_exec, "INIT")
315
316
317
              }
     /** state (INIT) transition processing **/
318
319
           FSM_INIT_COND (spawn_token)
320
           FSM_DFLT_COND
321
           FSM_TEST_LOGIC ("INIT")
322
           FSM_TRANSIT_SWITCH
323
324
              FSM_CASE_TRANSIT (0, 2, state2_enter_exec, ;)
325
              FSM_CASE_TRANSIT (1. 1. statel_enter_exec. ;)
326
           /*-----*/
327
     /** state (IDLE) enter executives **/
328
           FSM_STATE_ENTER_UNFORCED (1, statel_enter_exec, "IDLE")
329
330
331
              }
332
     /** blocking after enter executives of unforced state. **/
333
           FSM_EXIT (3,fddi_mac_mult)
334
     /** state (IDLE) exit executives **/
335
           FSM_STATE_EXIT_UNFORCED (1, statel_exit_exec, "IDLE")
336
337
     /* Determine if a trace is activated for the FDDI model */
338
              tk_trace_on = op_prg_odb_ltrace_active ("fddi_tk");
339
     /* Trap packets arriving from physical layer so that their */
340
     /* FC field can be extracted before evaluating conditions */
341
              if (op_intrpt_type () == OPC_INTRPT_STRM && op_intrpt_strm ()
342
                 != FDDI_LLC_STRM_IN)
343
     /* Acquire the arriving packet. */
344
345
                 pkptr = op_pk_get (FDDI_PHY_STRM_IN);
346
     /* Determine the type of packet by extracting */
347
     /* the frame control field. */
348
                 op_pk_nfd_get (pkptr. "fc", &frame_control);
349
     /* Physical layer arrival flag is set. */
350
                 phy_arrival = 1;
```

```
351
352
               else(
353
      /* The interrupt is not due to a physical layer arrival. */
354
                  phy_arrival = 0;
355
      /* If the interrupt is a remote interrupt with specified code. it
356
         signifies */
357
      /* the reinsertion of the token into the ring after an idle period. This
358
         only */
359
      /* occurs if the token acceleration mechanism is active.
                                                                             */
360
                  if (op_intrpt_type () == OPC_INTRPT_REMOTE && op_intrpt code
361
                      () == FDDIC_TK_INJECT)
362
      /* create a new token */
363
364
                     tk_pkptr = op_pk_create_fmt ("fddi_mac_tk");
365
      /* assign its frame control field */
366
                     op_pk_nfd_set (tk_pkptr, "fc", FDDI_FC_TOKEN);
367
      /* the token is non-restricted */
368
                     op_pk_nfd_set (tk_pkptr, "class", FDDI_TK_NONRESTRICTED);
369
      /* insert it into the ring */
370
                     op_pk_send (tk_pkptr, FDDI_PHY_STRM_OUT);
371
372
                  }
               )
373
374
     /** state (IDLE) transition processing **/
375
            FSM_INIT_COND (TK_RECEIVED)
376
            FSM_TEST_COND (RC_FRAME)
            FSM_TEST_COND (TRT_EXPIRE)
377
378
            FSM_TEST_COND (FRAME_ARRIVAL)
379
            FSM_DFLT_COND
380
            FSM_TEST_LOGIC ("IDLE")
381
            FSM_TRANSIT_SWITCH
382
383
               FSM_CASE_TRANSIT (0, 3, state3_enter_exec, :)
384
               FSM_CASE_TRANSIT (1, 4, state4_enter_exec. :)
385
               FSM_CASE_TRANSIT (2, 7, state7_enter_exec, ;)
386
               FSM_CASE_TRANSIT (3, 8, state8_enter_exec, ;)
               FSM_CASE_TRANSIT (4, 1, statel_enter_exec, ;)
387
388
389
     /** state (ISSUE_TK) enter executives **/
390
391
            FSM_STATE_ENTER_FORCED (2, state2_enter_exec, "ISSUE_TK")
392
393
     /* If the token is sent without having been used, and the station */
```

```
/* has no data to send, then indicate this fact to the */
394
     /* token acceleration mechanism which may have an */
395
     /* oppurtunity to block the token. */
396
               if (!tk_usable && op_q_stat (OPC_QSTAT_PKSIZE) == 0.0)
397
398
                  1
     /* Note that if the token cannot be blocked. */
399
     /* this procedure will forward the token physically. */
400
                  fddi_tk_indicate_no_data (tk_pkptr, my_address,
401
402
                     accum_bandwidth);
403
404
              else{
                 if (tk_trace_on == OPC_TRUE)
405
406
                     sprintf (str0, "Issuing token. accum_bw (%.9f), prop_del
407
                        (%.9f)", accum_bandwidth, Fddi_Prop_Delay);
408
                     op_prg_odb_print_major (str0, OPC_NIL);
409
410
     /* Send out the token packet using the accumulated */
411
412
     /* consumed bandwidth as a scheduling base. */
     /* In the case of the initial spawning of the token */
413
     /* this will be zero; otherwise this variable will */
414
     /* reflect the bandwidth consumed since the last capture */
415
     /* of the usable token. Propagation delay is also accounted for. */
416
                 op_pk_send_delayed (tk_pkptr, FDDI_PHY_STRM_OUT,
417
                     accum_bandwidth + Fddi_Prop_Delay);
418
419
420
              }
     /** state (ISSUE_TK) exit executives **/
421
422
           FSM_STATE_EXIT_FORCED (2, state2_exit_exec, "ISSUE_TK")
423
424
              }
     , ** state (ISSUE_TK) transition processing **/
425
426
           FSM_TRANSIT_FORCE (1, statel_enter_exec, ;)
427
428
     /** state (RCV_TK) enter executives **/
429
           FSM_STATE_ENTER_FORCED (3, state3_enter_exec, "RCV_TK")
430
     /* The arriving packet, when received in the IDLE state */
431
     /* is placed in the variable 'pkptr'. Since it is now */
432
433
     /* known that it is a token, it can be placed in 'tk_pkptr. */
434
              tk_pkptr = pkptr;
435
     /* Load the token's class into the temporary variable 'tk_class.' */
              op_pk_nfd_get (pkptr, "class", &tk_class);
436
     /* If the token is restricted, determine for which station. */
437
```

```
438
               if (tk_class == FDDI_TK_RESTRICTED)
439
     /* Place the station address in the variable 'res_station' */
440
     /* which may factor in to the determination of token usability. */
441
                  op_pk_nfd_get (tk_pkptr, "res_station", &res_station);
442
443
444
     /* Determine if the token is usable: */
445
     /* assume by default that it is not */
     /* Subsequent conditions may override this. */
446
               tk\_usable = 0;
447
     /* The token can only be usable if there are frames enqueued */
448
     /* 27DEC93: the entire bank of subqueues must be checked.
449
     /* starting at the highest priority (corresponding to */
450
451
     /* synchronous traffic), and stopping when a packet is */
     /* found. Then the loop is broken. -Nix
452
453
              NUM_PRIOS = 9;
              for (i = NUM_PRIOS - 1; i > -1; i--)
454
455
456
               if (op_subq_stat (i. OPC_QSTAT_PKSIZE) > 0.0)
457
458
     /* examine the attributes of the packet at the */
459
     /* head of the queue. */
460
     /* fddi_load_frame_attrs (&dest_addr, &svc_class, &pri_level); */
461
                  fddi_load_frame_attrs (dest_addr, &svc_class, &pri_level);
     /* If synchronous data is queued, the token is */
462
     /* necessarily usable, regardless of timing conditions. */
463
464
                  if (svc_class == FDDI_SVC_SYNC)
465
466
                     tk\_usable = 1:
467
                     break;
468
469
                  else{
470
     /* Otherwise, if asynchronous data is queued, it must */
     /* meet several criteria for the token to be usable. */
471
472
     /* The token is only usable only if it is early. */
473
                     if (Late_Ct == 0)
474
475
     /* The token's class must be nonrestricted, unless */
476
     /* this station is involved in the restricted transfer. */
477
                        if (tk_class == FDDI_TK_NONRESTRICTED || res_station
478
                           == my_address || restricted)
479
480
     /* Test the frame's priority assignment against the current TRT */
481
     /* This test uses the priority indirection table T_Pri */
482
     /* so that only packets whose T_Pri [pri_level] exceeds */
     /* the TRT can be transmitted. In other words, by */
483
484
     /* assigning lower values to T_Pri for a given priority */
```

```
/* level, packets of that level will be further restricted */
485
486
     /* from using the ring bandwidth. */
                           if (T_Pri [pri_level] >= fddi_timer_value (TRT))
487
488
489
                               tk\_usable = 1;
490
                                 break:
491
                           }
492
493
                        }
494
495
                  /* closes the "if (op_subq_stat (OPC_QSTAT_PKSIZE) > 0.0"
                        statment */
496
                  }/* closes the "for" loop */
497
     /* If the token is usable, timers must be readjusted. */
498
499
               if (tk_usable)
500
501
     /* The timer adjustment depends on whether the token is early or late.
502
503
                  if (Late_Ct == 0)
504
     /* Transfer the contents of TRT into THT. */
505
                     fddi_timer_copy (TRT, THT);
506
     /* Disable the THT timer. */
507
508
                     fddi_timer_disable (THT);
     /* Reset TRT to time the next rotation. */
509
510
                     op_ev_cancel (TRT_handle);
                     TRT_SET (my_address, op_sim_time () + Fddi_T_Opr);
511
512
                     }
513
                  else{
514
     /* If the token is late, set the THT to its expired */
     /* value, and disable it. This will prevent any */
515
     /* asynchronous transmissions from occuring. */
516
                     fddi_timer_set_value (THT, Fddi_T_Opr);
517
518
                        fddi_timer_disable (THT);
519
     /* clear the Late token counter (note that TRT is not modified, */
520
     /* so that less than a full TTRT remains before TRT expires again. */
521
                     Late_Ct = 0;
522
                  }
523
524
     /* If the token is not usable, different adjustments are made. */
525
              else(
526
     /* Again, the adjustments depend on the lateness of the token */
527
                  if (Late_Ct == 0)
528
                     {
```

```
/* If the token is not late, the TRT is reset to time the next rotation.
529
530
                    op_ev_cancel (TRT_handle);
531
                    TRT_SET (my_address, op_sim_time () + Fddi_T_Opr);
532
533
                    }
534
     /* clear the Late token counter (note that TRT is not modified. */
535
     /* so that less than a full TTRT remains before TRT expires again. */
536
                    Late_Ct = 0:
537
538
     /* also, account for the time needed by the token */
539
     /* to traverse the station, since it is about to be sent. */
540
     /* Note: station latency is not inclusive of token */
541
     /* transmission time, but only of the time required to */
542
     /* process and repeat the token's symbols.
543
                 accum_bandwidth = Fddi_St_Latency;
544
545
546
              }
     /** state (RCV_TK) exit executives **/
547
           FSM_STATE_EXIT_FORCED (3, state3_exit_exec, "RCV_TK")
548
549
550
              }
     /** state (RCV_TK) transition processing **/
551
552
           FSM_INIT_COND (tk_usable)
553
           FSM_DFLT_COND
554
           FSM_TEST_LOGIC ("RCV_TK")
555
           FSM_TRANSIT_SWITCH
556
557
              FSM_CASE_TRANSIT (0, 9, state9_enter_exec, ;)
558
              FSM_CASE_TRANSIT (1, 2, state2_enter_exec, ;)
559
560
     /*-----//
561
     /** state (FR_RCV) enter executives **/
           FSM_STATE_ENTER_FORCED (4, state4_enter_exec, "FR_RCV")
562
563
564
     /* A frame has been received from the physical layer. Note that */
     /* at this time, only the leading edge of the frame has arrived. */
565
     /* Extract the frame's source address (this will be used to */
566
567
     /* determine whether or not to strip the frame from the ring). */
568
              op_pk_nfd_get (pkptr. "src_addr", &src_addr);
569
     /** state (FR_RCV) exit executives **/
570
571
           FSM_STATE_EXIT_FORCED (4, state4_exit_exec, "FR_RCV")
```

```
572
573
             }
     /** state (FR_RCV) transition processing **/
574
           FSM_INIT_COND (STRIP)
575
           FSM_DFLT_COND
576
           FSM_TEST_LOGIC ("FR_RCV")
577
578
          FSM_TRANSIT_SWITCH
579
             FSM_CASE_TRANSIT (0, 5, state5_enter_exec, ;)
580
             FSM_CASE_TRANSIT (1, 6, state6_enter_exec, ;)
581
582
        /*----*/
583
     /** state (FR_STRIP) enter executives **/
584
           FSM_STATE_ENTER_FORCED (5, state5_enter_exec, "FR_STRIP")
585
586
     /* Destroy the frame which has now circulated the entire ring. */
587
588
             op_pk_destroy (pkptr);
589
     /** state (FR_STRIP) exit executives **/
590
           FSM_STATE_EXIT_FORCED (5, state5_exit_exec, "FR_STRIP")
591
592
593
             }
     /** state (FR_STRIP) transition processing **/
594
595
           FSM_TRANSIT_FORCE (1, statel_enter_exec, ;)
596
597
     /** state (FR_REPEAT) enter executives **/
           FSM_STATE_ENTER_FORCED (6, state6_enter_exec, "FR_REPEAT")
598
599
     /* Extract the destination address of the frame. */
600
601
     /* 20FEB94: use a pointer to the array dest_addr, */
602
     /* since referring to dest_addr directly produces */
     /* unexpected results. ·Nix
603
604
             op_pk_nfd_get (pkptr, "dest_addr", &da_ptr);
605
606
     /* printf("*da_ptr: %d; da_ptr: %d; &da_ptr: %d\n", *da_ptr, da_ptr,
607
       &da_ptr); */
     /******************************
608
609
610
             for (i = 0; i < NUM_STATIONS+1; i++);
611
                dest_addr[i] = da_ptr[i];
612
     /*****************
613
614
     /* 02MAR94: print out the address, and the contents. */
```

```
*/
     /* for (i = 0; i < \frac{1}{1} NUM_STATIONS+1; i++)
615
616
     /* printf("1.FR_REPEAT:element: %d, address: %X/%d, content: %d\n",
617
618
     /* i, &(dest_addr[i]), &(dest_addr[i]), dest_addr[i]);*/
619
620
621
     /* O8FEB94: re-initialize counters. -Nix */
622
623
              for me = 0:
624
              count_addees = 0:
     /* 08FEB94: inspect the address field: interested in */
625
                                                                */
626
     /* whether this packet is sent here only, or here and
     /* to others, or to others only.Note that a real packet */
627
     /* would carry all the addresses: the simulation refers */
628
629
     /* to memory locations. -Nix
              for (i = 1; i < NUM_STATIONS+1; i++)
630
631
632
              if (dest_addr[i] == 1)
633
                 count_addees += 1:
634
              }
635
     /* If the frame is for this station. make a copy */
636
     /* of the frame's data field and forward it to */
637
     /* the higher layer. */
638
     /* if (dest_addr == my_address) */
639
     /* O8FEB94: if this packet is addressed only to this
640
     /* station, make a copy of the frame's data field and */
     /* and forward it to the higher layer. -Nix */
641
     /* (a) If the packet is addressed to me only... */
642
643
     /* (note offset applied)
644
              if (dest_addr[my_address+1] == 1 && count_addees == 1)
645
     /* ********************
646
647
     /* printf("Here is Case 1.\n");
     /* **********************
648
649
     /* record total size of the frame (including data) */
650
                 pk_len = op_pk_total_size_get (pkptr);
651
     /* decapsulate the data contents of the frame */
652
     /* 29JAN94: a new field, "pri", has been added to */
653
     /* the fddi_llc_fr packet format in the Parameters */
654
     /* Editor, so that output statistics can be */
655
     /* generated by class and priority. -Nix */
                 op_pk_nfd_get (pkptr, "info", &data_pkptr);
656
657
                 op_pk_nfd_get (pkptr, "pri", &pri_level);
```

```
/* The source and destination address are placed in the */
658
659
     /* LLC's ICI before delivering the frame's contents. */
660
                op_ici_attr_set (to_llc_ici_ptr, "src_addr", src_addr);
661
                op_ici_attr_set (to_llc_ici_ptr, "dest_addr", da_ptr);
662
                op_ici_install (to_llc_ici_ptr);
663
     /* 18FEB94: print out the address, and the contents. */
664
             for (i = 0; i < NUM_STATIONS+1; i++)
665
666
                dest_addr[i] = da_ptr[i]:
     / ***************
667
668
     /* printf("2.FR_REPEAT:element: %d, address: %X/%d, content: %d\n", */
     /* i, &(dest_addr[i]), &(dest_addr[i]); */
669
670
     / *********************
671
672
     /* Because, as noted in the FR_RCV state, only the */
673
     /* frame's leading edge has arrived at this time, the */
674
     /* complete frame can only be delivered to the higher */
     /* layer after the frame's transmission delay has elapsed. */
675
     /* (since decapsulation of the frame data contents has occured. */
676
     /* the original MAC frame length is used to calculate delay) */
677
678
                tx_time = (double) pk_len / FDDI_TX_RATE;
679
                op_pk_send_delayed (data_pkptr, FDDI_LLC_STRM_OUT, tx_time);
680
     7* Note that the standard specifies that the original */
     /* frame should be passed along until the originating station */
681
682
    /* receives it, at which point it is stripped from the ring. */
683
     /* However, in the simulation model, there is no interest */
684
     /* in letting the frame continue past its destination unless */
685
    /* group addresses are used, so that the same frame could be */
686
    /* destined for several stations. Here the frame is stripped *
687
    /* for efficiency as it reaches the destination; if the model */
688
     /* is modified to include group addresses, this should be change_ */
689
     /* so that the frame is copied and the original repeated. */
690
    /* Logic is already present for stripping the frame at the origin. */
691
                op_pk_destroy (pkptr);
692
693
    /* O8FEB94: (b)...or if this packet is not for me at all... -Nix */
694
             else if (dest_addr[my_address+1] == 0)
695
                {
    696
697
    /* printf("Here is Case 2.\n"); */
    698
699
    /* Repeat the original frame on the ring and account for */
700
    /* the latency through the station and the propagation delay */
```

```
701
     /* for a single hop. */
     /* (Only the originating station can strip the frame). */
702
                op_pk_send_delayed (:kptr, FDDI_PHY_STRM_OUT.
703
704
                   Fddi_St_Latency > Fddi_Prop_Delay);
705
     /* OBFEB94: (c)...or if this packet is for me and for others, will */
706
     /* need to send the contents to the SINK, then re-encapsulate the
707
     /* packet for further transmission. Much of this code is */
708
     /* duplicated from the above. -Nix
709
             else if (dest_addr[my_address+1] == 1 && count_addees > 1)
710
711
     712
     /* printf("Here is Case 3.\n"); */
713
     714
     /* record total size of the frame (including data) */
715
716
                pk_len = op_pk_total_size_get (pkptr);
     /* decapsulate the data contents of the frame */
717
                op_pk_nfd_get (pkptr, "info", &data_pkptr);
718
                op_pk_nfd_get (pkptr, "pri". &pri_level);
719
     /* *****************
720
     /* Print out the address of the "info" field information */
721
     /* printf("Case 3: 'info' is located at address %X\n", &data_pkptr); */
722
     723
724
     /* OBMAR94: copy the "info" address into a local variable, so that
725
     /* it may be held for re-installation. -Nix
726
             info_ptr = op_pk_copy(data_pkptr);
727
     /* The source and destination address are placed in the */
728
     /* LLC's ICI before delivering the frame's contents. */
729
                op_ici_attr_set (to_llc_ici_ptr, "src_addr", src_addr);
                op_ici_attr_set (to_llc_ici_ptr. "dest_addr", dest_addr);
730
731
                op_ici_install (to_llc_ici_ptr);
    /* Because, as noted in the FR_RCV state, only the */
732
    /* frame's leading edge has arrived at this time, the */
733
734
    /* complete frame can only be delivered to the higher */
735
    /* layer after the frame's transmission delay has elapsed. */
736
    /* (since decapsulation of the frame data contents has occured, */
737
     /* the original MAC frame length is used to calculate delay) */
738
                tx_time = (double) pk_len / FDDI_TX_RATE;
739
                op_pk_send_delayed (data_pkptr, FDDI_LLC_STRM_OUT, tx time):
```

```
740
     /* O8FEB94: remove this station from the dest_addr array, reassemble */
741
     /* the packet, and send the packet on its way. -Nix */
742
               dest_addr[my_address+1] = 0;
743
               op_pk_nfd_set(pkptr, "src_addr", src_addr);
               op_pk_nfd_set(pkptr, "dest_addr", dest_addr);
744
               op_pk_nfd_set(pkptr.*pri*, pri_level);
745
              op_pk_nfd_set(pkptr,"info", info_ptr);
746
               op_pk_send_delayed (pkptr, FDDI_PHY_STRM_OUT,
747
748
                     Fddi_St_Latency + Fddi_Prop_Delay);
749
                  }
750
               }
751
     /** state (FR_REPEAT) exit executives **/
752
            FSM_STATE_EXIT_FORCED (6, state6_exit_exec, "FR_REPEAT")
753
754
755
     /** state (FR_REPEAT) transition processing **/
756
            FSM_TRANSIT_FORCE (1, statel_enter_exec, :)
757
758
     /** state (TRT_EXP) enter executives **/
759
            FSM_STATE_ENTER_FORCED (7, state7_enter_exec, "TRT_EXP")
760
761
     /* The timer is reset and allowed to continue running. */
762
              TRT_SET (my_address, op_sim_time () + Fddi_T_Opr);
763
     /* The late token counter is incremented. This will */
764
     /* prevent this station from making any asynchronous */
765
     /* transmissions when it next captures the token. */
766
              Late_Ct++;
767
              }
768
     /** state (TRT_EXP) exit executives **/
           FSM_STATE_EXIT_FORCED (7, state7_exit_exec, "TRT_EXP")
769
770
771
              }
     /** state (TRT_EXP) transition processing **/
772
773
           FSM_TRANSIT_FORCE (1, statel_enter_exec, ;)
774
775
     /** state (ENCAP) enter executives **/
776
           FSM_STATE_ENTER_FORCED (8, state8_enter_exec, "ENCAP")
777
778
     /* A frame has arrived from a higher layer; place it in 'pdu_ptr'. */
779
              pdu_ptr = op_pk_get (op_intrpt_strm ());
```

```
780
    /* Also get the interface control information */
781
    /* associated with the new frame. */
782
            ici_ptr = op_intrpt_ici ();
783
            if (ici_ptr == OPC_NIL)
784
               sprintf (error_string, "Simulation aborted; error in object
785
786
                  (%d)", op_id_self ());
               op_sim_end (error_string, "fddi_mac: required ICI not
787
                  received", " ", " ");
788
789
790
    /* Extract the requested service class */
    /* (e.g. synchronous or asynchronous). */
791
            if (op_ici_attr_exists (ici_ptr, "svc_class"))
792
               op_ici_attr_get (ici_ptr. "svc_class", &svc_class);
793
            else svc_class = FDDI_SVC_ASYNC;
794
    795
    /* for (i=0; i<NUM_STATIONS+1; i++)</pre>
796
    /* printf("ENCAP a.Field:%d, Address(dec/hex):%d/%%, Contents:%d\n",
797
798
       */
799
    /* i, &(dest_addr[i]), &(dest_addr[i]), dest_addr[i]); */
800
801
    /* Extract the destination address. */
802
    /* 20FEB94: use a pointer to the array, since the */
803
    /* use of dest_array as its own pointer causes */
804
    /* unexpected results. -Nix */
            op_ici_attr_get (ici_ptr, "dest_addr", &da_ptr);
805
    806
807
    /* printf("&da_ptr: %d/%X; da_ptr: %d\n\n", &da_ptr, &da_ptr, da_ptr);
808
809
    /* for (i=0; i<NUM_STATIONS+1; i++)</pre>
810
    /* printf("%d: &da_ptr: %d/%X; da_ptr: %d\n\n", i, &da_ptr, &da_ptr,
       da_ptr); */
811
    /***********************
812
813
            for (i=0; i<NUM_STATIONS+1; i++)</pre>
814
815
            dest_addr[i] = da_ptr[i];
816
    817
    /* for (i=0; i<NUM_STATIONS+1; i++)</pre>
818
    /* printf("ENCAP b.Field:%d, Address(dec/hex):%d/%X, Contents:%d\n",
819
820
       */
    /* i, &(dest_addr[i]), &(dest_addr[i]), dest_addr[i]); */
821
```

```
822
823
     /* If the frame is asynchronous, the priority and */
824
     /* requested token class parameter may be specified. */
825
               if (svc_class == FDDI_SVC_ASYNC)
826
     /* Extract the requested priority level. */
827
                  if (op_ici_attr_exists (ici_ptr, "pri"))
828
829
                     op_ici_attr_get (ici_ptr, "pri", &req_pri);
830
                  else req_pri = 0;
831
     /* Extract the token class (restrictred or non-restricted). */
                  if (op_ici_attr_exists (ici_ptr, "tk_class"))
832
                     op_ici_attr_get (ici_ptr, "tk_class", &req_tk_class);
833
834
                 else req_tk_class = FDDI_TK_NONRESTRICTED;
835
                  }
     /* Compose a mac frame from all these elements. */
836
837
              mac_frame_ptr = op_pk_create_fmt ("fddi_mac_fr");
              op_pk_nfd_set (mac_frame_ptr, "svc_class", svc_class);
838
              op_pk_nfd_set (mac_frame_ptr, "dest_addr", dest_addr);
839
              op_pk_nfd_set (mac_frame_ptr, "src_addr", my_address);
840
              op_pk_nfd_set (mac_frame_ptr, "info", pdu_ptr);
841
              if (svc_class == FDDI_SVC_ASYNC)
842
843
                 op_pk_nfd_set (mac_frame_ptr, "tk_class", req_tk_class);
844
                 op_pk_nfd_set (mac_frame_ptr, "pri", req_pri);
845
846
847
     /* O4JAN94: if the frame 1s synchronous, assign it a separate */
848
     /* priority so that 1t may be assigned its own subqueue, and */
849
     /* thereby be assigned its own probe for monitoring. -Nix */
850
              if (svc_class == FDDI_SVC_SYNC)
851
852
              op_pk_nfd_set (mac_frame_ptr, "pri", 8);
853
854
     /* Assign the frame control field, which in the model */
855
     /* is used to distinguish between tokens and ordinary */
856
     /* frames on the ring. */
857
              op_pk_nfd_set (mac_frame_ptr, "fc", FDDI_FC_FRAME);
858
     /* Enqueue the frame at the tail of the queue. */
859
     /* 27DEC93: at the tail of the prioritized queue.
860
     /* 04JAN94: must distinguish between synch & asynch. */
861
              if (svc_class == FDDI_SVC_ASYNC)
862
863
              op_subq_pk_insert (req_pri, mac_frame_ptr, OPC_QPOS_TAIL);
```

```
864
865
               if (svc_class == FDDI_SVC_SYNC)
866
              op_subq_pk_insert (8, mac_frame_ptr, OPC_QPOS_TAIL);
867
868
869
     /* if this station has not yet registered its intent to */
870
     /* use the token, it may do so now since it has data to send */
871
               if (!tk_registered)
872
873
                  fddi_tk_register ();
874
                  tk_registered = 1:
875
876
               }
877
     /** state (ENCAP) exit executives **/
878
           FSM_STATE_EXIT_FORCED (8, state8_exit_exec. "ENCAP")
879
880
               }
881
     /** state (ENCAP) transition processing **/
882
           FSM_TRANSIT_FORCE (1, statel_enter_exec. :)
883
884
     /** state (TX_DATA) enter executives **/
           FSM_STATE_ENTER_FORCED (9, state9_enter_&xec. "TX_DATA")
885
886
887
     /* In this state, frames are transmitted until the */
888
     /* token is no longer usable. Frames are taken from */
889
     /* the single input queue in FIFO order. */
890
     /* Reset the accumulator used to keep track of bandwidth */
891
     /* consumed by the transmissions. Because all the transmissions */
892
     /* are scheduled to happen at the appropriate times, but */
893
     /* these schedulings occur instantly. this accumulator serves */
894
     /* as the scheduling base for the transmissions. */
     /* In other words, each successively transmitted frame */
896
     /* is delayed relative to the previous one by the time which */
897
     /* the latter took to send. At the end of transmission (e.g. */
     /* when the token is no longer usable), this accumulator */
898
     /* serves to delay the forwarding of the token. */
899
900
              accum_bandwidth = 0.0;
901
     /* Note that, because all tranmsmissions are */
     /* scheduled, the value of the THT timer will not progress */
902
     /* between shcedulings (these all happen in zero time), and so */
903
     /* the variable 'tht_value' is used to emulate the timer's progress. */
904
905
              tht_value = fddi_timer_value (THT);
     /* Reset an accumulator which reflects the consumed */
906
```

```
907
     /* synchronous bandwidth. */
908
              accum_sync = 0.0;
     /* Reset counters for transmitted frames and bits. */
909
910
              num_frames_sent = 0;
911
              num_bits_sent = 0:
     /* The transmission sequence must end if the input queue */
912
913
     /* becomes exhausted. Other termination conditions are */
914
     /* embedded in the loop. */
     /* 27DEC93: modify the loop to accomodate subqueue structure. */
915
     /* A "for" loop is imposed over the original "while" loop. */
916
     /* First, reset the break marker, "punt". -Nix */
917
918
              punt = 0:
919
              for (i = NUM_PRIOS - 1; i > -1; i--)
920
921
              while (op_subq_stat (i.OPC_QSTAT_PKSIZE) > 0.0)
922
923
     /* Remove the next frame for transmission. */
924
                  pkptr = op_subq_pk_remove (i, OPC_QPOS_HEAD);
925
     /* Obtain the frame's service class. */
926
                  op_pk_nfd_get (pkptr, "svc_class", &svc_class);
927
     /* Synchronous and asynchronous frames are treated differently. */
928
                  if (svc_class == FDDI_SVC_SYNC)
929
930
     /* Obtain the frame's length, and compute */
931
     /* the time required to transmit it. */
932
                    pk_len = op_pk_total_size_get (pkptr);
933
                     tx_time = (double) pk_len / FDDI_TX_RATE;
934
     /* Check if synchronous bandwidth allocation for this */
935
     /* station would be exceeded if the transmission were to occur. */
936
                     if (accum_sync + tx_time > sync_bandwidth)
937
938
     /* The frame could not be sent without exceeding */
939
     /* the allocated synchronous bandwidth, */
940
     /* so it is replaced on the queue. */
     /* 27DEC93: in this case, i is the highest priority, */
941
942
     /* which is reserved for synchronous traffic. -Nix
                        op_subq_pk_insert (i. pkptr. OPC_QPOS_HEAD);
943
944
     /* Exit the transmission loop since the frame */
     /* transmission request cannot be honored. */
945
946
                        punt = 1;
947
                        break;
948
949
                     else(
     /* Send the frame into the ring after other frames have completed. */
950
```

```
/* Also, account for its proagation delay; because the token propagation
951
952
953
     /* delay and the frame propagation delay must be consistent, and the */
954
     /* token propagation delay is specified as a ring parameter (i.e.
        stations */
955
956
     /* are assumed to be equal y spaced), the ring is intended to run with
957
     /* the "delay" attributes of point-to-point links set at zero. */
958
959
                       op_pk_send_delayed (pkptr, FDDI_PHY_STRM_OUT,
                          accum_bandwidth + Fddi_Prop_Delay);
960
     /* increase the consumed bandwidth to reflect this */
961
     /* transmission. Also increase synchronous consumption. */
962
963
                       accum_bandwidth += tx_time;
964
                       accum_sync += tx_time;
     /* Increase counters for transmitted bits and frames. */
965
966
                       num_frames_sent++;
967
                       num_bits_sent += pk_len;
968
969
                    }
970
                 else{
971
     /* The request enqueued at the head of the queue is */
     /* asynchronous. It may only be honored if the THT timer */
972
     /* has not expired. */
973
974
                    if (tht_value >= Fddi_T_Opr)
975
976
     /* replace the packet on the queue and exit the transmission loop. */
                       op_subq_pk_insert (i, pkptr, OPC_QPOS_HEAD);
977
978
                       punt = 1;
979
                       break:
980
981
                    else{
982
     /* Obtain the priority assignment of the frame. */
983
                       op_pk_nfd_get (pkptr, "pri", &pri_level);
984
     /* If the packet's assigned priority level */
     /* is too low for it to be serviced, then exit the loop */
985
986
     /* after replacing the packet in the queue. */
     /* *******************************
987
988
     /* O8MAR94: print the values to be compared. -Nix */
     /* printf("1. TX_DATA: T_Pri[%d] < tht_value ?\n", i); */</pre>
989
     /* printf("%d < %d ?\n", T_Pri [i], tht_value); */
990
            **************
991
992
                       if (T_Pri [pri_level] < tht_value)</pre>
993
994
                          op_subq_pk_insert (i, pkptr, OPC_QPOS_HEAD);
995
                          punt = 1:
```

```
996
                            break:
 997
 998
      /* Obtain the frame's length, and compute the time */
 999
      /* which would be required to transmit it. */
1000
                         pk_len = op_pk_total_size_get (pkptr);
1001
                         tx_time = (double) pk_len / FDDI_TX_RATE;
      /* Determine the requested token class to be */
1002
1003
      /* released after this frame is transmitted. */
1004
                         op_pk_nfd_get (pkptr, "tk_class", &tk_class);
1005
      /* If the station is in restricted mode, then it may */
1006
      /* exit this mode if the class is now nonrestricted */
1007
      /* or if the restricted peer is not the addressee. */
1008
                         if (restricted)
1009
1010
      /* Determine the destination address for the new packet. */
1011
                            op_pk_nfd_get (pkptr, "dest_addr", dest_addr);
1012
      /* if (tk_class == FDDI_TK_NONRESTRICTED ||
                                                     */
1013
            res_peer != dest_addr) */
1014
1015
                         if (tk_class == FDDI_TK_NONRESTRICTED |)
1016
                            dest_addr[res_peer] != 1)
1017
1018
      /* Exit restricted mode */
1019
                               restricted = 0;
1020
      /* Modify the token to reflect the mode change. */
1021
                               op_pk_nfd_set (tk_pkptr, "class",
1022
                                   FDDI_TK_NONRESTRICTED);
1023
1024
                         else{
1025
1026
      /* Determine the class of the current captured token. */
1027
                            op_pk_nfd_get (tk_pkptr, "class",
1028
      &current_tk_class);
      /* When not in restricted mode, this mode may be entered */
1029
1030
      /* if the passed packet has the appropriate token class requested, */
      /* and the token is not already restricted. */
1031
1032
                            if (tk_class == FDDI_TK_RESTRICTED &&
1033
                               current_tk_class != FDDI_TK_RESTRICTED)
1034
1035
      /* Enter restricted mode. */
1036
                               restricted = 1;
1037
      /* Store the address of the resticted peer station. */
```

```
/* op_pk_nfd_get (pkptr, "dest_addr", &res_peer); */
1038
1039
                             op_pk_nfd_get (pkptr, "dest_addr",
1040
                                &dest_addr[res_peer]);
1041
      /* Modify the token to reflect the mode change. */
1042
                             op_pk_nfd_set (tk_pkptr, "class",
1043
                                FDDI_TK_RESTRICTED);
1044
                             op_pk_nfd_set (tk_pkptr, "res_station",
1045
      res_peer):
1046
                             }
                          }
1047
     /* Send the frame once previous transmissions have completed. */
1048
1049
      /* Account for propagation delay as well. */
1050
                       op_pk_send_delayed (pkptr, FDDI_PHY_STRM_OUT,
1051
                          accum_bandwidth + Fddi_Prop_Delay);
1052
      /* Increment THT emulation variable, and consumed bandwidth accumulator.
1053
1054
      /* O8MAR94: note that tht_value is incrementing, not decrementing. -Nix
1055
         */
1056
                       tht_value += tx_time:
1057
                       accum_bandwidth += tx_time;
      1058
1059
      /* OBMAR94: print the Token Holding Time value. -Nix */
1060
      /* printf("2. TX_DATA: tht_value is %d\n", tht_value);
      1061
1062
     /* Increase counters for transmitted bits and frames. */
1063
                       num_frames_sent++;
1064
                       num_bits_sent += pk_len;
1065
                       }
1066
                 }/* closes the 'while' loop */
1067
1068
              if (punt == 1)
                              /* If the 'while' loop was broken, */
1069
1070
              punt = 0;/* then reset the 'break' marker, */
              break;/* and break out of the 'for' loop too. */
1071
1072
              }/* closes the 'for' loop. */
1073
1074
     /* Since the token is about to be sent, its transmission time */
     /* must be reflected in the accumulated bandwidth. This is not */
1075
1076
     /* done in the ISSUE_TK state because when the token is merely */
     /* repeated, full transmission delay is not required, only */
1077
1078
     /* a small delay for repeating.
              accum_bandwidth += FDDIC_TOKEN_TX_TIME;
1079
```

```
1080
      /* If the station has no more data to send (synchronous or */
      /* asynchronous), it should indicate this to the token acceleration */
1081
1082
      /* mechanism by deregistering its interest in the token. */
1083
      /* 27DEC94: the original code must be modified to include a check */
      /* of subqueues. -Nix */
1084
1085
                q_{check} = 1;
1086
                for (i = NUM_PRIOS - 1; i < -1; i--)
1087
                if (op_subq_stat (i, OPC_QSTAT_PKSIZE) == 0.0)
1088
1089
1090
                   q_{check} = 0;
1091
1092
                else {
                   q_{check} = 1;
1093
1094
                   break:
1095
1096
1097
                if (tk_registered && q_check == 0)
1098
1099
                   tk_registered = 0:
1100
                   fddi_tk_deregister ();
1101
1102
                }
      /** state (TX_DATA) exit executives **/
1103
1104
             FSM_STATE_EXIT_FORCED (9, state9_exit_exec, "TX_DATA")
1105
1106
                }
1107
      /** state (TX_DATA) transition processing **/
            FSM_TRANSIT_FORCE (2, state2_enter_exec, ;)
1108
1109
1110
      /** state (CLAIM) enter executives **/
             FSM_STATE_ENTER_UNFORCED (10, state10_enter_exec, "CLAIM")
1111
1112
      /* Obtain this station's object id which is used */
1113
1114
      /* to access the station's attribute assignments. */
1115
               my_objid = op_id_self ();
      /* Using the object id, obtain the ring id. */
1116
      /* The ring id is used by macros defined in the */
1117
      /* header block to obtain "ring-global" values, */
1118
1119
      /* values shared by all stations on a ring. */
               op_ima_obj_attr_get (my_objid, "ring_id", &ring_id);
1120
      /* Initialize global variable values. */
1121
1122
               Fddi_Tk_Blocked = 0;
1123
               Fddi_Num_Stations = 0;
```

```
1124
                Fddi_Num_Registered = 0;
      /* Using the object id, obtain the value of 'T_Req', */
1125
1126
      /* the value of TTRT requested by this station. */
1127
                op_ima_obj_attr_get (my_objid, "T_Req", &T_Req);
1128
      /* The lowest value of T_Req becomes T_Opr for the ring as a whole. */
                if (T_Req < Fddi_T_Opr || Fddi_Claim_Start)</pre>
1129
1130
      /* The T_Req for this station is lower than any other to date */
1131
      /* so it is installed in the T_Opr variable. */
1132
1133
                   Fddi_T_Opr = T_Req;
      /* The flag indicating that the claim process is just */
1134
1135
      /* beginning may now be cleared. */
1136
                   Fddi_Claim_Start = 0;
1137
      /* Request a self interrupt from the Simulation Kernel at the current */
1138
      /* time so that after all stations have executed their claim states.
1139
      /* they can proceed with initializations. This is necessasary */
1140
      /* because some initializations are based in the value of T_Opr */
1141
      /* and it must therefore be known that all stations have settled */
1142
1143
      /* on a final value. */
               op_intrpt_schedule_self (op_sim_time (), 0);
1144
1145
      /** blocking after enter executives of unforced state. **/
1146
1147
            FSM_EXIT (21,fddi_mac_mult)
      /** state (CLAIM) exit executives **/
1148
            FSM_STATE_EXIT_UNFORCED (10, statel0_exit_exec, "CLAIM")
1149
1150
               }
1151
      /** state (CLAIM) transition processing **/
1152
            FSM_TRANSIT_FORCE (0, state0_enter_exec, ;)
1153
1154
1155
1156
         FSM_EXIT (10,fddi_mac_mult)
1157
1158
      void
      fddi_mac_mult_svar (prs_ptr,var_name,var_p_ptr)
1159
1160
         fddi_mac_mult_state *prs_ptr;
                                     *var_name, **var_p_ptr;
1161
         char
1162
1163
         FIN (fddi_mac_mult_svar (prs_ptr))
```

```
1164
          *var_p_ptr = VOS_NIL;
1165
          if (Vos_String_Equal ("ring_id" , var_name))
1166
             *var_p_ptr = (char *) (&prs_ptr->sv ring id):
1167
          if (Vos_String_Equal ("THT" , var_name))
1168
             *var_p_ptr = (char *) (&prs_ptr->sv_THT);
          if (Vos_String_Equal ("T_Req" , var_name))
1169
1170
             *var_p_ptr = (char *) (&prs_ptr->sv_T_Req);
          if (Vos_String_Equal ("T_Pri" , var_name))
1171
1172
             *var_p_ptr = (char *) (prs_ptr->sv_T_Pri):
1173
          if (Vos_String_Equal ("my_objid" , var_name))
1174
             *var_p_ptr = (char *) (&prs_ptr->sv_my_objid);
          if (Vos_String_Equal ("spawn_token" , var_name))
1175
1176
             *var_p_ptr = (char *) (&prs_ptr->sv_spawn_token);
1177
          if (Vos_String_Equal ("my_address" , var_name))
1178
             *var_p_ptr = (char *) (&prs_ptr->sv_my_address);
1179
          if (Vos_String_Equal ("tk_pkptr" , var_name))
1180
             *var_p_ptr = (char *) (&prs_ptr->sv_tk_pkptr);
1181
          if (Vos_String_Equal ("sync_bandwidth" , var_name))
1182
             *var_p_ptr = (char *) (&prs_ptr->sv_sync_bandwidth);
1183
          if (Vos_String_Equal ("sync_pc" , var_name))
1184
             *var_p_ptr = (char *) (&prs_ptr->sv_sync_pc);
1185
          if (Vos_String_Equal ("restricted" . var_name))
1186
             *var_p_ptr = (char *) (&prs_ptr->sv_restricted);
          if (Vos_String_Equal ("res_peer" , var_name))
1187
1188
             *var_p_ptr = (char *) (&prs_ptr->sv_res_peer);
1189
          if (Vos_String_Equal ("tk_registered" , var_name))
1190
             *var_p_ptr = (char *) (&prs_ptr->sv_tk_registered);
1191
         if (Vos_String_Equal ("to_llc_ici_ptr" , var_name))
1192
             *var_p_ptr = (char *) (&prs_ptr->sv_to_llc_ici_ptr);
1193
         if (Vos_String_Equal ("tk_trace_on" , var_name))
1194
             *var_p_ptr = (char *) (&prs_ptr->sv_tk_trace_on);
1195
         FOUT:
1196
         }
1197
      void
1198
      fddi_mac_mult_diag ()
1199
1200
      /* Packets and ICI's */
1201
         Packet*
                         mac_frame_ptr:
1202
         Packet*
                         pdu_ptr;
1203
         Packet*
                         pkptr;
1204
         Packet*
                         data_pkptr;
1205
         Ici*
                         ici_ptr;
1206
      /* Packet Fields and Attributes */
1207
         int
                         req_pri, svc_class, req_tk_class;
1208
         int
                         frame_control, src_addr:
1209
         int
                         pk_len, pri_level;
1210
         static
```

```
1211
          int
                          *da_ptr, dest_addr[];
1212
      /* Token - Related */
1213
          int
                          tk_usable, res_station, tk_class;
1214
          int
                          current_tk_class:
1215
          double
                          accum_sync;
1216
      /* Timer - Related */
1217
          double
                         tx_time, timer_remaining, accum_bandwidth;
          double
1218
                         tht_value:
1219
      /* Miscellaneous */
1220
          int
1221
          int
                          spawn_station, phy_arrival;
1222
          char
                      error_string [512];
1223
          int
                         num_frames_sent, num_bits_sent;
1224
      /* 26DEC93: loop management variables, used in RCV_TK */
1225
      /* and ENCAP states. -Nix */
1226
                NUM_PRIOS:
          int
1227
          int
                punt:
1228
                q_check;
         int
1229
      /* 08FEB94: case management variables, used in FR_REPEAT. -Nix */
1230
         int
                for_me;
1231
         int
                count_addees;
1232
      /* OBMAR94: "field holding" variables, used in FR_REPEAT. -Nix */
1233
         Packet* info_ptr:
1234
         FIN (fddi_mac_mult_diag ())
      /* Print out values of timers, and late token counter. */
1235
1236
      /* Also print out data about restricted mode.
      /* (This code may be executed by the simulation debugger */
1237
1238
      /* by invoking the command 'modprint').
1239
         sprintf (str
                         mers (count upwards): TRT (%.9g), THT (%.9g)",
1240
            fddi_timer_value (TRT), fddi_timer_value (THT));
         sprintf (strl, "Late_ct (%d)", Late_Ct);
1241
1242
         op_prg_odb_print_major (str0, str1, OPC_NIL);
1243
         if (restricted)
            sprintf (str0, "token is in restricted dialog with (%d)\n",
1244
1245
                res_peer);
         else sprintf (str0, "token is unrestricted\n");
1246
1247
         op_prg_odb_print_major (str0, OPC_NIL);
1248
         FOUT:
1249
         }
1250
      void
```

```
1251
      fddi_mac_mult_terminate ()
1252
1253
      /* Packets and ICI's */
1254
          Packet*
                          mac_frame_ptr;
1255
          Packet*
                          pdu_ptr:
1256
          Packet*
                          pkptr;
1257
          Packet*
                          data_pkptr;
1258
          Ici*
                          ici_ptr;
1259
      /* Packet Fields and Attributes */
1260
                          req_pri, svc_class, req_tk_class;
          int
1261
          int
                          frame_control, src_addr;
1262
          int
                          pk_len, pri_level;
1263
          static
                          *da_ptr, dest_addr[]:
1264
          int
      /* Token - Related */
1265
1266
          int
                          tk_usable, res_station, tk_class;
1267
          int
                          current_tk_class;
1268
         double
                          accum_sync;
1269
      /* Timer - Related */
1270
          double
                          tx_time, timer_remaining, accum_bandwidth;
1271
         double
                          tht_value:
      /* Miscellaneous */
1272
1273
         int
1274
          int
                          spawn_station, phy_arrival;
1275
         char
                      error_string [512]:
1276
                          num_frames_sent, num_bits_sent;
         int
1277
      /* 26DEC93: loop management variables, used in RCV_TK */
1278
      /* and ENCAP states. -Nix
1279
                NUM PRIOS:
         int
1280
         int
                punt:
1281
         int
                q_check;
1282
      /* O8FEB94: case management variables, used in FR_REPEAT. -Nix */
1283
         int
                for_me;
1284
         int
                count_addees;
1285
      /* OBMAR94: "field holding" variables, used in FR_REPEAT. -Nix */
1286
         Packet* info_ptr:
1287
         FIN (fddi_mac_mult_terminate ())
1288
         FOUT:
1289
         }
1290
      Compcode
1291
      fddi_mac_mult_init (pr_state_pptr)
```

```
1292
          fddi_mac_mult_state
                                      **pr_state_pptr;
1293
         static VosT_Cm_Obtype obtype = OPC_NIL;
1294
1295
         FIN (fddi_mac_mult_init (pr_state_pptr))
1296
          if (obtype == OPC_NIL)
1297
1298
             if (Vos_Catmem_Register ("proc state vars (fddi_mac_mult)", sizeof
                (fddi_mac_mult_state), Vos_Nop, &obtype) == VOSC_FAILURE)
1299
1300
                FRET (OPC_COMPCODE_FAILURE)
1301
             }
         if ((*pr_state_pptr = (fddi_mac_mult_state*) Vos_Catmem_Alloc
1302
1303
             (obtype, 1)) == OPC_NIL)
1304
             FRET (OPC_COMPCODE_FAILURE)
1305
         else
1306
             (*pr_state_pptr)->current_block = 20;
1307
1308
             FRET (OPC_COMPCODE_SUCCESS)
1309
1310
         )
      /** The procedures defined in this section serve **/
1311
      /** to simplify the code in the main body of the **/
1312
1313
      /** process model by providing primitives for timer **/
1314
      /** manipulation.. **/
1315
         fddi_timer_disable (timer_ptr)
1316
             FddiT_Timer*
                                timer_ptr:
1317
1318
      /* if the timer is already disabled, do nothing */
1319
            if (timer_ptr->enabled)
1320
1321
      /* disable the timer */
1322
                timer_ptr->enabled = 0;
      /* reassign the accumulated time so far */
1323
1324
               timer_ptr->accum = op_sim_time () - timer_ptr->start_time;
1325
                }
1326
            )
1327
         fddi_timer_enable (timer_ptr)
1328
            FddiT_Timer*
                               timer_ptr;
1329
1330
      /* if the timer is already enabled, simply return */
1331
            if (!timer_ptr->enabled)
1332
1333
      /* reenable the timer */
1334
               timer_ptr->enabled = 1:
```

```
1335
       /* set the start time to the current time */
 1336
       /* less the accumulated time so far */
1337
                 timer_ptr->start_time = op_sim_time () - timer_ptr->accum:
1338
1339
             }
1340
          fddi_timer_expired (timer_ptr)
             FddiT_Timer*
1341
                                timer_ptr;
1342
1343
             if (fddi_timer_remaining (timer_ptr) <= 0.0)</pre>
1344
                 return 1:
1345
             else return 0:
1346
             }
1347
          double
1348
          fddi_timer_remaining (timer_ptr)
1349
             FddiT_Timer*
                                timer_ptr:
1350
1351
       /* if the timer is enabled, update the accumulated time */
1352
             if (timer_ptr->enabled)
1353
1354
                timer_ptr->accum = op_sim_time () - timer_ptr->start_time:
1355
                }
1356
       /* return the timer remaining before expiration */
1357
       /* a non-positive value indicates an expired timer */
1358
             return (timer_ptr->target_accum - timer_ptr->accum);
1359
             }
1360
          double
1361
          fddi_timer_value (timer_ptr)
1362
             FddiT_Timer*
                                timer_ptr;
1363
1364
      /* if the timer is enabled, update the accumulated time */
1365
             if (timer_ptr->enabled)
1366
1367
                timer_ptr->accum = op_sim_time () - timer_ptr->start_time;
1368
1369
             return (timer_ptr->accum);
1370
1371
          fddi_timer_set_value (timer_ptr, value)
1372
             FddiT_Timer*
                                timer_ptr;
1373
            double
                                value:
1374
1375
            timer_ptr->accum = value:
1376
1377
         fddi_timer_copy (from_timer_ptr, to_timer_ptr)
```

```
FddiT_Timer*
1378
                                from_timer_ptr:
1379
             FddiT_Timer*
                                to_timer_ptr;
1380
             Vos_Copy_Memory (from_timer_ptr, to_timer_ptr, sizeof
1381
                (FddiT_Timer));
1382
1383
         fddi_timer_set (timer_ptr, duration)
1384
            FddiT_Timer*
                               timer_ptr;
1385
1386
      /* clear out accumulated time */
1387
1388
            timer_ptr->accum = 0.0;
      /* assign the timer duration */
1389
1390
            timer_ptr->target_accum = duration;
1391
      /* assign the current time */
1392
            timer_ptr->start_time = op_sim_time ();
      /* enable the timer */
1393
1394
            timer_ptr->enabled = 1;
1395
             }
1396
         FddiT_Timer*
1397
         fddi_timer_create ()
1398
1399
            FddiT_Timer*
                               timer_ptr:
      /* allocate memory for a timer structure */
1400
            timer_ptr = (FddiT_Timer*) malloc (sizeof (FddiT_Timer));
1401
      /* initialize the timer in the disabled mode */
1402
1403
            fddi_timer_init (timer_ptr);
      /* return the timer's address */
1404
1405
            return (timer_ptr);
1406
1407
         fddi_timer_init (timer_ptr)
1408
            FddiT_Timer*
                               timer_ptr;
1409
1410
      /* the timer is initially disabled */
1411
            timer_ptr->enabled = 0;
1412
      /* the accumulated time is zero */
1413
            timer_ptr->accum = 0.0;
1414
      /* the target accumulated time is infinite */
1415
            timer_ptr->target_accum = VOS_DOUBLE_INFINITY;
```

```
/* the start time is now */
1416
1417
             timer_ptr->start_time = op_sim_time ();
1418
         fddi_station_register (address, objid)
1419
1420
             Objid
                            objid:
1421
             int
                                address:
1422
      /* Fill an entry in the table which maps station */
1423
1424
      /* addresses to OPNET object ids */
             FIN (fddi_station_register (address. objid))
1425
             Fddi_Address_Table [address] = objid;
1426
      /* Keep track of total number of stations on the ring */
1427
1428
             Fddi_Num_Stations++:
1429
             FOUT
1430
             }
1431
         fddi_tk_register ()
1432
1433
      /* Register the station's intent to use the token. */
      /* This should be done whenever an unregistered */
1434
      /* station obtains new data to transmit. */
1435
1436
            FIN (fddi_tk_regsister ())
      /* increase the number of registered stations */
1437
1438
            Fddi_Num_Registered++;
1439
      /* if the token is currently blocked, unblock it */
1440
            if (Fddi_Tk_Blocked && Fddi_Tk_Accelerate)
1441
1442
               fddi_tk_unblock ();
1443
1444
            FOUT
1445
            }
1446
         fddi_tk_deregister ()
1447
1448
      /* Cancel the station's intent to use the token.
1449
      /* This should be done whenever a registered */
1450
      /* station exhausts its transmittable data.
1451
            FIN (fddi_tk_deregsister ())
1452
      /* decrease the number of registered stations */
1453
            Fddi_Num_Registered--;
1454
            FOUT
```

```
1455
             }
         fddi_tk_indicate_no_data (token, address, delay)
1456
             Packet*
                             token:
1457
                             address:
1458
             int
             double
                            delay:
1459
1460
             FIN (fddi_tk_indicate_no_data (token, address, delay))
1461
      /* The calling station is indicating that it has captured */
1462
      /* the token, but has no data to send. If no other stations */
1463
      /* have data to send either, the token may be blocked to gain */
1464
      /* simulation efficiency.
1465
            if (Fddi_Num_Registered == 0 && Fddi_Tk_Accelerate)
1466
1467
                fddi_tk_block (token, address);
1468
1469
            elsel
1470
1471
      /* If the token cannot be blocked, send it into the ring. */
1472
               op_pk_send_delayed (token, FDDI_PHY_STRM_OUT,
                   delay + Fddi_Prop_Delay);
1473
1474
            FOUT
1475
1476
             }
1477
         fddi_tk_block (token. address)
1478
             Packet*
                            token:
1479
            int
                               address:
1480
1481
            int
                            1:
1482
            FIN (fddi_tk_block (token, address))
1483
      /* Record the address of the blocking station and blocking time. */
1484
            Fddi_Tk_Block_Base_Time = op_sim_time ();
            Fddi_Tk_Block_Base_Station = address;
1485
1486
            if (tk_trace_on -- OPC_TRUE)
1487
               sprintf (str0, "Blocking Token: station (%d), time (%.9f)",
1488
                   Fddi_Tk_Block_Base_Station, Fddi_Tk_Block_Base_Time);
1489
1490
               op_prg_odb_print_major (str0, OPC_NIL);
1491
1492
      /* Indicate that the token is blocked */
1493
            Fddi_Tk_Blocked = 1:
1494
      /* discard the token packet; another one will be */
1495
      /* created when the token is unblocked. */
1496
            op_pk_destroy (token);
```

```
/* Cancel TRT timers at all MAC interfaces; otherwise these */
1497
1498
      /* timers may continue to expitr during the idle period, */
1499
      /* generating unnecessary events.
1500
            if (tk_trace_on == OPC_TRUE)
1501
1502
               sprintf (str0, "Canceling timers for (%d) stations",
1503
                   Fddi_Num_Stations);
               op_prg_odb_print_major (str0, OPC_NIL);
1504
1505
1506
            for (i = 0; i < Fddi_Num_Stations; i++)
1507
      /* Retain the time at which the TRT would have expired; */
1508
      /* this is used for calculations when the token is */
1509
      /* reinjected into the ring.
1510
               Fddi_Trt_Exp_Time [i] = op_ev_time (Fddi_Trt_Handle [i]);
1511
      /* Cancel the TRT expiration event. */
1512
               op_ev_cancel (Fddi_Trt_Handle [i]);
1513
1514
            FOUT
1515
1516
            }
         fddi_tk_unblock ()
1517
1518
                            elapsed_time, first_tk_rx, last_tk_rx;
1519
            double
1520
            double
                            tk_lap_time, next_time, current_time;
                            dbl_num_hops, num_tk_rx, floor (), ceil ();
1521
            double
                            i, num_hops, next_station;
1522
            int
1523
            FIN (fddi_tk_unblock ())
1524
      /* reset the blocking indicator */
1525
            Fddi_Tk_Blocked = 0;
      /* Get the current time, used for many calculations below */
1526
            current_time = op_sim_time ();
1527
1528
            if (tk_trace_on == OPC_TRUE)
1529
1530
               sprintf (str0, "Unblocking token for ring (%d)", ring_id);
1531
               op_prg_odb_print_major (str0, OPC_NIL);
1532
1533
      /* For all stations on the ring, adjust TRT timer and Late_Ct flag. */
1534
            for (i = 0; i < Fddi_Num_Stations; i++)</pre>
1535
               if (tk_trace_on == OPC_TRUE)
1536
1537
                  sprintf (str0, "adjusting state of station (%d)", i);
1538
```

```
op_prg_odb_print_minor ("", str0, OPC_NIL);
1539
1540
      /* Calculate number of hops separating station i from block base
1541
1542
         station. */
      /* In special case where i is the base station, the token must run a
1543
         full */
1544
                                                                */
1545
      /* lap before returning.
               if (i != Fddi_Tk_Block_Base_Station)
1546
1547
                   num_hops = (i - Fddi_Tk_Block_Base_Station) %
1548
                      Fddi_Num_Stations;
1549
1550
                   if (num\_hops < 0)
                      num_hops = Fddi_Num_Stations + num_hops;
1551
1552
               else num_hops = Fddi_Num_Stations:
1553
      /* Calculate first time at which token would have been received by
1554
1555
         station i. */
      /* Note that initial release of token from base station takes a
1556
1557
         different */
      /* amount of time than repeating of token by other stations. Thus, the
1558
         first */
1559
      /* hop is assumed, and the base time is augmented by the time required
1560
         to */
1561
1562
      /* complete it.
               first_tk_rx = Fddi_Tk_Block_Base_Time + FDDIC_TOKEN_TX_TIME +
1563
                   Fddi_Prop_Delay + (num_hops - 1) * Fddi_Tk_Hop_Delay;
1564
               if (tk_trace_on == OPC_TRUE)
1565
1566
                   sprintf (str0, "station is (%d) hops from base", num_hops);
1567
                   sprintf (strl, "first receipt of token would be at (%.9f)",
1568
                      first_tk_rx);
1569
1570
                   op_prg_odb_print_minor (str0, str1, OPC_NIL);
1571
      /* Case 1: the token would not yet have been received by station i. */
1572
1573
               if (first_tk_rx > current_time)
1574
      /* Case la: the TRT at station i would not yet have expired. */
1575
                   if (Fddi_Trt_Exp_Time [i] > current_time)
1576
1577
      /* Late Ct remains at i original value; only the TRT needs */
1578
1579
      /* to be started again, with the same expiration time. */
1580
                     TRT_SET (i, Fddi_Trt_Exp_Time [i])
                      if (tk_trace_on == OPC_TRUE)
1581
1582
                         sprintf (str0, "Restoring TRT to previous exp. time
1583
1584
                            (%.9f)", Fddi_Trt_Exp_Time [i]);
```

```
op_prg_odb_print_minor ("Token would not be received
1585
                            and TRT not expired", str0, OPC_NIL);
1586
1587
1588
1589
      /* Case 1b: the TRT at station i would have expired. */
1590
                   else
1591
      /* Late_Ct would have been set; also the timer would have been
1592
1593
         rescheduled */
      /* for an entire TTRT at the time of expiration.
1594
                      Fddi_Late_Ct [i] = 1:
1595
                      TRT_SET (i. (Fddi_T_Opr + Fddi_Trt_Exp_Time [i]))
1596
                      if (tk_trace_on == OPC_TRUE)
1597
1598
                         sprintf (str0, "Restoring TRT to proper exp. time
1599
                            (%.9f)", Fddi_T_Opr + Fddi_Trt_Exp_Time [i]);
1600
                         op_prg_odb_print_minor ("Token would not be received
1601
1602
                            and TRT would have expired", str0, OPC_NIL):
1603
1604
                      }
1605
                   }
1606
      /* Case 2: the token would have been received (perhaps more than once).
1607
1608
               else
1609
      /* Calculate the number of times the token would have been received */
1610
      /* not including the first receipt.
1611
1612
                  tk_lap_time = Fddi_Tk_Hop_Delay * Fddi_Num_Stations;
                   num_tk_rx = floor ((current_time - first_tk_rx) /
1613
1614
                      tk_lap_time):
1615
      /* Calculate the latest time at which the token would have been
1616
         received. */
1617
                  last_tk_rx = first_tk_rx + (num_tk_rx * tk_lap_time);
1618
      /* Clear Late_Ct and schedule timer to expire at last receipt of token
1619
1620
      /* plus one full TTRT. */
1621
                  Fddi_Late_Ct [i] = 0;
                  TRT_SET (i, (last_tk_rx + Fddi_T_Opr))
1622
1623
                  if (tk_trace_on == OPC_TRUE)
1624
1625
                      sprintf (str0, "token received (%g) times, last receipt
1626
                         at (%.9f)", num_tk_rx + 1.0, last_tk_rx);
                      sprintf (strl, "Restoring TRT to proper exp. time
1627
1628
                         (%.9f)". last_tk_rx + Fddi_T_Opr);
1629
                     op_prg_odb_print_minor ("Token would have been received:
1630
                         Late_Ct is cleared", strl, str0, OPC_NIL);
```

```
1631
                     }
1632
                  }
               }
1633
      /* compute the time since the token was blocked */
1634
            elapsed_time = current_time - Fddi_Tk_Block_Base_Time;
1635
      /* compute the number of hops completed on the ring. For the first hop
1636
1637
      /* the token is transmitted directly, not repeated. For all remaining
1638
1639
      /* hops, the delay is the station latency plus the propagation delay.
1640
1641
      /* Thus, the first hop is assumed, and the remaining time for
1642
1643
         additional*/
      /* hops is computed beginning at the time where the token enters the */
1644
      /* base station's downstream neighbor */
1645
            dbl_num_hops = 1.0 + (elapsed_time - FDDIC_TOKEN_TX_TIME -
1646
               Fddi_Prop_Delay) / Fddi_Tk_Hop_Delay:
1647
      /* If the token was unblocked in less time than it would have taken to
1648
1649
      /* be fully transmitted by the base station, dbl_num_hops will be */
1650
      /* negative. However, 1 full hop would still be required before the */
1651
1652
      /* token could be used, since the station had already committed to */
      /* issuing the token. Thus, the actual of number of hops should never */
1653
      /* be less than 1. If it is, round it to 1. */
1654
1655
            if (dbl_num_hops < 1.0)
               dbl_num_hops = 1.0;
1656
1657
            else
1658
1659
      /* In all other cases, round the number of hops up to the nearest */
      /* integer value. If already an integer, then leave as is. */
1660
1661
               dbl_num_hops = ceil (dbl_num_hops);
1662
1663
      /* Obtain an integer equivalent of dbl_num_hops. */
1664
            num_hops = dbl_num_hops;
      /* Based on the number of hops and the base station, compute the */
1665
1666
      /* next station where the token will appear. */
1667
            next_station = (num_hops + Fddi_Tk_Block_Base_Station) %
               Fddi_Num_Stations:
1668
      /* Compute the time at which the token will appear there. */
1669
1670
      /* Again, assume the first hop occurred, and measure time */
1671
      /* from there forward.
            next_time = Fddi_Tk_Block_Base_Time + (FDDIC_TOKEN_TX_TIME +
1672
1673
               Fddi_Prop_Delay) + (dbl_num_hops - 1.0) * Fddi_Tk_Hop_Delay;
```

```
if (tk_trace_on == OPC_TRUE)
1674
1675
               sprintf (str0, "Re-introducing token at station (%d), at time
1676
1677
                   (%.9f)", next_station, next_time);
               op_prg_odb_print_minor (str0, OPC_NIL);
1678
1679
1680
      /* reinject the token at that station */
1681
            fddi_tk_inject (next_station. next_time);
1682
            FOUT
1683
            }
1684
         fddi_tk_inject (address, arv_time)
1685
                            address;
            int
1686
            double
                            arv_time;
1687
      /* Re-insert the token into the ring after an idle period. */
1688
1689
            FIN (fddi_tk_inject (address, arv_time))
1690
      /* The token is recreated and reinserted onto the ring */
      /* at the specified station which is not necessarily the */
1691
      /* station now requesting the token. */
1692
1693
      /* The station which will rensert the token is */
      /* asked to do so by means .: a remote interrupt. */
1694
            op_intrpt_schedule_remote (arv_time, FDDIC_TK_INJECT,
1695
                   Fddi_Address_Table [address]);
1696
            FOUT
1697
1698
            1
1699
         fddi_load_frame_attrs (dest_addr_ptr, svc_class_ptr, pri_level_ptr)
1700
            int
                            *dest_addr_ptr, *svc_class_ptr, *pri_level_ptr;
1701
1702
                            NUM_PRIOS. i:
                                             /* 26JAN94 */
         int
                            *pkptr;
1703
            Packet
1704
            FIN (fddi_load_frame_attrs (dest_addr_ptr, svc_class_ptr,
1705
               pri_level_ptr))
1706
      /* remove next packet in queue */
      /* 27DEC94: loop structure superimposed to handle a bank of subqueues.
1707
1708
      /* Extract the packet with the highest priority, that is, the packet
1709
1710
1711
      /* at the head of the highest-numbered subqueue containing packets.
1712
1713
      /* Note that the C language vector numbering convention numbers the
1714
1715
      /* subqueues from 0 to 7, while FDDI convention is to number the */
```

```
/* corresponding asynchronous priorities from 1 to 8. This is */
1716
1717
      /* reconciled in the statistical outputs available in the Analysis */
1718
      /* Editor, where labels are assigned accordingly. Also note that */
      /* synchronous traffic is assigned priority 8 as an artifice to allow
1719
1720
          */
      /* routing through a separate subqueue, by which statistics may be */
1721
      /* gathered for traffic by class and by priority. -Nix */
1722
          NUM_PRIOS = 9;
1723
1724
          for (i = NUM_PRIOS - 1; i > -1; i--)
1725
                if (op_subq_stat (i, OPC_QSTAT_PKSIZE) > 0.0)
1726
1727
                pkptr = op_subq_pk_remove (1, OPC_QPOS_HEAD);
1728
1729
                break:
1730
1731
1732
      /* extract the fields of interest */
             op_pk_nfd_get (pkptr, "dest_addr", dest_addr_ptr);
op_pk_nfd_get (pkptr, "svc_class", svc_class_ptr);
1733
1734
      /* only read priority level if frame is asynchronous */
1735
1736
             if (*svc_class_ptr == FDDI_SVC_ASYNC)
                op_pk_nfd_get (pkptr, "pri", pri_level_ptr);
1737
1738
      /* replace the packet on the proper subqueue */
1739
             op_subq_pk_insert (1, pkptr, OPC_QPOS_HEAD);
1740
             FOUT
1741
             }
```

# APPENDIX F. SOURCE "C" CODE:

# "fddi\_gen\_mult.pr.c"

The line numbering in this appendix is used for reference within this thesis only, and does not correspond with that seen in OPNET stext editors.

```
/* Process model C form file: fddi_gen_mult.pr.c */
   /* Portions of this file Copyright (C) MIL 3, Inc. 1992 */
3
   /* OPNET system definitions */
    #include <opnet.h>
5
    #include "fddi_gen_mult.pr.h"
    FSM_EXT_DECS
    /* Header block */
   #define MAC_LAYER_OUT_STREAM
9
   /* define possible service classes for frames */
10
    #define FDDI_SVC_ASYNC
    #define FDDI_SVC_SYNC
                                     1
11
12
    /* define token classes */
13
    #define FDDI_TK_NONRESTRICTED
14
    #define FDDI_TK_RESTRICTED
15
    /* 07FEB94: define the number of stations
                                                  */
    #define NUM_STATIONS
17
    /* a global counting variable */
    /* nt genARRIVAL = 0: */
18
19
    /* State variable definitions */
20
    typedef struct
21
22
       FSM_SYS_STATE
23
       Distribution*
                            sv_inter_dist_ptr;
24
       Distribution*
                            sv_len_dist_ptr;
25
       Distribution*
                            sv_dest_dist_ptr:
26
       Distribution*
                            sv_pkt_priority_ptr;
       Objid
                            sv_mac_objid;
```

```
28
        Objid
                              sv_my_id;
                                 sv_low_dest_addr:
29
        int
                                 sv_high_dest_addr:
30
        int
31
                                 sv_station_addr:
        int
                                 sv_low_pkt_priority;
32
        int
33
        int
                                 sv_high_pkt_priority;
34
                                 sv_arrival_rate;
        double
35
                                 sv_mean_pk_len;
        double
36
                                 sv_async_mix:
        double
37
                              sv_mac_iciptr;
        Ici*
                              sv_num_addees_dist_ptr;
38
        Distribution*
                                 sv_num_addees;
39
        int
40
                                 sv_min_num_addees;
        int
                                 sv_max_num_addees:
41
        int
                                 sv_dest_addr[NUM_STATIONS+1];
42
        int
43
        } fddi_gen_mult_state;
                                    ((fddi_gen_mult_state*)
44
    #define pr_state_ptr
45
    SimI_Mod_State_Ptr)
                                    pr_state_ptr->sv_inter_dist_ptr
46
    #define inter_dist_ptr
                                    pr_state_ptr->sv_len_dist_ptr
47
    #define len_dist_ptr
                                    pr_state_ptr->sv_dest_dist_ptr
48
    #define dest_dist_ptr
                                    pr_state_ptr·>sv_pkt_priority_ptr
49
    #define pkt_priority_ptr
50
    #define mac_objid
                                    pr_state_ptr->sv_mac_objid
                                       pr_state_ptr->sv_my_id
    #define my_id
51
                                    pr_state_ptr->sv_low_dest_addr
52
    #define low_dest_addr
53
                                    pr_state_ptr->sv_high_dest_addr
    #define high_dest_addr
                                    pr_state_ptr->sv_station_addr
54
    #define station_addr
                                    pr_state_ptr->sv_low_pkt_priority
55
    #define low_pkt_priority
56
    #define high_pkt_priority
                                    pr_state_ptr->sv_high_pkt_priority
57
    #define arrival_rate
                                    pr_state_ptr·>sv_arrival_rate
58
    #define mean_pk_len
                                    pr_state_ptr·>sv_mean_pk_len
59
                                    pr_state_ptr->sv_async_mix
    #define async_mix
                                       pr_state_ptr->sv_mac_iciptr
60
    #define mac_iciptr
61
    #define num_addees_dist_ptr
                                    pr_state_ptr->sv_num_addees_dist_ptr
                                       pr_state_ptr->sv_num_addees
62
    #define num_addees
63
    #define min_num_addees
                                    pr_state_ptr->sv_min_num_addees
                                    pr_state_ptr->sv_max_num_addees
64
    #define max_num_addees
                                    pr_state_ptr->sv_dest_addr
65
    #define dest_addr
    /* Process model interrupt handling procedure */
66
67
    void
68
    fddi_gen_mult ()
69
70
        Packet
                    *pkptr:
71
        int
                    pklen;
72
        int
                    *da_ptr;
73
        int
                    i, restricted:
```

```
74
                     pkt_prio:
        int
 75
        int
                     nix;
76
        FSM_ENTER (fddi_gen_mult)
 77
        FSM_BLOCK_SWITCH
 78
           {
79
     /** state (INIT) enter executives **/
80
           FSM_STATE_ENTER_UNFORCED (0, state0_enter_exec, "INIT")
81
82
     /* determine id of own processor to use in finding attrs */
83
84
              my_id = op_id_self ();
     /* 07FEB94: determine the upper and lower limits for multiple */
85
     /* addressing from this station. -Nix
                                                 */
86
              op_ima_obj_attr_get (my_id, "min num addees", &min_num_addees);
87
              op_ima_obj_attr_get (my_id. "max num addees". &max_num_addees):
88
     /* 07FEB94: set up a distribution for the number of stations
89
90
     /* receive this packet. -Nix
              num_addees_dist_ptr = op_dist_load ("uniform_int",
91
92
                 min_num_addees, max_num_addees);
     /* determine address range for uniform desination assignment */
93
              op_ima_obj_attr_get (my_id, "low dest address",
94
95
     &low_dest_addr);
96
              op_ima_obj_attr_get (my_id, "high dest address",
97
                 &high_dest_addr);
     /* determine object id of connected 'mac' layer process */
98
              mac_objid = op_topo_assoc (my_id, OPC_TOPO_ASSOC_OUT.
99
                    OPC_OBJMTYPE_MODULE, MAC_LAYER_OUT_STREAM);
100
101
     /* determine the address assigned to it */
102
     /* which is also the address of this station */
              op_ima_obj_attr_get (mac_objid, "station_address",
103
104
                 &station_addr);
     /* set up a distribution for generation of addresses */
105
106
              dest_dist_ptr = op_dist_load ("uniform_int", low_dest_addr,
107
                 high_dest_addr);
108
     /* added 26DEC93 */
109
     /* determine priority range for uniform traffic generation */
110
              op_ima_obj_attr_get (my_id, "high pkt priority",
111
                 &high_pkt_priority);
              op_ima_obj_attr_get (my_id, "low pkt priority",
112
113
                 &low_pkt_priority);
```

```
/* set up a distribution for generation of priorities */
114
              pkt_priority_ptr = op_dist_load ("uniform_int",
115
                  low_pkt_priority, high_pkt_priority);
116
     /* above added 26DEC93 */
117
     /* also determine the arrival rate for packet generation */
118
              op_ima_obj_attr_get (my_id, "arrival rate", &arrival_rate);
119
120
     /* determine the mix of asynchronous and synchronous */
     /* traffic. This is expressed as the proportion of */
121
     /* asynchronous traffic. i.e a value of 1.0 indicates */
122
     /* that all the produced traffic shall be asynchronous. */
123
              op_ima_obj_attr_get (my_id, "async_mix", &async_mix);
124
     /* set up a distribution for arrival generations */
125
126
              if (arrival_rate != 0.0)
127
     /* arrivals are exponentially distributed, with given mean */
128
                  inter_dist_ptr = op_dist_load ("constant", 1.0 /
129
130
                    arrival_rate, 0.0);
     /* determine the distribution for packet size */
131
                  op_ima_obj_attr_get (my_id, "mean pk length", &mean_pk_len);
132
133
     /* set up corresponding distribution */
134
                  len_dist_ptr = op_dist_load ("constant", mean_pk_len, 0.0);
135
     /* designate the time of first arrival */
136
                  fddi_gen_schedule ();
137
     /* set up an interface control information (ICI) structure */
138
     /* to communicate parameters to the mac layer process */
139
     /* (it is more efficient to set one up now and keep it */
140
     /* as a state variable than to allocate one on each packet xfer) */
141
                 mac_iciptr = op_ici_create ("fddi_mac_req");
142
143
              }
144
     /** blocking after enter executives of unforced state. **/
145
146
           FSM_EXIT (1,fddi_gen_mult)
     /** state (INIT) exit executives **/
147
           FSM_STATE_EXIT_UNFORCED (0, stateO_exit_exec, "INIT")
148
149
150
151
     /** state (INIT) transition processing **/
           FSM_TRANSIT_FORCE (1, statel_enter_exec, ;)
152
153
```

```
154
      /** state (ARRIVAL) enter executives **/
            FSM_STATE_ENTER_UNFORCED (1. statel_enter_exec. "ARRIVAL")
155
156
      /* determine the length of the packet to be generated */
157
158
               pklen = op_dist_outcome (len_dist_ptr);
159
     /* 07FEB94: re-initialize the destination address array */
160
     /* to zeros. -Nix
161
               for (i = 0: i < NUM STATIONS+1: i++)
162
163
               dest_addr[i] = 0:
164
165
     /* determine the destination */
     /* don't allow this station's address as a possible outcome */
166
167
     /* gen_packet:
168
     /* dest_addr = op_dist_outcome (dest_dist_ptr); */
169
     /* if (dest_addr != -1 && dest_addr == station_addr) */
170
     /* goto gen_packet;
171
     /* 07FEB94: determine the destinations. -Nix */
172
     /* Determine the number of stations to receive this packet */
173
               num_addees = op_dist_outcome (num_addees_dist_ptr);
174
     /* Find these stations, using num_addees as a counter. -Nix */
175
               for (i = num\_addees; i > 0; i--)
176
               {
177
               gen_packet:
178
               nix = op_dist_outcome (dest_dist_ptr);
179
               if (dest_addr[nix] == 1 | | nix == station_addr)
180
181
                 goto gen_packet:
182
183
              dest_addr[nix] = 1;
184
185
     /* 05MAR94: because the op_pk_nfd_get() command in FR_REPEAT */
186
     /* overwrites the first field with the array address. an */*/
187
     /* offset needs to be applied so that the dest_array[0] */
188
     /* contents aren't lost; that is, one field more than the
                                                                    */*/
189
     /* number of stations is included to allow a one-step shift
                                                                     */
190
     /* that will preserve the address array. In fddi_mac, all
                                                                     */
191
     /* references to dest_addr must allow for this shift. -Nix
                                                                     */
192
              for (i=NUM_STATIONS: i>0; i--)
193
              dest_addr[i] = dest_addr[i-1];
194
195
     /* 26DEC94 & 29JAN94: determine its priority */
196
              pkt_prio = op_dist_outcome (pkt_priority_ptr);
```

```
197
     /* create a packet to send to mac */
             pkptr = op_pk_create_fmt ("fddi_llc_fr");
198
     /* assign its overall size. */
199
200
             op_pk_total_size_set (pkptr, pklen);
201
     /* assign the time of creation */
202
             op_pk_nfd_set (pkptr, "cr_time", op_sim_time ());
203
     /* place the destination address into the ICI */
204
     /* (the protocol_type field will default) */
205
     /* 15MAR94: note that dest_addr now serves as a */
206
     /* pointer to an array in memory, as it is the */
     /* name of an array of what will be 0s and 1s. -Nix */
207
             op_ici_attr_set (mac_iciptr, "dest_addr", dest_addr);
208
209
     /* assign the priority, and requested token class */
     /* also assign the service class */
210
211
     /* 29JAN94: the fddi_llc_fr format is modified */
212
     /* to include a "pri" field. -Nix */
213
             if (op_dist_uniform (1.0) <= async_mix)</pre>
214
                op_pk_nfd_set (pkptr, "pri", pkt_prio);/* 29JAN94 */
215
     216
217
218
     */
219
220
             else{
221
                op_pk_nfd_set (pkptr, "pri", 8);/* 29JAN94 */
222
                op_ici_attr_set (mac_iciptr, "svc_class", FDDI_SVC_SYNC);
223
                op_ici_attr_set (mac_iciptr, "pri", 8);
                                                        /* 29JAN94 */
224
225
     /* Request only nonrestricted tokens after transmission */
226
             op_ici_attr_set (mac_iciptr, "tk_class",
227
     FDDI_TK_NONRESTRICTED);
228
     /* send the packet coupled with the ICI */
229
             op_ici_install (mac_iciptr);
             up_pk_send (pkptr, MAC_LAYER_OUT_STREAM);
230
            *********
231
232
     /* 17MAR94: count and report the running total number
233
     /* of packets generated. -Nix
                                        */
     /* ******
                         ************
234
235
     /* genARRIVAL ++; */
236
     /* printf("Packets generated: %d\n", genARRIVAL); */
```

```
/* schedule the next arrival */
237
238
             fddi_gen_schedule ();
     239
     /* 18FEB94: print out the address, and the contents. */
240
     /* for (1=0; i<NUM_STATIONS+1; i++) */
241
     /* printf("ARRIVAL: %d. address: %X; contents: %d\n".
242
     /* i, &(dest_addr[i]), dest_addr[i]); */
243
     244
245
             }
     /** blocking after enter executives of unforced state. **/
246
           FSM_EXIT (3,fddi_gen_mult)
247
248
     /** state (ARRIVAL) exit executives **/
           FSM_STATE_EXIT_UNFORCED (1, statel_exit_exec. "ARRIVAL")
249
250
251
             }
252
     /** state (ARRIVAL) transition processing **/
          FSM_TRANSIT_FORCE (1, statel_enter_exec.;)
253
254
255
256
        FSM_EXIT (0,fddi_gen_mult)
257
258
     void
259
     fddi_gen_mult_svar (prs_ptr.var_name,var_p_ptr)
        fddi_gen_mult_state *prs_ptr;
260
                   *var_name, **var_p_ptr;
261
        char
262
263
        FIN (fddi_gen_mult_svar (prs_ptr))
264
        *var_p_ptr = VOS_NIL:
265
        if (Vos_String_Equal ("inter_dist_ptr" , var_name)) *var_p_ptr =
266
           (char*) (&prs_ptr->sv_inter_dist_ptr);
        if (Vos_String_Equal ("len_dist_ptr" , var_name)) *var_p_ptr =
267
268
           (char*) (&prs_ptr->sv_len_dist_ptr);
        if (Vos_String_Equal ("dest_dist_ptr" , var_name)) *var_p_ptr =
269
270
           (char*) (&prs_ptr->sv_dest_dist_ptr);
        if (Vos_String_Equal ("pkt_priority_ptr" , var_name)) *var_p_ptr =
271
272
           (char*) (&prs_ptr->sv_pkt_priority_ptr);
        if (Vos_String_Equal ("mac_objid" , var_name)) *var_p_ptr = (char*)
273
274
           (&prs_ptr->sv_mac_objid);
        if (Vos_String_Equal ("my_id" , var_name)) *var_p_ptr = (char*)
275
276
           (&prs_ptr->sv_my_id);
        if (Vos_String_Equal ("low_dest_addr" , var_name)) *var_p_ptr =
277
           (char*) (&prs_ptr->sv_low_dest_addr);
278
```

```
if (Vos_String_Equal ("high_dest_addr" , var_name)) *var_p_ptr =
279
            (char*) (&prs_ptr->sv_high_dest_addr);
280
        if (Vos_String_Equal ("station_addr" , var_name)) *var_p_ptr =
281
            (char*) (&prs_ptr->sv_station_addr);
282
        if (Vos_String_Equal ("low_pkt_priority" , var_name)) *var_p_ptr =
283
            (char*) (&prs_ptr->sv_low_pkt_priority);
284
        if (Vos_String_Equal ("high_pkt_priority" , var_name)) *var_p_ptr
285
            = (char*) (&prs_ptr->sv_high_pkt_priority);
286
        if (Vos_String_Equal ("arrival_rate" , var_name)) *var_p_ptr =
287
            (char*) (&prs_ptr->sv_arrival_rate);
288
        if (Vos_String_Equal ("mean_pk_len" , var_name)) *var_p_ptr = (char*)
289
290
            (&prs_ptr->sv_mean_pk_len);
        if (Vos_String_Equal ("async_mix" , var_name)) *var_p_ptr = (char*)
291
292
            (&prs_ptr->sv_async_mix);
        if (Vos_String_Equal ("mac_iciptr" , var_name)) *var_p_ptr = (char*)
293
294
            (&prs_ptr->sv_mac_iciptr);
        if (Vos String_Equal ("num_addees_dist_ptr" , var_name)) *var_p_ptr =
295
            (char*) (&prs_ptr->sv_num_addees_dist_ptr);
296
        if (Vos_String_Equal ("num_addees" , var_name)) *var_p_ptr = (char*)
297
298
            (&prs_ptr->sv_num_addees):
        if (Vos_String_Equal ("min_num_addees" , var_name)) *var_p_ptr =
299
            (char*) (&prs_ptr->sv_min_num_addees);
300
        if (Vos_String_Equal ("max_num_addees" , var_name)) *var_p_ptr =
301
            (char*) (&prs_ptr->sv_max_num_addees);
302
303
        if (Vos_String_Equal ("dest_addr" , var_name)) *var_p_ptr = (char*)
304
            (prs_ptr->sv_dest_addr):
305
        FOUT:
306
        }
307
     void
308
     fddi_gen_mult_diag ()
309
310
        Packet
                     *pkptr:
311
        int
                     pklen:
312
        int
                     *da_ptr;
313
        int
                     i, restricted:
314
        int
               pkt_prio;
315
        int
               nix;
316
        FIN (fddi_gen_mult_diag ())
317
        FOUT:
318
        }
319
     void
320
     fddi_gen_mult_terminate ()
321
322
        Packet
                     *pkptr;
323
        int
                     pklen:
```

```
324
         int
                     *da_ptr:
325
         int
                     i, restricted;
326
         int
                     pkt_prio;
327
         int
                     nix;
         FIN (fddi_gen_mult_terminate ())
328
329
         FOUT:
330
         }
331
     Compcode
332
     fddi_gen_mult_init (pr_state_pptr)
333
         fddi_gen_mult_state
                                     **pr_state_pptr;
334
        static VosT_Cm_Obtype obtype = OPC_NIL;
335
         FIN (fddi_gen_mult_init (pr_state_pptr))
336
337
         if (obtype == OPC_NIL)
338
            if (Vos_Catmem_Register ("proc state vars (fddi_gen_mult)",
339
340
               sizeof (fddi_gen_mult_state), Vos_Nop, &obtype) ==
341
     VOSC_FAILURE)
               FRET (OPC_COMPCODE_FAILURE)
342
343
            }
        if ((*pr_state_pptr = (fddi_gen_mult_state*) Vos_Catmem_Alloc
344
345
            (obtype, 1)) == OPC_NIL)
346
            FRET (OPC_COMPCODE_FAILURE)
347
        else
348
349
            (*pr_state_pptr)->current_block = 0:
            FRET (OPC_COMPCODE_SUCCESS)
350
351
            )
352
        }
353
     /* static added 2DEC93, on advice from MIL 3. Inc. */
354
        static
355
        fddi_gen_schedule ()
356
            1
                           inter_time:
357
            double
358
     /* obtain an interarrival period according to the */
359
     /* prescribed distribution */
360
            inter_time = op_dist_outcome (inter_dist_ptr);
361
     /* schedule the arrival of next generated packet */
            op_intrpt_schedule_self (op_sim_time () + inter_time, 0);
362
363
            }
```

### APPENDIX G. SINK "C" CODE:

## "fddi\_sink\_mult.pr.c"

The line numbering in this appendix is used for reference within this thesis only, and does not correspond with that seen in OPNET<sup>e</sup>'s text editors.

```
/* Process model C form file: fddi_sink_mult.pr.c */
1
   /* Portions of this file Copyright (C) MIL 3. Inc. 1992 */
    /* OPNET system definitions */
    #include <opnet.h>
    #include "fddi_sink_mult.pr.h"
    FSM_EXT_DECS
    /* Header block */
    /* Globals */
    /* array format installed 20JAN94; positions 0-7 represent the asynch
       priority levels, PRIORITIES + 1 */
10
    /* represents synch traffic, and grand totals are as given in the
11
12
       original. */
13
    #define PRIORITIES 8 /* 20JAN94 */
14
                 fddi_sink_accum_delay = 0.0;
    double
15
                 fddi_sink_accum_delay_a[PRIORITIES + 1] = {0.0, 0.0, 0.0.
16
    double
17
       0.0, 0.0, 0.0, 0.0, 0.0, 0.0);
             fddi_sink_total_pkts = 0;
18
    int
             fddi_sink_total_pkts_a[PRIORITIES + 1] = {0, 0, 0, 0, 0, 0, 0,
19
    int
       0. 0):
20
21
    double
                 fddi_sink_total_bits = 0.0;
                fddi_sink_total_bits_a[PRIORITIES + 1] = {0.0, 0.0, 0.0,
22
    double
23
       0.0, 0.0, 0.0, 0.0, 0.0, 0.0);
24
                fddi_sink_peak_delay = 0.0;
    double
                 fddi_sink_peak_delay_a[PRIORITIES + 2] = {0.0, 0.0, 0.0.
25
    double
26
       0.0, 0.0, 0.0, 0.0, 0.0, 0.0);
             fddi_sink_scalar_write = 0;
27
    int
             pri_set = 20:/* 20JAN94 */
28
    int
29
    /* Externally defined globals. */
30
    extern double fddi_t_opr [];
```

```
/* Attributes fromEnvironment file. */
32
             Offered Load:/* 12JAN94 */
    double
             Asynch_Offered_Load; /* 12JAN94 */
33
    double
    /* transition expressions */
34
    #define END_OF_SIM op_intrpt_type() == OPC_INTRPT_ENDSIM
35
    /* State variable definitions */
36
37
    typedef struct
38
39
       FSM_SYS_STATE
                            sv_thru_gshandle;
40
       Gshandle
41
       Gshandle
                           sv_m_delay_gshandle;
                           sv_ete_delay_gshandle;
42
       Gshandle
                           sv_thru_gshand?e_a[10];
43
       Gshandle
                           sv_m_delay_gshandle_a[10];
44
       Gshandle
                           sv_ete_delay_gshandle_a[9];
45
       Gshandle
46
       } fddi_sink_mult_state:
47
    #define pr_state_ptr
                                    ((fddi_sink_mult_state*)
       SimI_Mod_State_Ptr)
48
49
    #define thru_gshandle
                                    pr_state_ptr->sv_thru_gshandle
                                   pr_state_ptr->sv_m_delay_gshandle
   #define m_delay_gshandle
    #define ete_delay_gshandle pr_state_ptr->sv_ete_delay_gshandle
51
                                    pr_state_ptr->sv_thru_gshandle_a
52
    #define thru_gshandle_a
                                 pr_state_ptr->sv_m_delay_gshandle_a
    #define m_delay_gshandle_a
53
    #define ete_delay_gshandle_a pr_state_ptr->sv_ete_delay_gshandle_a
54
    /* Process model interrupt handling procedure */
55
56
    void
57
    fddi_sink_mult ()
58
       {
59
       double
                      delay. creat_time;
60
       Packet*
                   pkptr;
61
       int
                      src_addr, my_addr;
62
       Ici*
                   from_mac_ici_ptr;
63
       double
                      fddi_sink_ttrt;
64
       FSM_ENTER (fddi_sink_mult)
       FSM_BLOCK_SWITCH
65
66
    /*----*/
67
    /** state (DISCARD) enter executives **/
68
69
          FSM_STATE_ENTER_UNFORCED (0, state0_enter_exec, "DISCARD")
70
    /* get the packet and the interface control info */
71
72
             pkptr = op_pk_get (op_intrpt_strm ());
73
             from_mac_ici_ptr = op_intrpt_ici ();
```

```
/* 20JAN94: get the packet's priority level, which */
74
     /* will be used to index arrays of thruput and delay */
75
76
     /* computations.
77
     /* pri_set = op_pk_priority_get (pkptr); doesn't work here */
              op_pk_nfd_get (pkptr, "pri", &pri_set);/* 29JAN94 */
78
79
     /* add in its size */
80
              fddi_sink_total_bits += op_pk_total_size_get (pkptr);
              fddi_sink_total_bits_a[pri_set] +- op_pk_total_size_get
81
     (pkptr); /* 20JAN94 */
82
     /* determine the time of creation of the packet */
83
              op_pk_nfd_get (pkptr, "cr_time", &creat_time);
84
85
     /* accumulate delays */
86
              delay = op_sim_time () - creat_time;
87
              fddi_sink_accum_delay += delay:
              fddi_sink_accum_delay_a[pri_set] += delay; /* 20JAN94 */
88
     /* keep track of peak delay value */
89
90
              if (delay > fddi_sink_peak_delay)
91
                 fddi_sink_peak_delay = delay;
92
     /* 20JAN94: keep track by priority levels as well 23JAN94 */
93
              if (delay > fddi_sink_peak_delay_a[pri_set])
94
              fddi_sink_peak_delay_a[pri_set] = delay;
     95
     /* printf("DISCARD: pri_set is %d\n", pri_set); */
96
     97
98
     /* destroy the packet */
99
              op_pk_destroy (pkptr);
100
     /* 03FEB94: To convert this to the "fddi_sink_link"
     /* model, deactivate the 'destroy' code, and activate */
/* the following 'enqueue' code. This is a first */
101
102
     /* step toward developing a LAN bridging structure.
103
                              */
104
     /* -Nix
     /* op_subq_pk_insert (pri_set, pkptr, OPC_QPOS_TAIL); */
105
106
     /* increment packet counter; 20JAN94 */
107
              fddi_sink_total_pkts++:
              fddi_sink_total_pkts_a[pri_set]++:
108
     /* if a multiple of 25 packets is reached, update stats */
109
110
     /* 03FEB94: [0]->[7] represent asynch priorities 1->8, */
     /* respectively; [8] represents synchronous traffic,
111
     /* and [9] represents overall asynchronous traffic.-Nix */
112
113
              if (fddi_sink_total_pkts % 1 == 0)
```

```
114
                  op_stat_global_write (thru_gshandle, fddi_sink_total_bits /
115
                     op_sim_time ());
116
                  op_stat_global_write (thru_gshandle_a[0].
117
                     fddi_sink_total_bits_a[0] / op_sim_time());
118
                  op_stat_global_write (thru_gshandle_a[1],
119
                     fddi_sink_total_bits_a[1] / op_sim_time());
120
                 op_stat_global_write (thru_gshandle_a[2].
121
                     fddi_sink_total_bits_a[2] / op_sim_time());
122
123
                 op_stat_global_write
                     (thru_gshandle_a[3],fddi_sink_total_bits_a[3] /
124
125
                     op_sim_time());
                 op_stat_global_write (thru_gshandle_a[4].
126
                     fddi_sink_total_bits_a[4] / op_sim_time());
127
                 op_stat_global_write (thru_gshandle_a[5],
128
                     fddi_sink_total_bits_a[5] / op_sim_time());
129
                 op_stat_global_write (thru_gshandle_a[6].
130
                     fddi_sink_total_bits_a[6] / op_sim_time());
131
                 op_stat_global_write (thru_gshandle_a[7].
132
                     fddi_sink_total_bits_a[7] / op_sim_time());
133
                 op_stat_global_write (thru_gshandle_a[8],
134
                     fddi_sink_total_bits_a[8] / op_sim_time());
135
136
     /* 30JAN94: gather all asynch stats into one overall figure */
137
              op_stat_global_write (thru_gshandle_a[9],
                  (fddi_sink_total_bits_a[0] + fddi_sink_total_bits_a[1] +
138
                  fddi_sink_total_bits_a[2] + fddi_sink_total_bits_a[3] +
139
                  fddi_sink_total_bits_a[4] + fddi_sink_total_bits_a[5] +
140
                  fddi_sink_total_bits_a[6] + fddi_sink_total_bits_a[7]) /
141
142
                 op_sim_time()):
143
     /* (fddi_sink_total_bits - fddi_sink_total_bits_a[8]) / */
144
145
     /* op_sim_time()); */
              op_stat_global_write (m_delay_gshandle,
146
                                                          fddi_sink_accum_delay
147
                  / fddi_sink_total_pkts);
              op_stat_global_write (m_delay_gshandle_a[0],
148
                 fddi_sink_accum_delay_a[0] / fc _sink_total_pkts_a[0]);
149
              op_stat_global_write (m_delay_gsha :le_a[1],
150
                  fddi_sink_accum_delay_a[1] / fdgi_sink_total_pkts_a[1]);
151
152
              op_stat_global_write (m_delay_gshandle_a[2].
                  fddi_sink_accum_delay_a[2] / fddi_sink_total_pkts_a[2]);
153
154
              op_stat_global_write (m_delay_gshandle_a[3],
                 fddi_sink_accum_delay_a[3] / fddi_sink_total_pkts_a[3]);
155
              op_stat_global_write (m_delay_gshandle_a[4].
156
                 fddi_sink_accum_delay_a[4] / fddi_sink_total_pkts_a[4]);
157
              op_stat_global_write (m_delay_gshandle_a[5],
158
159
                  fddi_sink_accum_delay_a[5] / fddi_sink_total_pkts_a[5]);
              op_stat_global_write (m_delay_gshandle_a[6],
160
                 fddi_sink_accum_delay_a[6] / fddi_sink_total_pkts_a[6]);
161
```

```
op_stat_global_write (m_delay_gshandle_a[7],
162
                 fddi_sink_accum_delay_a[7] / fddi_sink_total_pkts_a[7]);
163
164
              op_stat_global_write (m_delay_gshandle_a[8],
                 fddi_sink_accum_delay_a[8] / fddi_sink_total_pkts_a[8]);
165
     /* 30JAN94: gather all asynch stats into one figure */
166
              op_stat_global_write (m_delay_gshandle_a[9].
167
                  (fddi_sink_accum_delay_a[0] + fddi_sink_accum_delay_a[1] +
168
                 fddi_sink_accum_delay_a[2] + fddi_sink_accum_delay_a[3] +
169
                 fddi_sink_accum_delay_a[4] + fddi_sink_accum_delay_a[5] +
170
                 fddi_sink_accum_delay_a[6] + fddi_sink_accum_delay_a[7]) /
171
                 (fddi_sink_total_pkts_a[0] + fddi_sink_total_pkts_a[1] +
172
                 fddi_sink_total_pkts_a[2] + fddi_sink_total_pkts_a[3] +
173
                 fddi_sink_total_pkts_a[4] + fddi_sink_total_pkts_a[5] +
174
                 fddi_sink_total_pkts_a[6] + fddi_sink_total_pkts_a[7]));
175
176
     /* (fddi_sink_accum_delay - fddi_sink_accum_delay_a[8]) /
177
     /* (fddi_sink_total_pkts - fddi_sink_total_pkts_a[8]));
178
179
180
     /* also record actual delay values */
              op_stat_global_write (ete_delay_gshandle, delay);
181
              op_stat_global_write (ete_delay_gshandle_a[pri_set], delay);
182
183
              }
     /** blocking after enter executives of unforced state. **/
184
           FSM_EXIT (1,fddi_sink_mult)
185
     /** state (DISCARD) exit executives **/
186
           FSM_STATE_EXIT_UNFORCED (0, state0_exit_exec, "DISCARD")
187
188
189
     /** state (DISCARD) transition processing **/
190
           FSM_INIT_COND (END_OF_SIM)
191
           FSM_DFLT_COND
192
           FSM_TEST_LOGIC ("DISCARD")
193
194
           FSM_TRANSIT_SWITCH
195
              FSM_CASE_TRANSIT (0, 1, statel_enter_exec, ;)
196
197
              FSM_CASE_TRANSIT (1, 0, state0_enter_exec, ;)
198
199
     /** state (STATS) enter executives **/
200
201
           FSM_STATE_ENTER_UNFORCED (1, statel_enter_exec, "STATS")
202
```

```
/* At end of simulation, scalar performance statistics */
203
     /* and input parameters are written out. */
204
     /* Only one station needs to do this */
205
206
               if (!fddi_sink_scalar_write)
207
208
     /* set the scalar write flag */
                  fddi_sink_scalar_write = 1:
209
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.),
210
                     Priority 1*, fddi_sink_accum_delay_a[0] /
211
                     fddi_sink_total_pkts_a[0]);
212
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.),
213
                     Priority 2", fddi_sink_accum_delay_a[1] /
214
                     fddi_sink_total_pkts_a[l]);
215
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.),
216
                     Priority 3", fddi_sink_accum_delay_a[2] /
217
                     fddi_sink_total_pkts_a[2]):
218
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.),
219
                     Priority 4", fddi_sink_accum_delay_a[3] /
220
221
                     fddi_sink_total_pkts_a[3]);
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.),
222
                     Priority 5". fddi_sink_accum_delay_a[4] /
223
                     fddi_sink_total_pkts_a[4]):
224
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.),
225
                     Priority 6", fddi_sink_accum_delay_a[5] /
226
                     fddi_sink_total_pkts_a[5]):
227
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.),
228
                     Priority 7", fddi_sink_accum_delay_a[6] /
229
230
                     fddi_sink_total_pkts_a[6]);
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.),
231
                     Priority 8", fddi_sink_accum_delay_a[7] /
232
233
                     fddi_sink_total_pkts_a[7]):
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.),
234
235
                     Asynchronous", (fddi_sink_accum_delay -
                     fddi_sink_accum_delay_a[8]) / (fddi_sink_total_pkts -
236
                     fddi_sink_total_pkts_a[8])):
237
238
     /* (fddi_sink_accum_delay_a[0] + fddi_sink_accum_delay_a[1] +
239
     /* fddi_sink_accum_delay_a[2] + fddi_sink_accum_delay_a[3] +
     /* fddi_sink_accum_delay_a[4] + fddi_sink_accum_delay_a[5] +
240
     /* fddi_sink_accum_delay_a[6] + fddi_sink_accum_delay_a[7]) / */
241
     /* (fddi_sink_total_pkts_a[0] + fddi_sink_total_pkts_a[1] +
242
                                                                       */
243
     /* fddi_sink_total_pkts_a[2] + fddi_sink_total_pkts_a[3] +
                                                                      */
     /* fddi_sink_total_pkts_a[4] + fddi_sink_total_pkts_a[5] +
244
                                                                      */
     /* fddi_sink_total_pkts_a[6] + fddi_sink_total_pkts_a[7]));
245
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.),
246
                     Synchronous*, fddi_sink_accum_delay_a[8] /
247
                     fddi_sink_total_pkts_a[8]);
248
```

```
249
                  op_stat_scalar_write ("Mean End-to-End Delay (sec.), Total",
250
                     fddi_sink_accum_delay / fddi_sink_total_pkts);
251
                  op_stat_scalar_write ("Throughput (bps), Priority 1",
                     fddi_sink_total_bits_a[0] / op_sim_time ());
252
                  op_stat_scalar_write ("Throughput (bps), Priority 2".
253
254
                     fddi_sink_total_bits_a[1] / op_sim_time ());
                  op_stat_scalar_write ("Throughput (bps), Priority 3",
255
                     fddi_sink_total_bits_a[2] / op_sim_time ());
256
                  op_stat_scalar_write ("Throughput (bps), Priority 4",
257
                     fddi_sink_total_bits_a[3] / op_sim_time ());
258
                  op stat scalar write ("Throughput (bps), Priority 5",
259
                     fddi sink_total_bits_a[4] / op_sim_time ());
260
                  op_stat_scalar_write ("Throughput (bps), Priority 6".
261
                     fddi_sink_total_bits_a[5] / op_sim_time ());
262
263
                  op_stat_scalar_write ("Throughput (bps), Priority 7",
                     fddi_sink_total_bits_a[6] / op_sim_time ());
264
                 op stat_scalar_write ("Throughput (bps), Priority 8",
265
                     fddi_sink_total_bits_a[7] / op_sim_time ());
266
                 op_stat_scalar_write ("Throughput (bps), Asynchronous".
267
268
                     (fddi_sink_total_bits - fddi_sink_total_bits_a[8]) /
                     op_sim_time ()):
269
     /* (fddi_sink_total_bits_a[0] + fddi_sink_total_bits_a[1] +
                                                                     */
270
     /* fddi_sink_total_bits_a[2] + fddi_sink_total_bits_a[3] +
                                                                    */
271
272
     /* fddi_sink_total_bits_a[4] + fddi_sink_total_bits_a[5] +
                                                                    */
     /* fddi_sink_total_bits_a[6] + fddi_sink_total_bits_a[7]) /
273
274
     /* op_sim_time ()): */
275
              op_stat_scalar_write ("Throughput (bps), Synchronous",
                  fddi_sink_total_bits_a[8] / op_sim_time ());
276
              op_stat_scalar_write ("Throughput (bps), Total",
277
                  fddi_sink_total_bits / op_sim_time ());
278
              op_stat_scalar_write ("Peak End-to-End Delay (sec.), Priority
279
                         fddi_sink_peak_delay_a[0]);
280
              op_stat_scalar_write ("Peak End-to-End Delay (sec.), Priority
281
282
                  2".
                         fddi_sink_peak_delay_a[l]);
              op_stat_scalar_write ("Peak End-to-End Delay (sec.), Priority
283
284
                         fddi_sink_peak_delay_a[2]);
              op_stat_scalar_write ("Peak End-to-End Delay (sec.), Priority
285
286
                         fddi_sink_peak_delay_a[3]);
287
              op_stat_scalar_write ("Peak End-to-End Delay (sec.), Priority
288
                         fddi_sink_peak_delay_a[4]);
289
              op_stat_scalar_write ("Peak End-to-End Delay (sec.), Priority
290
                         fddi_sink_peak_delay_a[5]);
                  6".
              op_stat_scalar_write ("Peak End-to-End Delay (sec.), Priority
291
292
                  7".
                         fddi_sink_peak_delay_a[6]);
293
              op_stat_scalar_write ("Peak End-to-End Delay (sec.), Priority
294
                         fddi_sink_peak_delay_a[7]);
```

```
op_stat_scalar_write ("Peak End-to-End Delay (sec.),
295
                  Synchronous*, fddi_sink_peak_delay_a[8]);
296
               op_stat_scalar_write ("Peak End-to-End Delay (sec.), Overall",
297
298
                  fddi_sink_peak_delay);
299
     /* Write the TTRT value for ring 0. This preserves */
     /* the old behavior for single-ring simulations. */
300
                  op_stat_scalar_write ("TTRT (sec.) - Ring O",fddi_t_opr
301
302
     [0]
     /* 12JAN94: obtain offered load information from the Environment */
303
304
     /* file: this will be used to provide abscissa information that */
     /* can be plotted in the Analysis Editor (see "fddi_sink" STATS */
305
     /* state. To the user: it's your job to keep these current in
306
     /* the Environment File. -Nix */
307
               op_ima_sim_attr_get (OPC_IMA_DOUBLE, "total_offered_load".
308
309
                  &Offered_Load);
               op_ima_sim_attr_get (OPC_IMA_DOUBLE, "asynch_offered_load".
310
311
                  &Asynch_Offered_Load);
312
313
               /* 12JAN94: write the total offered load for this run */
              op_stat_scalar_write ("Total Offered Load
314
315
     (Mbps)",Offered_Load);
              op_stat_scalar_write ("Asynchronous Offered Load (Mbps)".
316
                 Asynch_Offered_Load);
317
318
319
               }
320
     /** blocking after enter executives of unforced state. **/
321
           FSM_EXIT (3,fddi_sink_mult)
     /** state (STATS) exit executives **/
322
323
           FSM_STATE_EXIT_UNFORCED (1, statel_exit_exec. "STATS")
324
325
326
     /** state (STATS) transition processing **/
327
           FSM_TRANSIT_MISSING ("STATS")
328
329
     /** state (INIT) enter executives **/
330
           FSM_STATE_ENTER_FORCED (2, state2_enter_exec, "INIT")
331
332
     /* get the gshandles of the global statistic to be obtained */
333
     /* 20JAN94: set array format */
334
335
              thru_gshandle_a[0] = op_stat_global_reg ("pri 1 throughput
336
                 (bps)");
```

```
thru_gshandle_a[1] = op_stat_global_reg ("pri 2 throughput
337
338
                  (bps)"):
               thru_gshandle_a[2] = op_stat_global_reg ("pri 3 throughput
339
                  (bps)"):
340
               thru_gshandle_a[3] = op_stat_global_reg ("pri 4 throughput
341
342
                  (bps)*);
               thru_gshandle_a[4] = op_stat_global_reg ("pri 5 throughput
343
344
                  (bps)*):
               thru_gshandle_a[5] = op_stat_global_reg (*pri 6 throughput
345
346
                  (bps)");
               thru_gshandle_a[6] = op_stat_global_reg ("pri 7 throughput
347
348
                  (bps)*):
               thru_qshandle_a[7] = op_stat_global_reg (*pri 8 throughput
349
350
                  (bps)*):
               thru_qshandle_a[8] = op_stat_global_reg ("synch throughput
351
352
                  (bps)*);
353
               thru_gshandle_a[9] = op_stat_global_reg ("async throughput
354
                  (bps)"):
               thru_gshandle = op_stat_global_reg ("total throughput (bps)");
355
               m_delay_gshandle_a[0] = op_stat_global_reg ("pri 1 mean delay
356
357
                  (sec.)");
358
               m_delay_gshandle_a[1] = op_stat_global_reg ("pri 2 mean delay
359
                  (sec.)");
               m_delay_gshandle_a[2] = op_stat_global_reg ("pri 3 mean delay
360
                  (sec.)");
361
362
               m_delay_gshandle_a[3] = op_stat_global_reg ("pri 4 mean delay
363
                  (sec.)");
               m_{delay_gshandle_a[4]} = op_stat_global_reg ("pri 5 mean delay")
364
365
                  (sec.)");
366
               m_delay_gshandle_a[5] = op_stat_global_reg ("pri 6 mean delay
367
                  (sec.)"):
              m_delay_gshandle_a[6] = op_stat_global_reg (*pri 7 mean delay
368
369
                  (sec.)"):
370
               m_delay_gshandle_a[7] = op_stat_global_reg (*pri 8 mean delay
371
                  (sec.)");
372
               m_delay_gshandle_a[8] = op_stat_global_reg ("synch mean delay
373
                  (sec.)");
374
               m_delay_gshandle_a[9] = op_stat_global_reg ("async mean delay
375
                  (sec.)");
376
              m_delay_gshandle = op_stat_global_reg ("total mean delay
377
                  (sec.)"):
               ete_delay_gshandle_a[0] = op_stat_global_reg ("pri 1 end-to-end
378
379
                  delay (sec.)*);
380
               ete_delay_gshandle_a[1] = op_stat_global_reg (*pri 2 end-to-end
381
                  delay (sec.)");
382
               ete_delay_gshandle_a[2] = op_stat_global_reg ("pri 3 end-to-end
383
                  delay (sec.)");
               ete_delay_gshandle_a[3] = op_stat_global_reg ("pri 4 end-to-end
384
385
                  delay (sec.)*);
```

```
386
               ete_delay_gshandle_a[4] = op_stat_global_reg (*pri 5 end-to-end
387
                  delay (sec.)"):
388
               ete_delay_gshandle_a[5] = op_stat_global_reg ("pri 6 end-to-end
389
                  delay (sec.)*);
               ete_delay_gshandle_a[6] = op_stat_global_reg ("pri 7 end-to-end
390
391
                  delay (sec.)");
               ete_delay_gshandle_a[7] = op_stat_global_reg (*pri 8 end-to-end
392
                  delay (sec.)");
393
               ete_delay_gshandle_a[8] = op_stat_global_reg ("synch end-to-end
394
395
                  delay (sec.)*);
                                   = op_stat_global_reg ("total end-to-end
396
               ete_delay_gshandle
397
                  delay (sec.)");
398
               }
399
     /** state (INIT) exit executives **/
            FSM_STATE_EXIT_FORCED (2, state2_exit_exec, "INIT")
400
401
402
403
     /** state (INIT) transition processing **/
404
            FSM_INIT_COND (END_OF_SIM)
            FSM_DFLT_COND
405
            FSM_TEST_LOGIC ("INIT")
406
407
           FSM_TRANSIT_SWITCH
408
409
               FSM_CASE_TRANSIT (0, 1, statel_enter_exec, ;)
               FSM_CASE_TRANSIT (1, 0, state0_enter_exec, ;)
410
411
412
413
           }
414
        FSM_EXIT (2,fddi_sink_mult)
415
416
417
     fddi_sink_mult_svar (prs_ptr.var_name,var_p_ptr)
418
        fddi_sink_mult_state
                                 *prs_ptr;
419
        char
                     *var_name, **var_p_ptr;
420
        {
421
        FIN (fddi_sink_mult_svar (prs_ptr))
422
        *var_p_ptr = VOS_NIL;
423
        if (Vos_String_Equal ("thru_gshandle" , var_name))
424
           *var_p_ptr = (char *) (&prs_ptr->sv_thru_gshandle);
425
        if (Vos_String_Equal ("m_delay_gshandle" , var_name))
426
           *var_p_ptr = (char *) (&prs_ptr->sv_m_delay_gshandle);
427
        if (Vos_String_Equal ("ete_delay_gshandle" , var_name))
428
           *var_p_ptr = (char *) (&prs_ptr->sv_ete_delay_gshandle);
```

```
429
        if (Vos_String_Equal ("thru_gshandle_a" , var_name))
            *var_p_ptr = (char *) (prs_ptr->sv_thru_gshandle_a);
430
        if (Vos_String_Equal ("m_delay_gshandle_a" , var_name))
431
432
            *var_p_ptr = (char *) (prs_ptr->sv_m_delay_gshandle_a);
        if (Vos_String_Equal ("ete_delay_gshandle_a" , var_name))
433
            *var_p_ptr = (char *) (prs_ptr->sv_ete_delay_gshandle_a);
434
435
        FOUT:
436
        }
437
     void
438
     fddi_sink_mult_diag ()
439
440
                        delay, creat_time:
        double
441
        Packet*
                     pkptr;
                         src_addr, my_addr;
442
        int
443
        Ici*
                     from_mac_ici_ptr:
444
        double
                        fddi_sink_ttrt;
        FIN (fddi_sink_mult_diag ())
445
446
        FOUT:
447
        }
448
     void
     fddi_sink_mult_terminate ()
449
450
451
                         delay, creat_time;
        double
452
        Packet*
                     pkptr:
453
        int
                         src_addr, my_addr;
454
        Ici*
                     from_mac_ici_ptr;
455
        double
                        fddi_sink_ttrt;
456
        FIN (fddi_sink_mult_terminate ())
457
        FOUT:
458
        }
     Compcode
459
460
     fddi_sink_mult_init (pr_state_pptr)
461
        fddi_sink_mult_state
                                     **pr_state_pptr:
462
        static VosT_Cm_Obtype
463
                                  obtype = OPC_NIL;
        FIN (fddi_sink_mult_init (pr_state_pptr))
464
465
        if (obtype == OPC_NIL)
466
            if (Vos_Catmem_Register ("proc state vars (fddi_sink_mult)",
467
               sizeof (fddi_sink_mult_state), Vos_Nop, &obtype) ==
468
469
               VOSC_FAILURE)
               FRET (OPC_COMPCODE_FAILURE)
470
471
            }
```

# APPENDIX H. ENVIRONMENT FILE FOR 50-STATION MULTICAST CAPABLE FDDI LAN

```
# fddi50mult.ef
# sample simulation configuration file for fddi example model
# 50 station network with multiple addressing capability
#*** Attributes related to loading used by "fddi_gen" ***
# station addresses
*.*.f0.mac.station address: 0
*.*.fl.mac.station address: 1
*.*.f2.mac.station_address: 2
*.*.f3.mac.station_address: 3
*.*.f4.mac.station_address: 4
*.*.f5.mac.station_address: 5
*.*.f6.mac.station_address: 6
*.*.f7.mac.station_address: 7
*.*.f8.mac.station_address: 8
*.*.f9.mac.station_address: 9
*.*.fl0.mac.station_address: 10
*.*.fll.mac.station_address: ll
*.*.fl2.mac.station_address: 12
*.*.fl3.mac.station_address: 13
*.*.fl4.mac.station_address: 14
*.*.f15.mac.station_address: 15
*.*.fl6.mac.station_address: 16
*.*.fl7.mac.station_address: 17
*.*.fl8.mac.station_address: 18
*.*.fl9.mac.station_address: 19
*.*.f20.mac.station_address: 20
*.*.f21.mac.station_address: 21
*.*.f22.mac.station_address: 22
*.*.f23.mac.station_address: 23
*.*.f24.mac.station_address: 24
*.*.f25.mac.station_address: 25
*.*.f26.mac.station_address: 26
*.*.f27.mac.station_address: 27
*.*.f28.mac.station_address: 28
*.*.f29.mac.station_address: 29
*.*.f30.mac.station_address: 30
*.*.f31.mac.station_address: 31
*.*.f32.mac.station_address: 32
*.*.f33.mac.station_address: 33
```

```
*.*.f34.mac.station_address: 34
*.*.f35.mac.station_address: 35
*.*.f36.mac.station_address: 36
*.*.f37.mac.station_address: 37
*.*.f38.mac.station_address: 38
*.*.f39.mac.station_address: 39
*.*.f40.mac.station_address: 40
*.*.f41.mac.station_address: 41
*.*.f42.mac.station_address: 42
*.*.f43.mac.station_address: 43
*.*.f44.mac.station_address: 44
*.*.f45.mac.station_address: 45
*.*.f46.mac.station_address: 46
*.*.f47.mac.station_address: 47
*.*.f48.mac.station_address: 48
*.*.f49.mac.station_address: 49
*.*.*.mac.ring_id :0
# Range number of stations that may recieve this packet if more
# than one is designated (model defaults are both 1)
# Note that the code does not allow the originating station to
# address a packet to itself, so max_num_addees is less than
# the number of stations.
"*.*.llc_src.min num addees" :
                                    1
"*.*.11c_src.max num addees" :
# destination addresses for random message generation
"*.*.llc_src.low dest address" :
"*.*.llc_src.high dest address" :
# "*.f0.llc_src.low dest address" : 0
# "*.f0.llc_src.high dest address" : 49
# range of priority values that can be assigned to packets; FDDI
# standards allow for 8 priorities of asynchronous traffic. MIL3's
# original model is modified to allow each station to generate
# multiple priorities, within a specified range. (Note that while
# research literature refers to asynchronous priorities ranging
# from 1 to 8, the corresponding numbering here is 0 to 7, in
# keeping with the C language array element numbering convention.)
"*.*.llc_src.high pkt priority" : 7
"*.*.llc_src.low pkt priority" :
\# arrival rate(frames/sec), and message size (bits) for random
# message generation at each station on the ring.
                                750
"*.*.*.arrival rate" :
"*.*.*.mean pk length" :
                                1000
# These are the synchronus transmitters
```

```
"*.f0.*.arrival rate"
                             6000
"*.f0.*.mean pk length"
                             512
"*.f5.*.arrival rate"
                             6000
"*.f5.*.mean pk length"
                             512
"*.f10.*.arrival rate"
                             6000
"*.fl0.*.mean pk length" :
                             512
"*.f15.*.arrival rate"
                             6000
"*.f15.*.mean pk length"
                             512
"*.f20.*.arrival rate"
                             60000
"*.f20.*,mean pk length" :
                             512
"*.f25.*.arrival rate"
                             6000
"*.f25.*.mean pk length"
                            512
"*.f30.*.arrival rate"
                             6000
"*.f30.*.mean pk length"
                             512
"*.f35.*.arrival rate"
                             6000
"*.f35.*.mean pk length"
                             512
"*.f40.*.arrival rate"
                            6000
"*.f40.*.mean pk length"
                             512
"*.f45.*.arrival rate" : 60000
"*.f45.*.mean pk length": 512
# 12DEC93: total offered load is the sum of all stations'
# loads (Mbps). Compute this by hand; this value is used in
\# the sink process model for generating scalar plots where
# offered load is the abscissa.
total_offered_load :
                                         60.72
asynch_offered_load :
                                         30.00
# set the proportion of asynchronous traffic
# a value of 1.0 indicates all asynchronous traffic
"*.*.*.async_mix" :
"*.f0.*.async_mix" :
                        0.0
"*.f5.*.async_mix" :
                        0.0
"*.f10.*.async_mix" :
                        0.0
"*.f15.*.async_mix" :
                        0.0
"*.f20.*.async_mix" :
                        0.0
"*.f25.*.async_mix" :
                        0.0
"*.f30.*.async_mix" :
                        0.0
"*.f35.*.async_mix" :
                        0.0
"*.f40.*.async_mix" :
                        0.0
"*.f45.*.async_mix" :
                        0.0
#*** Ring configuration attributes used by "fddi_mac" ***
# allocate percentage of synchronous bandwidth to each station
# this value should not exceed 1 for all stations combined: OPNET
# does not
\# enforce this; OlFEB94: this must be less than 1; see equation below
"*.*.mac.sync bandwidth" :
```

```
"*.f0.mac.sync bandwidth" : .09358
"*.f5.mac.sync bandwidth" :
                                .09358
"*.fl0.mac.sync bandwidth": .09358
"*.f15.mac.sync bandwidth" : .09358
"*.f20.mac.sync bandwidth" : .09358
"*.f25.mac.sync bandwidth" : .09358
"*.f30.mac.sync bandwidth": .09358
"*.f35.mac.sync bandwidth" : .09358
"*.f40.mac.sync bandwidth" : .09358
"*.f45.mac.sync bandwidth" : .09358
# Target Token Rotation Time (one half of maximum
# synchronous response time)
                                .0107
"*.*.mac.T_Req" :
# Index of the station which initially launches the token
"spawn station":
# Delay incurred by packets as they traverse a station's
# ring interface (see Powers, p. 351 for a discussion
# of this (Powers gives lusec, but 60.0e-08 agrees with
# Dykeman & Bux)
                                60.0e-08
station_latency:
# Propagation Delay separating stations on the ring.
prop_delay:
                             5.085e-06
# Simulation related attributes
# Token Acceleration Mechanism enabling flag.
# It is reccomended that this mechanism be enabled for most
# situations
accelerate_token:
                                          1
seed:
                                         10
# Run control attributes
duration:
                                          . 5
verbose_sim:
                                        TRUE
upd_int:
                                           . 1
os_file:
                                   fddi50mult
ov_file:
                                   fddi50mult
# Opnet Debugger (odb) enabling attribute
# debug:
                                  TRUE
```

### APPENDIX I. CONVENTIONS

One of the purposes of this report is that it will be used both as a teaching tool and a springboard for future researchers and assessors of fiber optic network simulations implementing OPNET. Throughout the writing of this report, the author has kept these goals in sight and the resulting narrative contains technical stylistic conventions in keeping with the projected use of this material in a teaching, reference, and research environment These conventions, implemented in the narrative portion of this report only, are briefly described here.

All excerpted programming code fragments are isolated on their own lines within the text and highlighted by a standard san-serif font. Variable names, function names, and names of programming objects referred to within the text of the report are also highlighted in this manner, with a standard san-serif font. Messages from the computer or responses to be made to computer queries are set off in double quotes and a "bold standard san-serif font." Single keystrokes are highlighted in capitalized italics, (e.g. <CTRL+S>), while parameters are also set off in the same manner (e.g. <number of nodes>).

### APPENDIX J. GLOSSARY

BONeS<sup>®</sup> Block Oriented Network Simulator

CDL Common Data Link

DSPO Defense Support Project Office

Environment file A command file containing descriptors and values

utilized by a system to define the operating

parameters. Sometimes this file is referred to as the

"configuration file."

FDDI LAN Fiber Distributed Data Interface Local Area Network

MAC Medium Access Control

OPNET<sup>®</sup> Optimized Network Engineering Tool

THT Token Holding Timer

TRT Token Rotation Timer

TTRT Target Token Rotation Time

#### LIST OF REFERENCES

Defense Support Project Office, CDL System Description Document for Common Data link (CDL), 1993.

Dykeman, D., and Bux, W., "Analysis and Tuning of the FDDI Media Access Control Protocol," *IEEE Journal on Selected Areas in Communications*, v. 6, no. 6, pp. 997-1010, July 1988.

Jain, Raj, "Performance Analysis of FDDI Token Ring Networks: Effect of Parameters and Guidelines for Setting TTRT," *IEEE LTS*, v. 2, no. 2, pp. 16-22, May 1991.

MIL 3, Inc., OPNET Modeler, (user's manual in 11 volumes), 3400 International Drive NW, Washington D.C. 20008, 1993.

Powers, John P., An Introduction to Fiber Optic Systems, Richard D. Irwin, Inc., and Aksen Associates, Inc., 1993.

Shukla, Shridhar B., "Interfacing Remote Platforms Using the Common Data Link - Requirements and Structural Alternatives," report submitted to CDL Project Manager, Defense Support Project Office, December 1993.

Schenone, Aldo B., Modeling and Simulation of a Fiber Distributed Data Interface Local Area Network, Master's Thesis, Naval Postgraduate School, Monterey, California, September 1993.

Stallings, William, Data and Computer Communications, Third Edition, Macmillan Publishing Company, 1991

Stallings, William, Local and Metropolitan Area Networks, Fourth Edition, Macmillan Publishing Company, 1993.

Tari, F., and others, "Analyzing FDDI-Based Networks Using BONeS," SPIE, v. 1577, pp. 54-65, 1991.

# **INITIAL DISTRIBUTION LIST**

		No Copies
1.	Defense Technical Information Center	2
	Cameron Station	_
	Alexandria, VA 22304-6145	
2.	Library, Code 52	2
	Naval Postgraduate School	
	Monterey, CA 93943-5101	
3.	Chairman, Code EC	1
	Department of Electrical and Computer Engineering	
	Naval Postgraduate School	
	Monterey, CA 93943-5121	
<b>4</b> .	Professor Shridhar Shukla, Code EC/Sh	2
	Department of Electrical and Computer Engineering	
	Naval Postgraduate School	
	Monterey, CA 93943-5121	
<b>5</b> .	Professor Gilbert Lundy, Code CS/Ln	2
	Department of Computer Science	
	Naval Postgraduate School	
	Monterey, CA 93943-5118	
<b>6</b> .	Professor Paul Moose, Code EC/Me	1
	Department of Electrical and Computer Engineering	
	Naval Postgraduate School	
	Monterey, CA 93943-5121	
7.	Mark Russon, UNISYS	1
	Mail Station F2-G14	_
	640 North 2200 West	
	Salt Lake City ITT 94116-2099	

8.	CDL Program Manager	1
	Defense Support Project Office	
	Washington D.C. 20330-1000	
9.	LT Ernest E. Nix, Jr., USN	1
	502 Paris View Dr.	
	Travelers Rest SC 29690	